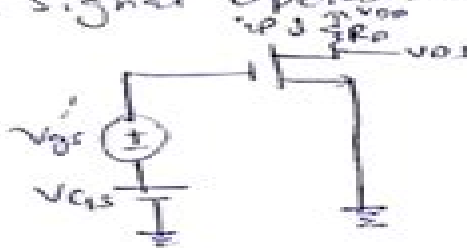




ausst  
Vorsatz



Set  
 $V_{DS} < V_E$   
 $V_{DS} > V_{OV}$

$\xrightarrow{\text{Finite Set}} \text{Vas} \rightarrow \text{Finite}$   
 $\text{Vas} \rightarrow \text{Finite}$   
 - cutoff  $\rightarrow$  cutoff  $\rightarrow$  open switch  
 $\rightarrow$  C  $\rightarrow$  E mode  $\rightarrow$  closed switch  
 - we keep  $R \geq 0$   
 $\text{V} \geq 0$   
 $\text{V} \geq 0$   
 $\text{V} \geq 0$

① DC Bias Point

DC bias current find by letting  $V_{gs} = 0$

$$\frac{2}{3} D_2 \frac{1}{2} K n^2 \frac{\omega}{2} (V_{G2} - V_L)^2$$

$$T_{O_2} = \frac{1}{2} K_{O_2} V_{O_2}^2$$

$$V_{DS} = V_{DD} - I_D R_D$$

For saturation  $V_{DS} > V_{DS,sat}$

since  $V_{DS}$  is superimposed on  $V_{GS}$   
 $V_{GS} \gg V_{DS}$

② Signal Contact in Ocean Terminal

Consider input signal  $V_{gs}$  applied.

$$V_{C,S} = V_{C,S1} + V_{g1}$$

$$e_D = \frac{1}{2} k_B (V_{Gs} + V_{gs} - V_t)^2$$

$$= \frac{1}{2} k_{en} (V_{c,s} - V_E)^2 + k_{en} (V_{c,s} - V_E) V_{js}$$

$$+ \frac{1}{2} k n v g_1^2$$

de bias  
currit  
To

Current component  
Vgs input

Current  $\frac{V_{gs}}{V_{gs} - V_{th}}$   $\rightarrow$  undervoltage

# Electronics Ii

**L Cohen**



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**Catalogue for the Academic Year** Naval Postgraduate School (U.S.), 1956

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