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Electronic Packaging Materials Science VII

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Harsh Environment Electronics Ahmed Sharif,2019-03-19 Provides in depth knowledge on novel materials that make electronics work under high temperature and high pressure conditions This book reviews the state of the art in research and development of lead free interconnect materials for electronic packaging technology It identifies the technical barriers to the development and manufacture of high temperature interconnect materials to investigate into the complexities introduced by harsh conditions It teaches the techniques adopted and the possible alternatives of interconnect materials to cope with the impacts of extreme temperatures for implementing at industrial scale The book also examines the application of nanomaterials current trends within the topic area and the potential environmental impacts of material usage Written by world renowned experts from academia and industry Harsh Environment Electronics Interconnect Materials and Performance Assessment covers interconnect materials based on silver gold and zinc alloys as well as advanced approaches utilizing polymers and nanomaterials in the first section

The second part is devoted to the performance assessment of the different interconnect materials and their respective environmental impact. Takes a scientific approach to analyzing and addressing the issues related to interconnect materials involved in high temperature electronics. Reviews all relevant materials used in interconnect technology as well as alternative approaches otherwise neglected in other literature. Highlights emergent research and theoretical concepts in the implementation of different materials in soldering and die attach applications. Covers wide bandgap semiconductor device technologies for high temperature and harsh environment applications: transient liquid phase bonding, glass frit based die attach solution for harsh environment and more. A pivotal reference for professionals, engineers, students and researchers. Harsh Environment Electronics: Interconnect Materials and Performance Assessment is aimed at materials scientists, electrical engineers and semiconductor physicists and treats this specialized topic with breadth and depth.

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Extreme Environment Electronics John D. Cressler, H. Alan Mantooth, 2017-12-19. Unfriendly to conventional electronic devices, circuits and systems, extreme environments represent a serious challenge to designers and mission architects. The first truly comprehensive guide to this specialized field, *Extreme Environment Electronics* explains the essential aspects of designing and using devices, circuits and electronic systems intended to operate in extreme environments including across wide temperature ranges and in radiation intense scenarios.

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