

# FPGA design of a camera control system for road traffic monitoring

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**Abstract**—Video surveillance and monitoring more than a decade now remains a big challenge. Today is becoming more and more vital due to rapid development of hardware equipments as well as the software methods that support intelligent monitoring and analytics. This paper proposes the architecture of a FPGA design of a camera controller for surveillance that could be applied for road traffic monitoring and image data management purposes. The proposed system could deal with real-time monitoring of persistent and transient objects within the specific environment of a motorway or crossroad, taking input from a camera. Functional models were synthesized in VHDL. The design and simulations analysis were carried out within Quartus II Altera software package while further analysis was also performed with ModelSim. The functional performance and operation of the proposed design was evaluated based on such simulations and analysis.

**Keywords**—*monitoring; camera controller; FPGA design; simulations*

## I. INTRODUCTION

Video-surveillance systems that use analog CCTV cameras and interfaces as the basis are not easily expandable, and have low video resolution with little or no signal processing. However, next generation of video surveillance systems with digital LAN cameras, that support complex image processing, and video-over-IP routing are more intelligent and allow further scalability and flexibility [6], [7], [8]. In this direction, programmable modules, with or without embedded DSP blocks, with memory blocks, several interfaces, and other units, fulfill the new contemporary system requirements [5], [12].

Today several systems are designed on the same chip, based on a Field Programmable Gate Array (FPGA), for intelligent and fast data processing and miniaturization of the system. Particularly, in modern surveillance systems, FPGA-based intelligent controllers play an important role in improving the performance of the camera control system [2], [3], [4], [16], [19].

The purpose of this paper is to present first results of an ongoing research project on the implementation of a smart camera control system for traffic surveillance based on a

FPGA. The problem of providing automated vision surveillance on an efficient way that last years has been intensively examined [10], [11], [13]. Today there is a rapid development of such systems towards intelligent control units [1], [9], [15] and several algorithms have been proposed towards this direction [14], [17].

In this research, the system proposed captures a video stream, computes traffic information and performs on chip video analytics and finally transfers the video stream and the traffic information to a host PC in order to be generated further alarm signals and events to be triggered according to various traffic situations. The objective of the paper is to describe the design of an intelligent surveillance system within an academic environment for traffic monitoring purposes. The proposed architecture incorporates a camera controller that obtains traffic images and performs analytics. It performs contour identification, axis specification, axis shift, percent zoom, smooth, etc., as well as facilities for mathematical and statistical analysis of the numerical data (pedestrians, cars, etc.), such as: densities, deviations, aggregates, etc.

In section II is described the design methodology followed and the goals aimed to be achieved. In section III the system architecture is detailed and section IV discusses the simulations carried towards the evaluation of system's functionality. Finally, section V presents a brief discussion and conclusions on the proposed system.

## II. DESIGN METHODOLOGY AND GOALS

### A. Design Approach

The overall design of the IP surveillance camera controller is to be implemented as a complete system on a chip using a FPGA device. An FPGA-based architecture is quite flexible and customizable, to such extend that easily allows a system architecture to be enhanced and modified according to various system specifications and requirements. Thus FPGA architectures are easily reconfigurable.

In addition, in our case study of remote traffic monitoring would be possible to add custom video processing software in real time (video analytics) and system upgrades (i.e. a new

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**IEEE International Conference on Intelligent Transportation Systems Proceedings**, 2003 3rd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing Anandakumar Haldorai, Arulmurugan Ramu, Sudha Mohanram, Joan Lu, 2022-01-01

This book features the proceedings of The EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing BDCC 2020 which took place 18 19 December 2020 The papers feature detail on cognitive computing and its self learning systems that use data mining pattern recognition and natural language processing NLP to mirror the way the human brain works This international conference focuses on technologies from knowledge representation techniques and natural language processing algorithms to dynamic learning approaches Topics covered include Data Science for Cognitive Analysis Real Time Ubiquitous Data Science Platform for Privacy Preserving Data Science and Internet Based Cognitive Platform

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