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Efficient C

**Andrew Sloss, Dominic Symes, Chris
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Efficient C:

Efficient C/C++ Programming Steve Heller, 2014-05-10 Efficient C C Programming describes a practical real world approach to efficient C C programming Topics covered range from how to save storage using a restricted character set and how to speed up access to records by employing hash coding and caching A selective mailing list system is used to illustrate rapid access to and rearrangement of information selected by criteria specified at runtime Comprised of eight chapters this book begins by discussing factors to consider when deciding whether a program needs optimization In the next chapter a supermarket price lookup system is used to illustrate how to save storage by using a restricted character set and how to speed up access to records with the aid of hash coding and caching Attention is paid to rapid retrieval of prices A selective mailing list system is then used to illustrate rapid access to and rearrangement of information selected by criteria specified at runtime The book also considers the Huffman coding and arithmetic coding methods of data compression a token threaded interpreter whose code can run faster than equivalent compiled C code due to its greater code density a customer database program with variable length records and index and key access to variable length records The final chapter summarizes the characteristics of the algorithms encountered in previous chapters as well as the future of the art of optimization This monograph will be a useful resource for practicing computer programmers and those who intend to be working programmers

Efficient C++ Dov Bulka, David Mayhew, 2000 Far too many programmers and software designers consider efficient C to be an oxymoron They regard C as inherently slow and inappropriate for performance critical applications Consequently C has had little success penetrating domains such as networking operating system kernels device drivers and others Efficient C explodes that myth Written by two authors with first hand experience wringing the last ounce of performance from commercial C applications this book demonstrates the potential of C to produce highly efficient programs The book reveals practical everyday object oriented design principles and C coding techniques that can yield large performance improvements It points out common pitfalls in both design and code that generate hidden operating costs This book focuses on combining C s power and flexibility with high performance and scalability resulting in the best of both worlds Specific topics include temporary objects memory management templates inheritance virtual functions inlining reference counting STL and much more With this book you will have a valuable compendium of the best performance techniques at your fingertips

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ARM System Developer's Guide Andrew Sloss, Dominic Symes, Chris Wright, 2004-05-10 Over the last ten years the ARM architecture has become one of the most pervasive architectures in the

world with more than 2 billion ARM based processors embedded in products ranging from cell phones to automotive braking systems A world wide community of ARM developers in semiconductor and product design companies includes software developers system designers and hardware engineers To date no book has directly addressed their need to develop the system and software for an ARM based system This text fills that gap This book provides a comprehensive description of the operation of the ARM core from a developer s perspective with a clear emphasis on software It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software The book covers both the ARM and Thumb instruction sets covers Intel s XScale Processors outlines distinctions among the versions of the ARM architecture demonstrates how to implement DSP algorithms explains exception and interrupt handling describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques A final chapter looks forward to the future of the ARM architecture considering ARMv6 the latest change to the instruction set which has been designed to improve the DSP and media processing capabilities of the architecture No other book describes the ARM core from a system and software perspective Author team combines extensive ARM software engineering experience with an in depth knowledge of ARM developer needs Practical executable code is fully explained in the book and available on the publisher s Website Includes a simple embedded operating system

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