

Overview of Floating-Gate Devices



Information Storage

Floating-Gate Transistor

Modifying Floating-Gate Charge

- UV photo-injection
- Electron tunneling
- Hot-electron injection



Floating Gate Devices

**Shih-Chii Liu, Tobi Delbruck, Giacomo
Indiveri, Adrian Whatley, Rodney
Douglas**

Floating Gate Devices:

Floating Gate Devices: Operation and Compact Modeling Paolo Pavan, Luca Larcher, Andrea Marmiroli, 2007-05-08 Floating Gate Devices Operation and Compact Modeling focuses on standard operations and compact modeling of memory devices based on Floating Gate architecture Floating Gate devices are the building blocks of Flash EPROM EEPROM memories Flash memories which are the most versatile nonvolatile memories are widely used to store code BIOS Communication protocol Identification code and data solid state Hard Disks Flash cards for digital cameras The reader who deals with Floating Gate memory devices at different levels from test structures to complex circuit design will find an essential explanation on device physics and technology and also circuit issues which must be fully understood while developing a new device Device engineers will use this book to find simplified models to design new process steps or new architectures Circuit designers will find the basic theory to understand the use of compact models to validate circuits against process variations and to evaluate the impact of parameter variations on circuit performances Floating Gate Devices Operation and Compact Modeling is meant to be a basic tool for designing the next generation of memory devices based on FG technologies *Floating-gate Devices, Circuits, and Systems* Paul Hasler, Tor Sverre Lande, Institute of Electrical and Electronics Engineers, 2001 **Offset Reduction Using Floating-gate Devices** Farhan Adil, 2003 **Fundamentals of Modern VLSI Devices** Yuan Taur, Tak H. Ning, 2021-12-02 A thoroughly updated third edition of a classic and widely adopted text perfect for practical transistor design and in the classroom Covering a variety of recent developments the internationally renowned authors discuss in detail the basic properties and designs of modern VLSI devices as well as factors affecting performance Containing around 25% new material coverage has been expanded to include high k gate dielectrics metal gate technology strained silicon mobility non GCA Gradual Channel Approximation modelling of MOSFETs short channel FinFETs and symmetric lateral bipolar transistors on SOI Chapters have been reorganized to integrate the appendices into the main text to enable a smoother learning experience and numerous additional end of chapter homework exercises 30% are included to engage students with real world problems and test their understanding A perfect text for senior undergraduate and graduate students taking advanced semiconductor devices courses and for practicing silicon device professionals in the semiconductor industry **Special Issue on Floating-gate Devices, Circuits, and Systems** Paul Hasler, Tor S. Lande, 2001 Non-logic Devices in Logic Processes Yanjun Ma, Edwin Kan, 2017-03-29 This book shows readers how to design semiconductor devices using the most common and lowest cost logic CMOS processes Readers will benefit from the author's extensive industrial experience and the practical approach he describes for designing efficiently semiconductor devices that typically have to be implemented using specialized processes that are expensive time consuming and low yield The author presents an integrated picture of semiconductor device physics and manufacturing techniques as well as numerous practical examples of device designs that are tried and true Silicon Based Unified Memory Devices and

Technology Arup Bhattacharyya, 2017-07-06 The primary focus of this book is on basic device concepts memory cell design and process technology integration The first part provides in depth coverage of conventional nonvolatile memory devices stack structures from device physics historical perspectives and identifies limitations of conventional devices The second part reviews advances made in reducing and or eliminating existing limitations of NVM device parameters from the standpoint of device scalability application extendibility and reliability The final part proposes multiple options of silicon based unified nonvolatile memory cell concepts and stack designs SUMs The book provides Industrial R D personnel with the knowledge to drive the future memory technology with the established silicon FET based establishments of their own It explores application potentials of memory in areas such as robotics avionics health industry space vehicles space sciences bio imaging genetics etc

Physics of Semiconductor Devices Simon M. Sze, Kwok K. Ng, 2006-12-13 The Third Edition of the standard textbook and reference in the field of semiconductor devices This classic book has set the standard for advanced study and reference in the semiconductor device field Now completely updated and reorganized to reflect the tremendous advances in device concepts and performance this Third Edition remains the most detailed and exhaustive single source of information on the most important semiconductor devices It gives readers immediate access to detailed descriptions of the underlying physics and performance characteristics of all major bipolar field effect microwave photonic and sensor devices Designed for graduate textbook adoptions and reference needs this new edition includes A complete update of the latest developments New devices such as three dimensional MOSFETs MODFETs resonant tunneling diodes semiconductor sensors quantum cascade lasers single electron transistors real space transfer devices and more Materials completely reorganized Problem sets at the end of each chapter All figures reproduced at the highest quality Physics of Semiconductor Devices Third Edition offers engineers research scientists faculty and students a practical basis for understanding the most important devices in use today and for evaluating future device performance and limitations A Solutions Manual is available from the editorial department

The Physics of Semiconductor Devices R. K. Sharma, D.S. Rawal, 2019-01-31 This book disseminates the current knowledge of semiconductor physics and its applications across the scientific community It is based on a biennial workshop that provides the participating research groups with a stimulating platform for interaction and collaboration with colleagues from the same scientific community The book discusses the latest developments in the field of III nitrides materials devices compound semiconductors VLSI technology optoelectronics sensors photovoltaics crystal growth epitaxy and characterization graphene and other 2D materials and organic semiconductors

The VLSI Handbook Wai-Kai Chen, 2019-07-17 Over the years the fundamentals of VLSI technology have evolved to include a wide range of topics and a broad range of practices To encompass such a vast amount of knowledge The VLSI Handbook focuses on the key concepts models and equations that enable the electrical engineer to analyze design and predict the behavior of very large scale integrated circuits It provides the most up to date information on IC technology you can find Using frequent examples the

Handbook stresses the fundamental theory behind professional applications Focusing not only on the traditional design methods it contains all relevant sources of information and tools to assist you in performing your job This includes software databases standards seminars conferences and more The VLSI Handbook answers all your needs in one comprehensive volume at a level that will enlighten and refresh the knowledge of experienced engineers and educate the novice This one source reference keeps you current on new techniques and procedures and serves as a review for standard practice It will be your first choice when looking for a solution Memory, Microprocessor, and ASIC Wai-Kai Chen, 2003-03-26 Timing memory power dissipation testing and testability are all crucial elements of VLSI circuit design In this volume culled from the popular VLSI Handbook experts from around the world provide in depth discussions on these and related topics Stacked gate embedded and flash memory all receive detailed treatment including their power cons Electrical Memory Materials and Devices Wen-Chang Chen, 2015-10-16 Information technology is essential to our daily life and the limitations of silicone based memory systems mean a growing amount of research is focussed on finding an inexpensive alternative to meet our needs and allow the continued development of the industry Inorganic silicone based technology is increasingly costly and complex and is physically limited by the problems of scaling down Organic electrical memory devices are comparatively low cost offer flexibility in terms of chemical structure are compatible with flexible substrates and allow easy processing For these reasons polymeric memory nanoscale materials are considered by many to be a potential substitute for conventional semiconductor memory systems This edited book focusses solely on organic memory devices providing a full background and overview of the area before bringing the reader up to date with the current and ongoing research in this area The broad appeal of this book will be applicable to a wide range of researchers and those working in industry in particular those working in materials electrical and chemical engineering *Floating-gate Material Systems for Transistor-type Memory Devices*, 2015

Analog VLSI Shih-Chii Liu, 2002 An introduction to the design of analog VLSI circuits Neuromorphic engineers work to improve the performance of artificial systems through the development of chips and systems that process information collectively using primarily analog circuits This book presents the central concepts required for the creative and successful design of analog VLSI circuits The discussion is weighted toward novel circuits that emulate natural signal processing Unlike most circuits in commercial or industrial applications these circuits operate mainly in the subthreshold or weak inversion region Moreover their functionality is not limited to linear operations but also encompasses many interesting nonlinear operations similar to those occurring in natural systems Topics include device physics linear and nonlinear circuit forms translinear circuits photodetectors floating gate devices noise analysis and process technology *The Physics of Microfabrication* Ivor Brodie, Julius J. Muray, 2013-11-11 The Physical Electronics Department of SRI International formerly Stanford Research Institute has been pioneering the development of devices fabricated to submicron tolerances for well over 20 years In 1961 a landmark paper on electron beam lithography and its associated technologies was published by K R

Shoulderst then at SRI which set the stage for our subsequent efforts in this field He had the foresight to believe that the building of such small devices was actually within the range of human capabilities As a result of this initial momentum our experience in the technologies associated with microfabrication has become remarkably comprehensive despite the relatively small size of our research activity We have frequently been asked to deliver seminars or provide reviews on various aspects of micro fabrication These activities made us aware of the need for a comprehensive overview of the physics of microfabrication We hope that this book will fill that need

Event-Based Neuromorphic Systems Shih-Chii Liu,Tobi Delbruck,Giacomo Indiveri,Adrian Whatley,Rodney Douglas,2015-02-16 Neuromorphic electronic engineering takes its inspiration from the functioning of nervous systems to build more power efficient electronic sensors and processors Event based neuromorphic systems are inspired by the brain s efficient data driven communication design which is key to its quick responses and remarkable capabilities This cross disciplinary text establishes how circuit building blocks are combined in architectures to construct complete systems These include vision and auditory sensors as well as neuronal processing and learning circuits that implement models of nervous systems Techniques for building multi chip scalable systems are considered throughout the book including methods for dealing with transistor mismatch extensive discussions of communication and interfacing and making systems that operate in the real world The book also provides historical context that helps relate the architectures and circuits to each other and that guides readers to the extensive literature Chapters are written by founding experts and have been extensively edited for overall coherence This pioneering text is an indispensable resource for practicing neuromorphic electronic engineers advanced electrical engineering and computer science students and researchers interested in neuromorphic systems Key features Summarises the latest design approaches applications and future challenges in the field of neuromorphic engineering Presents examples of practical applications of neuromorphic design principles Covers address event communication retinas cochleas locomotion learning theory neurons synapses floating gate circuits hardware and software infrastructure algorithms and future challenges

Learning on Silicon G. Cauwenberghs,Magdy Bayoumi,1999-06-30 Learning on Silicon combines models of adaptive information processing in the brain with advances in microelectronics technology and circuit design The premise is to construct integrated systems not only loaded with sufficient computational power to handle demanding signal processing tasks in sensory perception and pattern recognition but also capable of operating autonomously and robustly in unpredictable environments through mechanisms of adaptation and learning This edited volume covers the spectrum of Learning on Silicon in five parts adaptive sensory systems neuromorphic learning learning architectures learning dynamics and learning systems The 18 chapters are documented with examples of fabricated systems experimental results from silicon and integrated applications ranging from adaptive optics to biomedical instrumentation As the first comprehensive treatment on the subject Learning on Silicon serves as a reference for beginners and experienced researchers alike It provides excellent material for an advanced course and a

source of inspiration for continued research towards building intelligent adaptive machines

Semiconductor Memory Devices and Circuits Shimeng Yu, 2022-04-19 This book covers semiconductor memory technologies from device bit cell structures to memory array design with an emphasis on recent industry scaling trends and cutting edge technologies The first part of the book discusses the mainstream semiconductor memory technologies The second part of the book discusses the emerging memory candidates that may have the potential to change the memory hierarchy and surveys new applications of memory technologies for machine deep learning applications This book is intended for graduate students in electrical and computer engineering programs and researchers or industry professionals in semiconductors and microelectronics Explains the design of basic memory bit cells including 6 transistor SRAM 1 transistor 1 capacitor DRAM and floating gate charge trap FLASH transistor Examines the design of the peripheral circuits including the sense amplifier and array level organization for the memory array Examines industry trends of memory technologies such as FinFET based SRAM High Bandwidth Memory HBM 3D NAND Flash and 3D X point array Discusses the prospects and challenges of emerging memory technologies such as PCM RRAM STT MRAM SOT MRAM and FeRAM FeFET Explores the new applications such as in memory computing for AI hardware acceleration

Artificial Neural Nets. Problem Solving Methods José Mira, 2003-05-22 The two volume set LNCS 2686 and LNCS 2687 constitute the refereed proceedings of the 7th International Work Conference on Artificial and Natural Neural Networks IWANN 2003 held in Ma Menorca Spain in June 2003 The 197 revised papers presented were carefully reviewed and selected for inclusion in the book and address the following topics mathematical and computational methods in neural modelling neurophysiological data analysis and modelling structural and functional models of neurons learning and other plasticity phenomena complex systems dynamics cognitive processes and artificial intelligence methodologies for net design bio inspired systems and engineering and applications in a broad variety of fields

Intelligence of Things: Technologies and Applications Nhu-Ngoc Dao, Tran Ngoc Thinh, Ngoc Thanh Nguyen, 2023-10-19 This book aims to provide state of the art knowledge in the field of Intelligence of Things to both academic and industrial readers In particular undergraduate graduate and researchers may find valuable information to drive their future research This book is considered a reference for numerous courses such as Artificial Intelligence Internet of Things Intelligent Systems and Mobile Networks In the industrial area this book provides information on recent studies in applying AI to IoT developments which help to align and shorten R D processes to introduce new classes of intelligent IoT products This book provides a technical reference for interdisciplinary studies which utilize machine learning and IoT as tools in their fields such as constructional management smart agriculture Earth sciences and geo spatial analysis intelligent business and digital transformation in education

Floating Gate Devices Book Review: Unveiling the Power of Words

In a global driven by information and connectivity, the ability of words has be more evident than ever. They have the ability to inspire, provoke, and ignite change. Such could be the essence of the book **Floating Gate Devices**, a literary masterpiece that delves deep into the significance of words and their effect on our lives. Written by a renowned author, this captivating work takes readers on a transformative journey, unraveling the secrets and potential behind every word. In this review, we shall explore the book is key themes, examine its writing style, and analyze its overall effect on readers.

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