
INTERACTION BETWEEN COMPILERS AND COMPUTER ARCHITECTURES

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Eighth Workshop On Interaction Between Compilers And Computer Architecture

Peipei Pang



Eighth Workshop On Interaction Between Compilers And Computer Architecture:

Eighth Workshop on Interaction Between Compilers and Computer Architecture, 2004-01-01 INTERACT 2004 presents new ideas and explores recent developments in compiler techniques and computer architectures that enhance each other's capabilities and performance. Its papers cover various aspects of interaction between compilers and architectures in the design of high performance microprocessors, embedded controllers, multiprocessors and other parallel computer systems.

INTERACT-8 2004, 2004 Interaction Between Compilers and Computer Architectures Gyungho Lee, Pen-Chung Yew, 2013-03-14 Effective compilers allow for a more efficient execution of application programs for a given computer architecture while well conceived architectural features can support more effective compiler optimization techniques. A well thought out strategy of trade offs between compilers and computer architectures is the key to the successful designing of highly efficient and effective computer systems. From embedded micro controllers to large scale multiprocessor systems it is important to understand the interaction between compilers and computer architectures. The goal of the Annual Workshop on Interaction between Compilers and Computer Architectures INTERACT is to promote new ideas and to present recent developments in compiler techniques and computer architectures that enhance each other's capabilities and performance. *Interaction Between Compilers and Computer Architectures* is an updated and revised volume consisting of seven papers originally presented at the Fifth Workshop on Interaction between Compilers and Computer Architectures INTERACT 5 which was held in conjunction with the IEEE HPCA 7 in Monterrey Mexico in 2001. This volume explores recent developments and ideas for better integration of the interaction between compilers and computer architectures in designing modern processors and computer systems. *Interaction Between Compilers and Computer Architectures* is suitable as a secondary text for a graduate level course and as a reference for researchers and practitioners in industry. **INTERACT-7 2003**, 2003 Distributed Computing and Internet Technology Sanjay K. Madria, 2006-12-07 This book constitutes the refereed proceedings of the Third International Conference on Distributed Computing and Internet Technology ICDCIT 2006 held in Bhubaneswar India in December 2006. The 24 revised full papers and 10 revised short papers presented together with 1 keynote address and 1 invited talk cover the main areas: distributed computing, internet technology, system security, data mining and software engineering. **Performance Evaluation and Benchmarking** Lizy Kurian John, Lieven Eeckhout, 2018-10-03 Computer and microprocessor architectures are advancing at an astounding pace. However, increasing demands on performance coupled with a wide variety of specialized operating environments act to slow this pace by complicating the performance evaluation process. Carefully balancing efficiency and accuracy is key to avoid slowdowns and such a balance can be achieved with an in depth understanding of the available evaluation methodologies. *Performance Evaluation and Benchmarking* outlines a variety of evaluation methods and benchmark suites considering their strengths, weaknesses and when each is appropriate to use. Following a general overview of important performance analysis techniques

the book surveys contemporary benchmark suites for specific areas such as Java embedded systems CPUs and Web servers Subsequent chapters explain how to choose appropriate averages for reporting metrics and provide a detailed treatment of statistical methods including a summary of statistics how to apply statistical sampling for simulation how to apply SimPoint and a comprehensive overview of statistical simulation The discussion then turns to benchmark subsetting methodologies and the fundamentals of analytical modeling including queuing models and Petri nets Three chapters devoted to hardware performance counters conclude the book Supplying abundant illustrations examples and case studies Performance Evaluation and Benchmarking offers a firm foundation in evaluation methods along with up to date techniques that are necessary to develop next generation architectures

Languages and Compilers for High Performance Computing

Rudolf Eigenmann,Zhiyuan Li,Samuel P. Midkiff,2005-08-25 The 17th International Workshop on Languages and Compilers for High Performance Computing was hosted by Purdue University in September 2004 on Purdue campus in West Lafayette Indiana USA

Languages and Compilers for Parallel Computing José Nelson Amaral,2008-12 This book constitutes the thoroughly refereed post conference proceedings of the 21th International Workshop on Languages and Compilers for Parallel Computing LCPC 2008 held in Edmonton Canada in July August 2008 The 18 revised full papers and 6 revised short papers presented were carefully reviewed and selected from 35 submissions The papers address all aspects of languages compiler techniques run time environments and compiler related performance evaluation for parallel and high performance computing and comprise also presentations on program analysis that are precursors of high performance in parallel environments

Embedded Systems: Design, Analysis and Verification Gunar Schirner,Marcelo Götz,Achim Rettberg,Mauro C. Zanella,Franz J. Rammig,2013-06-13 This book constitutes the refereed proceedings of the 4th IFIP TC 10 International Embedded Systems Symposium IESS 2013 held in Paderborn Germany in June 2013 The 22 full revised papers presented together with 8 short papers were carefully reviewed and selected from 42 submissions The papers have been organized in the following topical sections design methodologies non functional aspects of embedded systems verification performance analysis real time systems embedded system applications and real time aspects in distributed systems The book also includes a special chapter dedicated to the BMBF funded ARAMIS project on Automotive Railway and Avionics Multicore Systems

INTERACT-7 2003 : Seventh Annual Workshop on Interaction Between Compilers and Computer Architectures

,2003-01-01 Embedded Systems Design with FPGAs Peter Athanas,Dionisios Pnevmatikatos,Nicolas Sklavos,2012-12-05 This book presents the methodologies and for embedded systems design using field programmable gate array FPGA devices for the most modern applications Coverage includes state of the art research from academia and industry on a wide range of topics including applications advanced electronic design automation EDA novel system architectures embedded processors arithmetic and dynamic reconfiguration

Speculative Execution in High Performance Computer Architectures

David Kaeli,Pen-Chung Yew,2005-05-26 Until now there were few textbooks that focused on the dynamic subject of

speculative execution a topic that is crucial to the development of high performance computer architectures Speculative Execution in High Performance Computer Architectures describes many recent advances in speculative execution techniques It covers cutting edge research High Performance Computing - HiPC 2002 Sartaj Sahni,Viktor K. Prasanna,Uday Shukla,2003-07-01 This book constitutes the refereed proceedings of the 9th International Conference on High Performance Computing HiPC 2002 held in Bangalore India in December 2002 The 57 revised full contributed papers and 9 invited papers presented together with various keynote abstracts were carefully reviewed and selected from 145 submissions The papers are organized in topical sections on algorithms architecture systems software networks mobile computing and databases applications scientific computation embedded systems and biocomputing *Proceedings of the Estonian Academy of Sciences, Engineering* ,2005-12 **The Interaction of Compilation Technology and Computer Architecture** David J. Lilja,Peter L. Bird,2012-12-06 In brief summary the following results were presented in this work A linear time approach was developed to find register requirements for any specified CS schedule or filled MRT An algorithm was developed for finding register requirements for any kernel that has a dependence graph that is acyclic and has no data reuse on machines with depth independent instruction templates We presented an efficient method of estimating register requirements as a function of pipeline depth We developed a technique for efficiently finding bounds on register requirements as a function of pipeline depth Presented experimental data to verify these new techniques discussed some interesting design points for register file size on a number of different architectures REFERENCES 1 Robert P Colwell Robert P Nix John J O Donnell David B Papworth and Paul K Rodman A VLIW Architecture for a Trace Scheduling Compiler In Architectural Support for Programming Languages and Operating Systems pages 180 192 1982 2 C Eisenbeis W Jalby and A Lichnewsky Compile Time Optimization of Memory and Register Usage on the Cray 2 In Proceedings of the Second Workshop on Languages and Compilers Urbana l inois August 1989 3 C Eisenbeis William Jalby and Alain Lichnewsky Squeezing More CPU Performance Out of a Cray 2 by Vector Block Scheduling In Proceedings of Supercomputing 88 pages 237 246 1988 4 Michael J Flynn Very High Speed Computing Systems Proceedings of the IEEE 54 1901 1909 December 1966 *Embedded Computing* Joseph A. Fisher,Paolo Faraboschi,2005 Embedded Computing is enthralling in its clarity and exhilarating in its scope If the technology you are working on is associated with VLIWs or embedded computing then clearly it is imperative that you read this book If you are involved in computer system design or programming you must still read this book because it will take you to places where the views are spectacular You don t necessarily have to agree with every point the authors make but you will understand what they are trying to say and they will make you think From the Foreword by Robert Colwell R E Colwell Assoc Inc The fact that there are more embedded computers than general purpose computers and that we are impacted by hundreds of them every day is no longer news What is news is that their increasing performance requirements complexity and capabilities demand a new approach to their design Fisher Faraboschi and Young describe a new age of embedded

computing design in which the processor is central making the approach radically distinct from contemporary practices of embedded systems design. They demonstrate why it is essential to take a computing centric and system design approach to the traditional elements of nonprogrammable components, peripherals, interconnects, and buses. These elements must be unified in a system design with high performance processor architectures, microarchitectures, and compilers, and with the compilation tools, debuggers, and simulators needed for application development. In this landmark text, the authors apply their expertise in highly interdisciplinary hardware, software development, and VLIW processors to illustrate this change in embedded computing. VLIW architectures have long been a popular choice in embedded systems design, and while VLIW is a running theme throughout the book, embedded computing is the core topic. *Embedded Computing* examines both in a book filled with fact and opinion based on the authors' many years of R D experience. Features Complemented by a unique professional quality embedded tool chain on the authors' website <http://www.vliw.org/book>. Combines technical depth with real world experience. Comprehensively explains the differences between general purpose computing systems and embedded systems at the hardware, software, tools, and operating system levels. Uses concrete examples to explain and motivate the trade offs.

Languages and Compilers for Parallel Computing Guang R. Gao, Lori Pollock, John Cavazos, Xiaoming Li, 2010-06-09. This book constitutes the thoroughly refereed post workshop proceedings of the 22nd International Workshop on Languages and Compilers for Parallel Computing LCPC 2009 held in Newark, DE, USA, in October 2009. The 25 revised full papers and 5 revised short papers presented were carefully reviewed and selected from 52 submissions. The papers address all aspects of languages, compiler techniques, run time environments, and compiler related performance evaluation for parallel and high performance computing, and extend the area of interest to new parallel computing accelerators such as IBM Cell processor and Graphic Processing Unit (GPU), presenting the latest work of leading researchers and practitioners in the field.

Computational Science and Its Applications - ICCSA 2006 Marina L. Gavrilova, 2006 [Applied Parallel Computing](#) Bo Kagström, Erik Elmroth, Jack Dongarra, Jerzy Wasniewski, 2007-09-22. This book constitutes the thoroughly refereed post proceedings of the 8th International Workshop on Applied Parallel Computing PARA 2006. It covers partial differential equations, parallel scientific computing, algorithms, linear algebra, simulation environments, algorithms, and applications for blue gene L, scientific computing tools, and applications, parallel search, algorithms, peer to peer computing, mobility, and security, algorithms for single chip multiprocessors.

Encyclopedia of Parallel Computing David Padua, 2011-09-08. Containing over 300 entries in an A-Z format, the *Encyclopedia of Parallel Computing* provides easy, intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing. Topics for this comprehensive reference were selected, written, and peer reviewed by an international pool of distinguished researchers in the field. The *Encyclopedia* is broad in scope, covering machine organization, programming languages, algorithms, and applications. Within each area, concepts, designs, and specific implementations are presented. The

highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance computing distributed computing

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