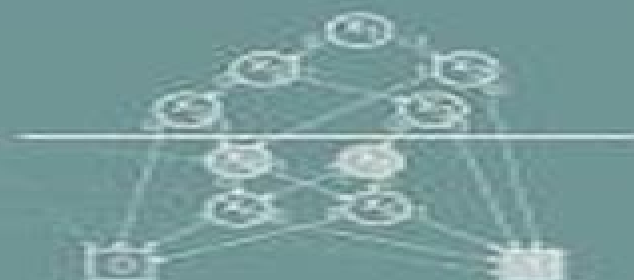


Functional Decomposition with Application to FPGA Synthesis

Christoph Scholl

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Functional Decomposition With Applications To Fpga Synthesis

**Marian Adamski, Alexander
Barkalov, Marek Wegrzyn**



Functional Decomposition With Applications To Fpga Synthesis:

Functional Decomposition with Applications to FPGA Synthesis Christoph Scholl, 2001-10-31 This consistently written book provides a comprehensive presentation of a multitude of results stemming from the author's as well as various researchers' work in the field. It also covers functional decomposition for incompletely specified functions, decomposition for multi-output functions and non-disjoint decomposition. Functional Decomposition with Applications to FPGA Synthesis Christoph Scholl, 2014-01-15

Advanced Techniques in Logic Synthesis, Optimizations and Applications Kanupriya Gulati, 2010-11-25 This book covers recent advances in the field of logic synthesis and design including Boolean Matching, Logic Decomposition, Boolean satisfiability, Advanced Synthesis Techniques and Applications of Logic Design. All of these topics are valuable to CAD engineers working in Logic Design, Logic Optimization and Verification. Engineers seeking opportunities for optimizing VLSI integrated circuits will find this book as an invaluable reference since there is no existing book that covers this material in a systematic fashion. Design of Embedded Control Systems Marian Andrzej Adamski, Andrei Karatkevich, Marek Wegrzyn, 2006-11-22 A set of original results in the field of high-level design of logical control devices and systems is presented in this book. These concern different aspects of such important and long-term design problems including the following which seem to be the main ones: First, the behavior of a device under design must be described properly and some adequate formal language should be chosen for that. Second, effective algorithms should be used for checking the prepared description for correctness for its syntactic and semantic verification at the initial behavior level. Third, the problem of logic circuit implementation must be solved using some concrete technological base of efficient methods of logic synthesis, test and verification should be developed for that. Fourth, the task of the communication between the control device and controlled objects and maybe between different control devices waits for its solution. All these problems are hard enough and cannot be successfully solved without efficient methods and algorithms oriented toward computer implementation. Some of these are described in this book. The languages used for behavior description have been descended usually from two well-known abstract models which became classic Petri nets and finite state machines (FSMs). Anyhow, more detailed versions are developed and described in the book which enable to give more complete information concerning specific qualities of the regarded systems. For example, the model of parallel automaton is presented which, unlike the conventional finite automaton, can be placed simultaneously into several places called partial. As a base for circuit implementation of control algorithms, FPGA is accepted in majority of cases. **Logic Synthesis for Finite State Machines Based on Linear Chains of States** Alexander Barkalov, Larysa Titarenko, Jacek Bieganski, 2017-06-24 This book discusses Moore finite state machines (FSMs) implemented with field-programmable gate arrays (FPGAs) including look-up table (LUT) elements and embedded memory blocks (EMBs). To minimize the number of LUTs in FSM logic circuits, the authors propose replacing a state register with a state counter. They also put forward an approach allowing linear chains of states to

be created which simplifies the system of input memory functions and therefore decreases the number of LUTs in the resulting FSM circuit. The authors combine this approach with using EMBs to implement the system of output functions microoperations. This allows a significant decrease in the number of LUTs as well as eliminating a lot of interconnections in the FSM logic circuit. As a rule, it also reduces the area occupied by the circuit and diminishes the resulting power dissipation. This book is an interesting and valuable resource for students and postgraduates in the area of computer science as well as for designers of digital systems that included complex control units.

Logic Synthesis for VLSI-Based Combined Finite State Machines Alexander Barkalov, Larysa Titarenko, Kamil Mielcarek, Małgorzata Mazurkiewicz, Elżbieta Kawecka, 2022-11-24. The book is devoted to design and optimization of control units represented by combined finite state machines CFSMs. The CFSMs combine features of both Mealy and Moore FSMs. Having states of Moore FSM, they produce output signals of both Mealy and Moore types. To optimize the circuits of CFSMs, we propose to use optimization methods targeting both Mealy and Moore FSMs. The book contains some original synthesis and optimization methods targeting hardware reduction in VLSI based CFSM circuits. These methods take into account the peculiarities of both a CFSM model and a VLSI chip in use. The optimization is achieved due to combining classical optimization methods with new methods proposed in this book. These new methods are a mixed encoding of collections of microoperations and a twofold state assignment in CFSMs. All proposed methods target reducing the numbers of arguments in systems of Boolean functions representing CFSM circuits. Also, we propose to use classes of pseudoequivalent states of Moore FSMs to reduce the number of product terms in these systems. The book includes a lot of examples which contribute to a better understanding of the features of the synthesis methods under consideration. This is the first book entirely devoted to the problems associated with synthesis and optimization of VLSI based CFSMs. We hope that the book will be interesting and useful for students and PhD students in the area of Computer Science as well as for designers of various digital systems. We think that proposed CFSM models enlarge the class of models applied for implementation of control units with modern VLSI chips.

Field-Programmable Logic and Applications: The Roadmap to Reconfigurable Computing Reiner W. Hartenstein, Herbert Grünbacher, 2003-06-29. This book is the proceedings volume of the 10th International Conference on Field Programmable Logic and its Applications FPL held August 27-30, 2000 in Villach, Austria, which covered areas like reconfigurable logic, RL, reconfigurable computing, RC, and its applications and all other aspects. Its subtitle, *The Roadmap to Reconfigurable Computing*, reminds us that we are currently witnessing the runaway of a breakthrough. The annual FPL series is the eldest international conference in the world covering configware and all its aspects. It was founded in 1991 at Oxford University, UK, and is 2 years older than its two most important competitors usually taking place at Monterey and Napa. FPL has been held at Oxford, Vienna, Prague, Darmstadt, London, Tallinn, and Glasgow; also see <http://www.fpl.uni-kl.de>. FPL: The New Case for Reconfigurable Platforms Converging Media. Indicated by palmtops, smart mobile phones, many other portables, and consumer

electronics media such as voice sound video TV wireless cable telephone and Internet continue to converge This creates new opportunities and even necessities for reconfigurable platform usage The new converged media require high volume flexible multi purpose multi standard low power products adaptable to support evolving standards emerging new standards field upgrades bug fixes and to meet the needs of a growing number of different kinds of services offered to zillions of individual subscribers preferring different media mixes

Logic Synthesis for Compositional Microprogram Control Units

Alexander Barkalov,Larysa Titarenko,2008-07-17 One of the very important parts of any digital system is the control unit coordin ing interplay of other system blocks As a rule control units have irregular str ture which makes process of their logic circuits design very sophisticated In case of complex logic controllers the problem of system design is reduced practically to the design of control units Actually we observe a real technical boom connected with achievements in semiconductor technology One of these is the development of integrated circuit known as the systems on a programmable chip SoPC where the number of elements approaches one billion Because of the extreme complexity of microchips it is very important to develop effective design methods oriented on particular properties of logical elements Solution of this problem permits impr ing functional capabilities of the target digital system inside single SoPC chip As majority of researches point out design methods used in case of industrial packages are in case of complex digital system design far from optimal Similar problems concern the design of control units with standard eld programmable logic devices FPLD such as PLA PAL GAL CPLD and FPGA Let us point out that modern SoPC are based on CPLD or FPGA technology Thus the development of eff tive design methods oriented on FPLD implementation of logic circuits used in the control units still remains the problem of great importance

***Design of Digital Systems and Devices* Marian Adamski,Alexander Barkalov,Marek Wegrzyn,2011-02-04**

Logic design of digital devices is a very important part of the Computer Science It deals with design and testing of logic circuits for both data path and control unit of a digital system Design methods depend strongly on logic elements using for implementation of logic circuits Different programmable logic devices are wide used for implementation of logic circuits Nowadays we witness the rapid growth of new and new chips but there is a strong lack of new design methods This book includes a variety of design and test methods targeted on different digital devices It covers methods of digital system design the development of theoretical base for construction and designing of the PLD based devices application of UML for digital design A considerable part of the book is devoted to design methods oriented on implementing control units using FPGA and CPLD chips Such important issues as design of reliable FSMs automatic design of concurrent logic controllers the models and methods for creating infrastructure IP services for the SoCs are also presented The editors of the book hope that it will be interesting and useful for experts in Computer Science and Electronics as well as for students who are viewed as designers of future digital devices and systems

***Design of Reconfigurable Logic Controllers* Andrei Karatkevich,Arkadiusz Bukowiec,Michał Doligalski,Jacek Tkacz,2015-12-23** This book presents the original concepts and modern techniques for

specification synthesis optimisation and implementation of parallel logical control devices It deals with essential problems of reconfigurable control systems like dependability modularity and portability Reconfigurable systems require a wider variety of design and verification options than the application specific integrated circuits The book presents a comprehensive selection of possible design techniques The diversity of the modelling approaches covers Petri nets state machines and activity diagrams The preferences of the presented optimization and synthesis methods are not limited to increasing of the efficiency of resource use One of the biggest advantages of the presented methods is the platform independence the FPGA devices and single board computers are some of the examples of possible platforms These issues and problems are illustrated with practical cases of complete control systems If you expect a new look at the reconfigurable systems designing process or need ideas for improving the quality of the project this book is a good choice

Technology Mapping for LUT-Based FPGA Marcin Kubica, Adam Opara, Dariusz Kania, 2020-11-07 This book covers selected topics of automated logic synthesis dedicated to FPGAs The authors focused on two main problems decomposition of the multioutput functions and technology mapping Additionally the idea of using binary decision diagrams BDD in these processes was presented The book is a scientific monograph summarizing the authors many years of research As a result it contains a large number of experimental results which makes it a valuable source for other researchers The book has a significant didactic value Its arrangement allows for a gradual transition from basic things e g description of logic functions to much more complex issues This approach allows less advanced readers to better understand the described problems In addition the authors made sure that the issues described in the book were supported by practical examples thanks to which the reader can independently analyze even the most complex problems described in the book

Prototyping of Concurrent Control Systems Implemented in FPGA Devices

Remigiusz Wiśniewski, 2016-09-30 This book focuses on prototyping aspects of concurrent control systems and their further implementation and partial reconfiguration in programmable devices Further it lays out a full prototyping flow for concurrent control systems Based on a given primary specification a system is described with an interpreted Petri net which naturally reflects the concurrent and sequential relationships of the design The book shows that apart from the traditional option of static configuration of the entire system the latest programmable devices especially FPGAs offer far more sophistication Partial reconfiguration allows selected parts of the system to be replaced without having to reprogram the entire structure of the device Approaches to dynamic and static partial reconfiguration of concurrent control systems are presented and described in detail p The theoretical work is illustrated by examples drawn from various applications with a milling machine and a traffic light controller highlighted as representative interpreted Petri nets Given the ubiquity of concurrent control systems in a huge variety of technological areas including transportation medicine artificial intelligence manufacturing security and safety and planetary exploration the innovative software and hardware design methods described here will be of

considerable interest to control engineers and systems and circuits researchers in many areas of industry and academia

VLSI-SoC: The Advanced Research for Systems on Chip Salvador Mir, Chi-Ying Tsui, Ricardo Reis, Oliver C.S.

Choy, 2012-09-25 This book contains extended and revised versions of the best papers presented at the 19th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2011 held in Hong Kong China in October 2011. The 10 papers included in the book were carefully reviewed and selected from the 45 full papers and 16 special session papers presented at the conference. The papers cover a wide range of topics in VLSI technology and advanced research. They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems. *EDA for IC Implementation, Circuit Design, and Process Technology* Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms, tools, and methodologies used to design integrated circuits, the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes. The second volume, *EDA for IC Implementation: Circuit Design and Process Technology*, thoroughly examines real-time logic to GDSII, a file format used to transfer data of semiconductor physical layout, analog mixed-signal design, physical verification, and technology CAD. TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale, power supply network design and analysis, design modeling, and much more. Save on the complete set [Embedded Systems Handbook 2-Volume Set](#) Richard Zurawski, 2018-10-08 During the past few years, there has been a dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems. These areas include automotive electronics, industrial automated systems, and building automation and control. Comprising 48 chapters and the contributions of 74 leading experts from industry and academia, the *Embedded Systems Handbook, Second Edition*, presents a comprehensive view of embedded systems, their design, verification, networking, and applications. The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials, research surveys, and technology overviews exploring new developments, deployments, and trends. To accommodate the tremendous growth in the field, the handbook is now divided into two volumes. New in This Edition: Processors for embedded systems; Processor-centric architecture; description languages; Networked embedded systems in the automotive and industrial automation fields; Wireless embedded systems; *Embedded Systems Design and Verification, Volume I* of the handbook is divided into three sections. It begins with a brief introduction to embedded systems design and verification. The book then provides a comprehensive overview of embedded processors and various aspects of system-on-chip and FPGA, as well as solutions to design challenges. The final section explores power-aware embedded computing design issues specific to secure embedded systems and web services for embedded devices. *Networked Embedded Systems, Volume II* focuses on selected application areas of networked embedded systems. It covers automotive

field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems

Foundations of Embedded Systems Alexander Barkalov, Larysa Titarenko, Małgorzata Mazurkiewicz, 2019-02-04 This book is devoted to embedded systems ESs which can now be found in practically all fields of human activity Embedded systems are essentially a special class of computing systems designed for monitoring and controlling objects of the physical world The book begins by discussing the distinctive features of ESs above all their cybernetic physical character and how they can be designed to deliver the required performance with a minimum amount of hardware In turn it presents a range of design methodologies Considerable attention is paid to the hardware implementation of computational algorithms It is shown that different parts of complex ESs could be implemented using models of finite state machines FSMs Also field programmable gate arrays FPGAs are very often used to implement different hardware accelerators in ESs The book pays considerable attention to design methods for FPGA based FSMs before the closing section turns to programmable logic controllers widely used in industry This book will be interesting and useful for students and postgraduates in the area of Computer Science as well as for designers of embedded systems In addition it offers a good point of departure for creating embedded systems for various spheres of human activity

Memory-Based Logic Synthesis Tsutomu Sasao, 2011-03-01 This book describes the synthesis of logic functions using memories It is useful to design field programmable gate arrays FPGAs that contain both small scale memories called look up tables LUTs and medium scale memories called embedded memories This is a valuable reference for both FPGA system designers and CAD tool developers concerned with logic synthesis for FPGAs

Swarm and Evolutionary computation Leszek Rutkowski, Marcin Korytkowski, Rafał Scherer, Ryszard Tadeusiewicz, Lotfi Zadeh, Jacek Zurada, 2012-04-15 The volume LNCS 7269 constitutes the refereed proceedings of the International Symposium on Swarm Intelligence and Differential Evolution SIDE 2012 held in Zakopane Poland in April May 2012 in conjunction with the 11th International Conference on Artificial Intelligence and Soft Computing ICAISC 2012 proceedings published as two volume set LNAI 7267 and 7268 The 212 revised full papers presented were carefully reviewed and selected from 483 submissions The volume is divided into two topical parts proceedings of the 2012 symposium on swarm intelligence and differential evolution and on evolutionary algorithms and their applications

Logic and Architecture Synthesis Gabriele Saucier, Anne Mignotte, 2016-01-09 This book describes several methods and systems solving one of the highlighted problems within computer aided design namely architectural and logic synthesis The book emphasises the most recent technologies in high level synthesis concentrating on applicative studies and practical constraints or criteria during synthesis Logic and Architecture Synthesis concentrates on the practical problems involving automatic synthesis of designs It is essential reading for researchers and CAD Managers working in this

area *Logic Synthesis and Verification* Soha Hassoun, Tsutomu Sasao, 2012-12-06 Research and development of logic synthesis and verification have matured considerably over the past two decades Many commercial products are available and they have been critical in harnessing advances in fabrication technology to produce today's plethora of electronic components While this maturity is assuring the advances in fabrication continue to seemingly present unwieldy challenges *Logic Synthesis and Verification* provides a state of the art view of logic synthesis and verification It consists of fifteen chapters each focusing on a distinct aspect Each chapter presents key developments outlines future challenges and lists essential references Two unique features of this book are technical strength and comprehensiveness The book chapters are written by twenty eight recognized leaders in the field and reviewed by equally qualified experts The topics collectively span the field *Logic Synthesis and Verification* fills a current gap in the existing CAD literature Each chapter contains essential information to study a topic at a great depth and to understand further developments in the field The book is intended for seniors graduate students researchers and developers of related Computer Aided Design CAD tools From the foreword The commercial success of logic synthesis and verification is due in large part to the ideas of many of the authors of this book Their innovative work contributed to design automation tools that permanently changed the course of electronic design by Aart J de Geus Chairman and CEO Synopsys Inc

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