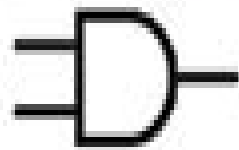
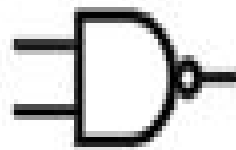


Basic Digital Logic Gates

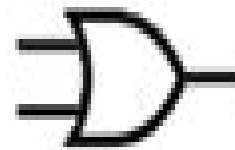
INPUT		OUTPUT
A	B	
0	0	0
1	0	0
0	1	0
1	1	1



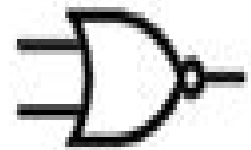
AND



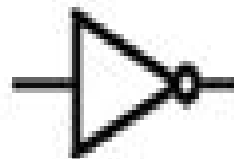
NAND



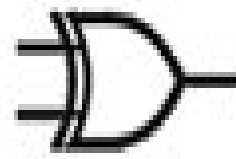
OR



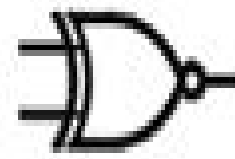
NOR



NOT



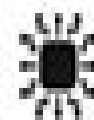
XOR



XNOR



A AND B	$A \cdot B$
A OR B	$A + B$
NOT A	\overline{A}
A XOR B	$A \oplus B$



Electronic Logic Systems

**Dale R. Patrick, Stephen W.
Fardo, Vigyan (Vigs) Chandra, Brian W.
Fardo**

Electronic Logic Systems:

Electronic Logic Circuits J. Gibson, 1992-02-06 First Published in 1992 Routledge is an imprint of Taylor Francis an informa company Electronic Logic Systems A. E. A. Almaini, 1986 **Digital Logic Techniques** T. J. Stonham, 1996

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