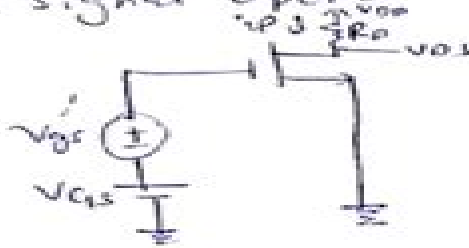


# Lecture 7

## Small signal Operation and Models.



### ① DC Bias Point

DC bias current find by letting  $V_{gs} = 0$

$$I_{D2} = \frac{1}{2} k_n \frac{W}{L} (V_{GS} - V_T)^2$$

$$I_{D2} = \frac{1}{2} k_n V_{GS}^2$$

$$V_{DS} = V_{DD} - I_D R_D$$

For saturation  $V_{DS} > V_{DS,sat}$

since  $V_{GS}$  is superimposed on  $V_{DS}$   
 $V_{GS} \gg V_{DS}$

### ② Signal current in DC bias point

Consider input signal  $V_{gs}$  applied.

$$V_{GS} = V_{GS1} + V_{gs}$$

$$I_D = \frac{1}{2} k_n (V_{GS1} + V_{gs} - V_T)^2$$

$$= \frac{1}{2} k_n (V_{GS1} - V_T)^2 + k_n (V_{GS1} - V_T) V_{gs} + \frac{1}{2} k_n V_{gs}^2$$

dc bias current  
 $I_{D1}$

current component  
 $V_{gs}$  input

current component  
 $V_{gs}$  input  $\rightarrow$  undesirable

①

sat

$$V_{GS} < V_T$$

$$V_{GS} > V_{GS,sat}$$

cutoff  
 $V_{GS} < V_T$

Transition  $V_{GS}$   $V_{GS,sat}$   $V_{GS}$   $V_{GS,sat}$   
 cutoff  $\rightarrow$  A  $\rightarrow$  cutoff  $\rightarrow$  open switch  
 C  $\rightarrow$  triode  $\rightarrow$  closed switch  
 -ve half  $R \approx 0$   
 $V_{GS} = 0$   
 $V_{GS} > 0$



# Electronics Ii

**BL Theraja | RS Sedha**





## Electronics II:



## Reviewing **Electronics Ii**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is really astonishing. Within the pages of "**Electronics Ii**," an enthralling opus penned by a very acclaimed wordsmith, readers attempt an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve in to the book is central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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