Jürgen Becker
Marco Platzner
Serge Vernalde (Eds.)

Field-Programmable Logic and Applications

14th International Conference, FPL 2004
Antwerp, Belgium, August/September 2004
Proceedings

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Will Moore,Wayne Luk

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Field Programmable Logic and Application Jürgen Becker,Marco Platzner,Serge Vernalde,2004-08-19 This book constitutes the refereed proceedings of the 14th International Conference on Field Programmable Logic FPL 2003 held in Leuven Belgium in August September 2004 The 78 revised full papers 45 revised short papers and 29 poster abstracts presented together with 3 keynote contributions and 3 tutorial summaries were carefully reviewed and selected from 285 papers submitted The papers are organized in topical sections on organic and biologic computing security and cryptography platform based design algorithms and architectures acceleration application architecture physical design arithmetic multitasking circuit technology network processing testing applications signal processing computational models and compiler dynamic reconfiguration networks and optimisation algorithms system on chip high speed design image processing network on chip power aware design IP based design co processing architectures system level design physical interconnect computational models cryptography and compression network applications and architecture and debugging and test

Cryptographic Algorithms on Reconfigurable Hardware Francisco Rodriguez-Henriquez,N.A. Saqib,Arturo Díaz Pérez,Cetin Kaya Koc,2007-04-03 Software based cryptography can be used for security applications where data traffic is not too large and low encryption rate is tolerable But hardware methods are more suitable where speed and real time encryption are needed Until now there has been no book explaining how cryptographic algorithms can be implemented on reconfigurable hardware devices This book covers computational methods computer arithmetic algorithms and design improvement techniques needed to implement efficient cryptographic algorithms in FPGA reconfigurable hardware platforms The author emphasizes the practical aspects of reconfigurable hardware design explaining the basic mathematics involved and giving a comprehensive description of state of the art implementation techniques   Power Analysis Attacks Stefan Mangard,Elisabeth Oswald,Thomas Popp,2008-01-03 Power analysis attacks allow the extraction of secret information from smart cards Smart cards are used in many applications including banking mobile communications pay TV and electronic signatures In all these applications the security of the smart cards is of crucial importance Power Analysis Attacks Revealing the Secrets of Smart Cards is the first comprehensive treatment of power analysis attacks and countermeasures Based on the principle that the only way to defend against power analysis attacks is to understand them this book explains how power analysis attacks work Using many examples it discusses simple and differential power analysis as well as advanced techniques like template attacks Furthermore the authors provide an extensive discussion of countermeasures like shuffling masking and DPA resistant logic styles By analyzing the pros and cons of the different countermeasures this volume allows practitioners to decide how to protect smart cards   **BORPH: an Operating System for FPGA-based Reconfigurable Computers** Hayden Kwok-Hay So,2007   CODES+ISSS ... ,2006   **Proceedings : 2006 International Conference on**

Field-Programmable Logic and Applications (FPL) : Madrid, Mapain, August 28-30, 2006 International Conference on Field Programmable Logic and Applications,2006 <u>Field Programmable Logic and Applications</u> Peter Y.K. Cheung,Georg A. Constantinides,Jose T. de Sousa,2003-10-02 This book contains the papers presented at the 13th International Workshop on Field Programmable Logic and Applications FPL held on September 1 3 2003 The conference was hosted by the Institute for Systems and Computer Engineering Research and Development of Lisbon INESC ID and the Depa ment of Electrical and Computer Engineering of the IST Technical University of Lisbon Portugal The FPL series of conferences was founded in 1991 at Oxford University UK and has been held annually since in Oxford 3 times Vienna Prague Darmstadt London Tallinn Glasgow Villach BelfastandMontpellier Itbrings together academic researchers industrial experts users and newcomers in an formal welcomingatmospherethatencouragesproductiveexchangeofideasand knowledge between delegates Exciting advances in eld programmable logic show no sign of slowing down New grounds have been broken in architectures design techniques run time con guration and applications of eld programmable devices in several di erent areas Many of these innovations are reported in this volume The size of FPL conferences has grown signi cantly over the years FPL in 2002 saw 214 papers submitted representing an increase of 83% when compared to the year before The interest and support for FPL in the programmable logic community continued this year with 216 papers submitted The technical p gram was assembled from 90 selected regular papers and 56 posters resulting in this volume of proceedings The program also included three invited plenary keynote presentations from LSI Logic Xilinx and Cadence and three industrial tutorials from Altera Mentor Graphics and Dafca **Field-Programmable Logic and Applications** Peter Y.K. Cheung,Georg A. Constantinides,2003-08-27 This book constitutes the refereed proceedings of the 13th International Conference on Field Programmable Logic and Applications FPL 2003 held in Lisbon Portugal in September 2003 The 90 revised full papers and 56 revised poster papers presented were carefully reviewed and selected from 216 submissions The papers are organized in topical sections on technologies and trends communications applications high level design tools reconfigurable architecture cryptographic applications multi context FPGAs low power issues run time reconfiguration compilation tools asynchronous techniques bio related applications codesign reconfigurable fabrics image processing applications SAT techniques application specific architectures DSP applications dynamic reconfiguration SoC architectures emulation cache design arithmetic bio inspired design SoC design cellular applications fault analysis and network applications

Field-Programmable Logic and Applications Will Moore,Wayne Luk,1995-08-21 This volume constitutes the proceedings of the Fifth International Workshop on Field Programmable Logic and Its Applications FPL 95 held in Oxford UK in August September 1995 The volume presents 46 full revised papers carefully selected by the program committee from a large number and wide range of submissions The papers document the progress achieved since the predecessor conference see LNCS 849 They are organized in sections on architectures platforms tools arithmetic and signal processing embedded

systems and other applications and reconfigurable design and models   2010 International Conference on Field Programmable Logic and Applications ,2010   Proceedings Koen Bertels,Circuits and Systems Society,Technische Universiteit,2007   **Field-Programmable Logic and Applications** Manfred Glesner,Peter Zipf,Michel Renovell,2002-08-21 This book constitutes the refereed proceedings of the 12th International Conference on Field Programmable Logic and Applications FPL 2002 held in Montpellier France in September 2002 The 104 revised regular papers and 27 poster papers presented together with three invited contributions were carefully reviewed and selected from 214 submissions The papers are organized in topical sections on rapid prototyping FPGA synthesis custom computing engines DSP applications reconfigurable fabrics dynamic reconfiguration routing and placement power estimation synthesis issues communication applications new technologies reconfigurable architectures multimedia applications FPGA based arithmetic reconfigurable processors testing and fault tolerance crypto applications multitasking compilation techniques etc
     Proceedings ,2006   *2016 26th International Conference on Field Programmable Logic and Applications (FPL)* IEEE Staff,2016-08-29 The International Conference on Field Programmable Logic and Applications FPL is the first and largest conference covering the rapidly growing area of field programmable logic During the past 26 years many of the advances achieved in reconfigurable system architectures applications embedded processors design automation methods EDA and tools have been first published in the proceedings of the FPL conference series FPL 2016 will offer the following five conference tracks Architectures and Technology Applications and Benchmarks Design Methods and Tools Self aware and Adaptive Systems Surveys Trends and Education   **22nd International Conference on Field Programmable Logic and Applications (FPL)** ,2012   *2012 22nd International Conference on Field Programmable Logic and Applications* IEEE Staff,2012-09-01   **Proceedings** ,2006   2017 27th International Conference on Field Programmable Logic and Applications (FPL) IEEE Staff,2017-09-04 The International Conference on Field Programmable Logic and Applications FPL was the first and remains the largest conference covering the rapidly growing area of field programmable logic and reconfigurable computing The conference objective is to bring together researchers and practitioners from both academia and industry and from around the world FPL 2017 will offer the following five conference tracks Architectures and Technology Applications and Benchmarks Design Methods and Tools Self aware and Adaptive Systems Surveys Trends and Education   **21st International Conference on Field Programmable Logic and Applications** Institute of Electrical and Electronics Engineers,IEEE Computer Society,2011   **2014 24th International Conference on Field Programmable Logic and Applications (FPL).** ,2014

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