Flash Memories

Tobias Bleicker

Flash Memories:

    Flash Memories Paulo Cappelletti,Carla Golla,Piero Olivo,Enrico Zanoni,2013-11-27 A Flash memory is a Non Volatile Memory NVM whose unit cells are fabricated in CMOS technology and programmed and erased electrically In 1971 Frohman Bentchkowsky developed a folating polysilicon gate tran sistor 1 2 in which hot electrons were injected in the floating gate and removed by either Ultra Violet UV internal photoemission or by Fowler Nordheim tunneling This is the unit cell of EPROM Electrically Pro grammable Read Only Memory which consisting of a single transistor can be very densely integrated EPROM memories are electrically programmed and erased by UV exposure for 20 30 mins In the late 1970s there have been many efforts to develop an electrically erasable EPROM which resulted in EEPROMs Electrically Erasable Programmable ROMs EEPROMs use hot electron tunneling for program and Fowler Nordheim tunneling for erase The EEPROM cell consists of two transistors and a tunnel oxide thus it is two or three times the size of an EPROM Successively the combination of hot carrier programming and tunnel erase was rediscovered to achieve a single transistor EEPROM called Flash EEPROM The first cell based on this concept has been presented in 1979 3 the first commercial product a 256K memory chip has been presented by Toshiba in 1984 4 The market did not take off until this technology was proven to be reliable and manufacturable 5   **Flash Memories** Igor Stievano,2011-09-06 Flash memories and memory systems are key resources for the development of electronic products implementing converging technologies or exploiting solid state memory disks This book illustrates state of the art technologies and research studies on Flash memories Topics in modeling design programming and materials for memories are covered along with real application examples   **Flash Memories** Detlev Richter,2013-09-12 The subject of this book is to introduce a model based quantitative performance indicator methodology applicable for performance cost and reliability optimization of non volatile memories The complex example of flash memories is used to introduce and apply the methodology It has been developed by the author based on an industrial 2 bit to 4 bit per cell flash development project For the first time design and cost aspects of 3D integration of flash memory are treated in this book Cell array performance and reliability effects of flash memories are introduced and analyzed Key performance parameters are derived to handle the flash complexity A performance and array memory model is developed and a set of performance indicators characterizing architecture cost and durability is defined Flash memories are selected to apply the Performance Indicator Methodology to quantify design and technology innovation A graphical representation based on trend lines is introduced to support a requirement based product development process The Performance Indicator methodology is applied to demonstrate the importance of hidden memory parameters for a successful product and system development roadmap Flash Memories offers an opportunity to enhance your understanding of product development key topics such as Reliability optimization of flash memories is all about threshold voltage margin understanding and definition Product performance parameter are analyzed in depth in all aspects in relation to the threshold voltage operation window Technical

characteristics are translated into quantitative performance indicators Performance indicators are applied to identify and quantify product and technology innovation within adjacent areas to fulfill the application requirements with an overall cost optimized solution Cost density performance and durability values are combined into a common factor performance indicator which fulfills the application requirements   <u>Inside NAND Flash Memories</u> Rino Micheloni,Luca Crippa,Alessia Marelli,2010-07-27 Digital photography MP3 digital video etc make extensive use of NAND based Flash cards as storage media To realize how much NAND Flash memories pervade every aspect of our life just imagine how our recent habits would change if the NAND memories suddenly disappeared To take a picture it would be necessary to find a film as well as a traditional camera disks or even magnetic tapes would be used to record a video or to listen a song and a cellular phone would return to be a simple mean of communication rather than a multimedia console The development of NAND Flash memories will not be set down on the mere evolution of personal entertainment systems since a new killer application can trigger a further success the replacement of Hard Disk Drives HDDs with Solid State Drives SSDs SSD is made up by a microcontroller and several NANDs As NAND is the technology driver for IC circuits Flash designers and technologists have to deal with a lot of challenges Therefore SSD system developers must understand Flash technology in order to exploit its benefits and countermeasure its weaknesses Inside NAND Flash Memories is a comprehensive guide of the NAND world from circuits design analog and digital to Flash reliability including radiation effects from testing issues to high performance DDR interface from error correction codes to NAND applications like Flash cards and SSDs   **3D Flash Memories** Rino Micheloni,2016-05-26 This book walks the reader through the next step in the evolution of NAND flash memory technology namely the development of 3D flash memories in which multiple layers of memory cells are grown within the same piece of silicon It describes their working principles device architectures fabrication techniques and practical implementations and highlights why 3D flash is a brand new technology After reviewing market trends for both NAND and solid state drives SSDs the book digs into the details of the flash memory cell itself covering both floating gate and emerging charge trap technologies There is a plethora of different materials and vertical integration schemes out there New memory cells new materials new architectures 3D Stacked BiCS and P BiCS 3D FG 3D VG 3D advanced architectures basically each NAND manufacturer has its own solution Chapter 3 to chapter 7 offer a broad overview of how 3D can materialize The 3D wave is impacting emerging memories as well and chapter 8 covers 3D RRAM resistive RAM crosspoint arrays Visualizing 3D structures can be a challenge for the human brain this is way all these chapters contain a lot of bird s eye views and cross sections along the 3 axes The second part of the book is devoted to other important aspects such as advanced packaging technology i e TSV in chapter 9 and error correction codes which have been leveraged to improve flash reliability for decades Chapter 10 describes the evolution from legacy BCH to the most recent LDPC codes while chapter 11 deals with some of the most recent advancements in the ECC field Last but not least chapter 12 looks at 3D flash memories from a system

perspective Is 14nm the last step for planar cells Can 100 layers be integrated within the same piece of silicon Is 4 bit cell possible with 3D Will 3D be reliable enough for enterprise and datacenter applications These are some of the questions that this book helps answering by providing insights into 3D flash memory design process technology and applications
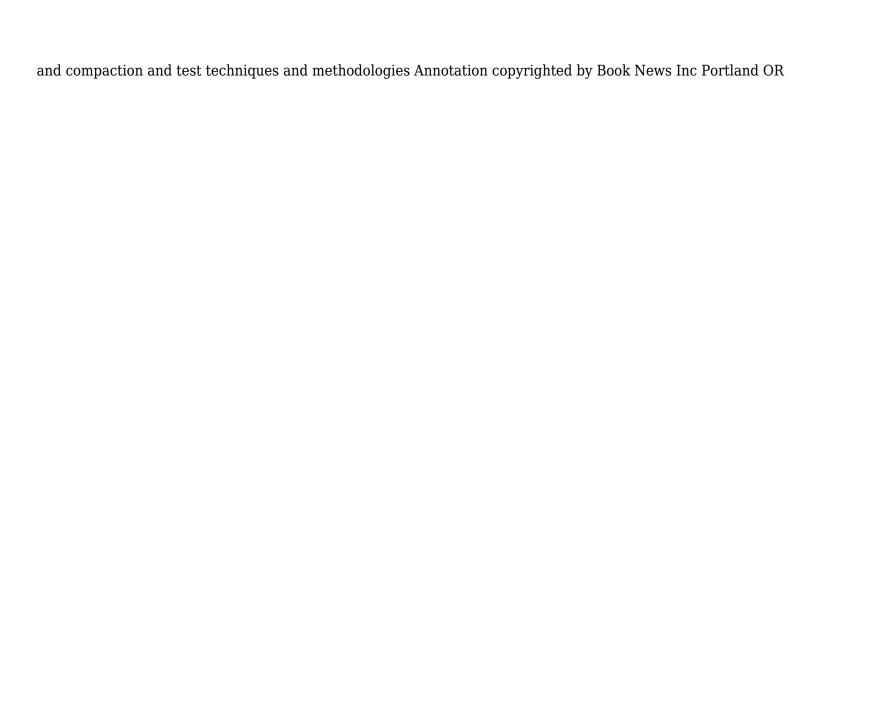
Channel and Source Coding for Non-Volatile Flash Memories Mohammed Rajab,2020-01-02 Mohammed Rajab proposes different technologies like the error correction coding ECC sources coding and offset calibration that aim to improve the reliability of the NAND flash memory with low implementation costs for industrial application The author examines different ECC schemes based on concatenated codes like generalized concatenated codes GCC which are applicable for NAND flash memories by using the hard and soft input decoding Furthermore different data compression schemes are examined in order to reduce the write amplification effect and also to improve the error correct capability of the ECC by combining both schemes   NAND Flash Memory Technologies Seiichi Aritome,2015-11-30 Offers a comprehensive overview of NAND flash memories with insights into NAND history technology challenges evolutions and perspectives Describes new program disturb issues data retention power consumption and possible solutions for the challenges of 3D NAND flash memory Written by an authority in NAND flash memory technology with over 25 years experience   **Nonvolatile Memory Technologies with Emphasis on Flash** Joe Brewer,Manzur Gill,2011-09-23 Presented here is an all inclusive treatment of Flash technology including Flash memory chips Flash embedded in logic binary cell Flash and multilevel cell Flash The book begins with a tutorial of elementary concepts to orient readers who are less familiar with the subject Next it covers all aspects and variations of Flash technology at a mature engineering level basic device structures principles of operation related process technologies circuit design overall design tradeoffs device testing reliability and applications   Flash Memory Integration Jalil Boukhobza,Pierre Olivier,2017-03-10 4 zettabytes 4 billion terabytes of data generated in 2013 44 zettabytes predicted for 2020 and 185 zettabytes for 2025 These figures are staggering and perfectly illustrate this new era of data deluge Data has become a major economic and social challenge The speed of processing of these data is the weakest link in a computer system the storage system It is therefore crucial to optimize this operation During the last decade storage systems have experienced a major revolution the advent of flash memory Flash Memory Integration Performance and Energy Issues contributes to a better understanding of these revolutions The authors offer us an insight into the integration of flash memory in computer systems their behavior in performance and in power consumption compared to traditional storage systems The book also presents in their entirety various methods for measuring the performance and energy consumption of storage systems for embedded as well as desktop server computer systems We are invited on a journey to the memories of the future Ideal for computer scientists featuring low level details to concentrate on system issues Tackles flash memory aspects while spanning domains such as embedded systems and HPC Contains an exhaustive set of experimental results conducted in the Lab STICC laboratory Provides details on methodologies to perform performance and energy measurements

on flash storage systems **Scaled Planar Floating-gate NAND Flash Memory Technology** Shyam Sunder Raghunathan,2010 NAND flash memories are ubiquitous in their use as portable storage media in cellphones cameras music players and other portable electronic devices The NAND flash memory device consisting of a floating gate transistor cell is the most aggressively scaled electronic device as evidenced by ever increasing memory capacities In this work we will examine possible problems arising from continued scaling of these structures and discuss novel solutions to overcome them Firstly we investigate scaling of the conventional poly silicon floating gate aimed at reducing cell to cell interference We experimentally delineate a new reliability concern for the first time with programming current through ultra thin poly silicon floating gates becoming increasingly ballistic We also experimentally demonstrate doping related issues in the poly silicon floating gate We then apply a novel metal based floating gate cell for the first time designed to overcome the problems discussed above We explore factors that influence the choice of metal and demonstrate excellent functionality in ultra thin metal floating gate cells scaled down to 3 nm TiN floating gate thickness thus greatly reducing cell to cell interference Finally in order to facilitate continued scaling of the control dielectric we explore replacement of the conventional silicon oxide nitride dielectric with high k dielectric materials We integrate poly silicon and metal floating gate cells with Al2O3 high k control dielectric Further we establish that a deeper work function control gate is helpful in reducing gate injection Combining ultra thin metal floating gate high k control dielectric and deep work function control gate we enable the planar floating gate cell as a scalable candidate **Embedded Flash Memory for Embedded Systems: Technology, Design for Sub-systems, and Innovations** Hideto Hidaka,2017-09-09 This book provides a comprehensive introduction to embedded flash memory describing the history current status and future projections for technology circuits and systems applications The authors describe current main stream embedded flash technologies from floating gate 1Tr floating gate with split gate 1 5Tr and 1Tr 1 5Tr SONOS flash technologies and their successful creation of various applications Comparisons of these embedded flash technologies and future projections are also provided The authors demonstrate a variety of embedded applications for auto motive smart IC cards and low power representing the leading edge technology developments for eFlash The discussion also includes insights into future prospects of application driven non volatile memory technology in the era of smart advanced automotive system such as ADAS Advanced Driver Assistance System and IoE Internet of Everything Trials on technology convergence and future prospects of embedded non volatile memory in the new memory hierarchy are also described Introduces the history of embedded flash memory technology for micro controller products and how embedded flash innovations developed Includes comprehensive and detailed descriptions of current main stream embedded flash memory technologies sub system designs and applications Explains why embedded flash memory requirements are different from those of stand alone flash memory and how to achieve specific goals with technology development and circuit designs Describes a mature and stable floating gate 1Tr cell technology imported from stand alone flash memory products

that then introduces embedded specific split gate memory cell technologies based on floating gate storage structure and charge trapping SONOS technology and their eFlash sub system designs Describes automotive and smart IC card applications requirements and achievements in advanced eFlash beyond 4 0nm node   Signal ,1993   **Flash Memory Disturb Faults** Mohammad Gh Mohammad,2002   *1995 Product Line Databooks: Flash memory (3 v.)* Intel Corporation,1993   Flash Memory ,1994   **Flash Memory Devices** Cristian Zambelli,Rino Micheloni,2022-02-10 Flash memory devices have represented a breakthrough in storage since their inception in the mid 1980s and innovation is still ongoing The peculiarity of such technology is an inherent flexibility in terms of performance and integration density according to the architecture devised for integration The NOR Flash technology is still the workhorse of many code storage applications in the embedded world ranging from microcontrollers for automotive environment to IoT smart devices Their usage is also forecasted to be fundamental in emerging AI edge scenario On the contrary when massive data storage is required NAND Flash memories are necessary to have in a system You can find NAND Flash in USB sticks cards but most of all in Solid State Drives SSDs Since SSDs are extremely demanding in terms of storage capacity they fueled a new wave of innovation namely the 3D architecture Today 3D means that multiple layers of memory cells are manufactured within the same piece of silicon easily reaching a terabit capacity So far Flash architectures have always been based on floating gate where the information is stored by injecting electrons in a piece of polysilicon surrounded by oxide On the contrary emerging concepts are based on charge trap cells In summary flash memory devices represent the largest landscape of storage devices and we expect more advancements in the coming years This will require a lot of innovation in process technology materials circuit design flash management algorithms Error Correction Code and finally system co design for new applications such as AI and security enforcement   NAND Flash Memory Technologies Seiichi Aritome,2015-12-29 Offers a comprehensive overview of NAND flash memories with insights into NAND history technology challenges evolutions and perspectives Describes new program disturb issues data retention power consumption and possible solutions for the challenges of 3D NAND flash memory Written by an authority in NAND flash memory technology with over 25 years experience   **Memory Products** ,1993   Engineering the Nanocrystal Floating Gates for Nonvolatile Flash Memory Devices Yan Zhu,2007   International Workshop on Electronic Design, Test and Applications Michel Renovell,2002 A collection of the 78 oral presentations and 24 poster papers from the January 2002 international workshop which brought together specialists from a broad area of electronic design manufacturing test and advanced system applications in the hope that the conference would integrate design test and application as cross dependent disciplines The contributions are organized into sessions focusing on analog test communications digital signal processing and architectures low to high level fault simulation and identification high level design memory power issues in design and test sensor and analog design electrical engineering education electromagnetics and control fault tolerant digital systems image processing robotics submicron technology test generation

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The Enigmatic Realm of **Flash Memories**: Unleashing the Language is Inner Magic

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