

- (a) From condition (4), maximum power transfer is achieved when

$$R = \sqrt{r^2 + (x + X)^2} = \sqrt{4^2 + (10 - 7)^2} \\ = \sqrt{4^2 + 3^2} = 5 \, \Omega$$

$$(b) \text{ Current } I = \frac{60 \angle 0^\circ}{(4 + j10) + (5 - j7)} = \frac{60 \angle 0^\circ}{(9 + j3)} \\ = \frac{60 \angle 0^\circ}{9.487 \angle 18.43^\circ} = 6.324 \angle -18.43^\circ \text{ A}$$

Thus the **maximum power** transferred,

$$P = I^2 R = (6.324)^2 (5) = 200 \text{ W}$$

Problem 5. Determine the value of the load resistance R shown in Figure 35.5 that gives maximum power dissipation and calculate the value of this power.

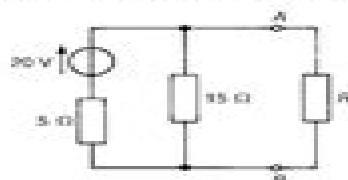


Figure 35.5

Using the procedure of Thévenin's theorem (see page 400):

- (i) R is removed from the network as shown in Figure 35.6

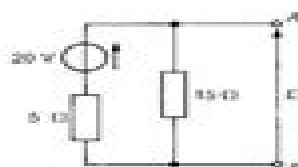


Figure 35.6

- (ii) P.d. across AB, $E = (15/(15 + 5))(20) = 15 \text{ V}$
 (iii) Impedance 'looking-in' at terminals AB with the 20 V source removed is given by $r = (5 \times 15)/(5 + 15) = 3.75 \, \Omega$
 (iv) The equivalent Thévenin circuit supplying terminals AB is shown in Figure 35.7. From condition (2), for maximum power transfer, $R = r$, i.e. $R = 3.75 \, \Omega$

$$\text{Current } I = \frac{E}{R + r} = \frac{15}{3.75 + 3.75} = 2 \text{ A}$$

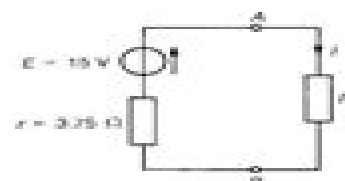


Figure 35.7

Thus the **maximum power** dissipated in the load,

$$P = I^2 R = (2)^2 (3.75) = 15 \text{ W}$$

Problem 6. Determine, for the network shown in Figure 35.8, (a) the values of R and X that will result in maximum power being transferred across terminals AB, and (b) the value of the maximum power.

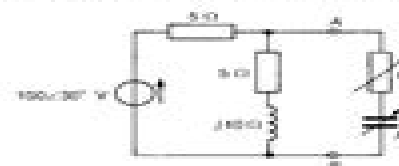


Figure 35.8

- (a) Using the procedure for Thévenin's theorem:

- (i) Resistance R and reactance X are removed from the network as shown in Figure 35.9

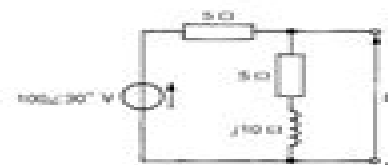


Figure 35.9

- (ii) P.d. across AB,

$$E = \left(\frac{5 + j10}{5 + j10 + 5} \right) (100 \angle 30^\circ) \\ = \frac{(11.18 \angle 63.43^\circ)(100 \angle 30^\circ)}{14.14 \angle 45^\circ} \\ = 79.07 \angle 48.43^\circ \text{ V}$$

Electrical Circuit Theory And Technology

Emilie Sanchez



Electrical Circuit Theory And Technology:

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