

tor and resistor connected across the tubes, or "cushion circuit" as this combination is called. This group includes circuits such as rectifiers supplying motors or capacitive loads running in the discontinuous region, half wave circuits, relaxation inverters, and series transformers.

There is a third group in which, on every cycle, each tube conducts full current up to the instant its successor is fired and shortly thereafter high initial inverse voltage is applied to the anode. This may be damaging and requires study. The group includes full wave or poly-phase rectifier or inverter circuits operating in the continuous current region with firing delayed by grid action, and half-wave tubes used as back rectifiers with firing angle of the controlled tubes retarded.

To analyze the process that occurs during the few microseconds necessary to commute, an example may be taken. A full wave controlled rectifier with firing delayed by grid action and load inductance so great that the ripple current is small, is a typical case. The inherent leakage inductance of each secondary winding of the transformer is indicated at L_1 , L_2 . Cushion resistors and capacitors C_1 , C_2 to limit voltage recovery are shown.



Figure 23

In this circuit the load inductance forces current to flow through the conducting tube (assumed #1 above) beyond the end of each positive half cycle by generating a voltage to more than oppose the voltage of the transformer secondary as it goes negative. This conduction continues until the grid circuit allows the next or "entering tube" (#2 above) to fire.

The events of present interest follow the firing of this entering tube: they are depicted in the following oscillograms. First, since both cathodes are tied together and both tubes are conducting and have substantially equal arc drop, their anodes are at the same potential. Hence the transformer voltage is effectively applied to the leakage inductance, and the net transformer output voltage is zero. The transformer voltages cause a rise of current in L_2 and a decay of current in L_1 at an initial rate of $ec / (L_1 + L_2)$ where ec is the instantaneous commutating (anode-to-anode) voltage. The load inductance holds the sum of these two currents constant. These conditions hold for a period called the current commutation

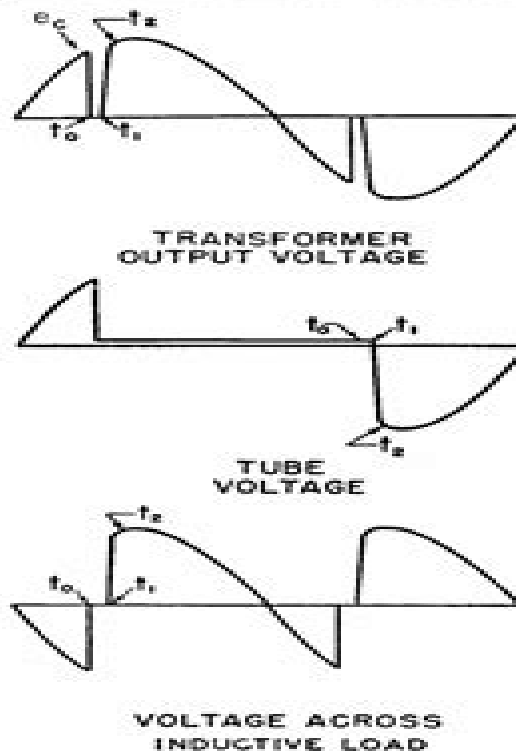


Figure 24

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RD Boyd



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