AM2: Protected-Mode Programming

Duration: 3 hours

Components: Lab exercises and report.

Objectives:

    (a) To examine how protected–mode programming is done on x86 based computer.

    (b) To evaluate the advantages of protected-mode programming compared to real-mode programming in x86 microprocessor.

    (c) To develop protected-mode assembly language programs which utilize the floating-point unit (FPU) on x86 based computer.

    (d) To develop Win32 Console programs based on x86 assembly language.

1. Floating-Point Unit

The Intel 8086 processor was designed to handle only integer arithmetic. This turned out to be a problem for graphics and calculation-intensive software using floating-point calculations. It was possible to emulate floating-point arithmetic purely through software, but the performance penalty was severe. Programs such as *AutoCad* (by Autodesk) demanded a more powerful way to perform floating-point math. Intel sold a separate floating-point coprocessor chip named the 8087, and upgraded it along with each processor generation. Beginning from Intel 80486, the floating-point hardware was integrated into the processor and known as the floating-point unit (FPU).

1.1 FPU Register Stack

The FPU does not use the general-purpose registers (EAX, EBX, etc.). Instead, it has its own set of registers called a *register stack*. It loads values from memory into the register stack, performs calculations, and stores stack values into memory. FPU instructions evaluate mathematical expressions in *postfix* format. The following, for example, is called an *infix* expression: (5 * 6) + 4. The postfix equivalent is

$$5 \quad 6 \quad * \quad 4 \quad +$$

The infix expression **(A + B) * C** requires parenthesis to override the default precedence rules (multiplication before addition). The parenthesis is not required in the equivalent postfix expression:

$$A \quad B \quad + \quad C \quad *$$

1.1.1 Expression Stack

A stack holds intermediate values during the evaluation of postfix expressions. Figure 1.1 shows the steps required to evaluate the postfix expression **5 6 * 4 −**. The stack entries are labelled ST(0) and ST(1), with ST(0) indicating where the stack pointer would normally be pointing.

Eighty Three Eighty Six Protected Mode Programming In C

Léo Pomar

Eighty Three Eighty Six Protected Mode Programming In C:

80386 Protected Mode Programming in C Len Dorfman,1991 The protected mode 80386 processor is an important step in the evolution of PC software particularly in the development of better multitasking and data storage capabilities This book aims to show C programmers how to make the transition to protected mode without having to leave their 8086 code behind   **Eight Thousand Three Hundred and Eighty Six Protected Mode Programming in C** Len Dorfman,1991-02-01   **Paperbound Books in Print 1995** Reed Reference Publishing,R5ference Reed,1995-12   **Paperbound Books in Print** ,1992   Microprocessor System Saifullah Khalid,Dr. Neetu Agrawal,2009-05   The X86 Microprocessors: Architecture and Programming (8086 to Pentium) Lyla B. Das,2010-09   **Inside the Model 80** Chris H. Pappas,William H. Murray,1988   **InfoWorld** ,1983-06-13 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects   *PC Mag* ,1988-08 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology   **PC Mag** ,1988-08 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology   PC Mag ,1989-12-12 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology   **PC Mag** ,1988-09-13 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology   **TN TRB Computer Instructor Grade I Exam | 10 Full-length Mock Tests (Solved 1500+ Questions)** EduGorilla Prep Experts,2022-08-03 Best Selling Book for TN TRB Computer Instructor Grade I Exam with objective type questions as per the latest syllabus given by the Tamil Nadu Teacher Recruitment Board Compare your performance with other students using Smart Answer Sheets in EduGorilla s TN TRB Computer Instructor Grade I Exam Practice Kit TN TRB Computer Instructor Grade I Exam Preparation Kit comes with 10 Full length Mock Tests with the best quality content Increase your chances of selection by 14X TN TRB Computer Instructor Grade I Exam Prep Kit comes with well structured and 100% detailed solutions for all the questions Clear exam with good grades using thoroughly Researched Content by experts   **PC Mag** ,1988-10-11 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology   **Code of Federal Regulations, Title 47, Telecommunication, PT. 80-End, Revised as of October 1, 2012** ,2013-01-28 The Code of Federal Regulations is a codification of the general and permanent rules published in the Federal

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    ○ Chapter 4: Eighty Three Eighty Six Protected Mode Programming In C in Specific Contexts
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