



# Error Detection Circuits

**Ronald Donald Schrimpf, Dan M.  
Fleetwood**



## **Error Detection Circuits:**

**Error Detection Circuits** Michael Gössel, Steffen Graf, 1993 The first comprehensive description of systematic methods for designing optional error detection circuits Table of Contents Overview and Introduction Combinatorial Error Detection Circuitry Sequential Error Detection Circuits Design Algorithms for Error Detection Circuits Appendix References Index 100 illustrations

**New Methods of Concurrent Checking** Michael Gössel, Vitaly Ocheretny, Egor Sogomonyan, Daniel Marienfeld, 2008-04-26 Computers are everywhere around us We for example as air passengers car drivers laptop users with Internet connection cell phone owners hospital patients inhabitants in the vicinity of a nuclear power station students in a digital library or customers in a supermarket are dependent on their correct operation Computers are incredibly fast inexpensive and equipped with almost unimaginable large storage capacity Up to 100 million transistors per chip are quite common today a single transistor for each citizen of a large capital city in the world can be easily accommodated on an ordinary chip The size of such a chip is less than 1 cm This is a fantastic achievement for an unbelievably low price However the very small and rapidly decreasing dimensions of the transistors and their connections over the years are also the reason for growing problems with reliability that will dramatically increase for the nano technologies in the near future Can we always trust computers Are computers always reliable Are chips sufficiently tested with respect to all possible permanent faults if we buy them at a low price or have errors due to undetected permanent faults to be discovered by current checking Besides permanent faults many temporary or transient faults are also to be expected *On-line Error Detection and Fast Recover Techniques for Dependable Embedded Processors* Matthias Pflanz, 2003-07-31 This book presents a new approach to on line observation and concurrent checking of processors by refining and improving known techniques and introducing new ideas The proposed on line error detection and fast recover techniques support and complement other established methods In combination with other on line observation principles and with a combined hardware software test these techniques are used to fulfill a complete self check scheme for an embedded processor Error Detection, Decoding, and Error Correction for Circuit Codes John Christopher Beebe, 1972

**Error Detecting Logic for Digital Computers** Frederick F. Sellers, Muyue Xiao, Mu-yüeh Hsiao, Leroy W. Bearnson, 1968

**Adaptive Clocking and Dynamic Error Detection in Integrated Circuits** Suparna Das, 2008

**Concurrent Error Detection with Latency in Sequential Circuits** Lawrence Paul Holmquist, 1989

**Radiation Effects and Soft Errors in Integrated Circuits and Electronic Devices** Ronald Donald Schrimpf, Dan M. Fleetwood, 2004 This book provides a detailed treatment of radiation effects in electronic devices including effects at the material device and circuit levels The emphasis is on transient effects caused by single ionizing particles single event effects and soft errors and effects produced by the cumulative energy deposited by the radiation total ionizing dose effects Bipolar Si and SiGe metal oxide semiconductor MOS and compound semiconductor technologies are discussed In addition to considering the specific issues associated with high performance devices and technologies the book

includes the background material necessary for understanding radiation effects at a more general level *Cryptographic Hardware and Embedded Systems - CHES 2008* Elisabeth Oswald, 2008-07-18 by Andrey Bogdanov Thomas Eisenbarth Andy Rupp and Christopher Wolf The purpose of the award is to formally acknowledge excellence in research We would like to congratulate the authors of these two papers **Official Gazette of the United States Patent and Trademark Office**, 1999

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published here range from pure experimental neurobiology to neuro ethology mathematics physics and engineering In all cases the research described is focused on understanding how nervous systems compute The actual subjects of the research include a highly diverse number of preparations modeling approaches and analysis techniques Accordingly this volume reflects the breadth and depth of current research in computational neuroscience taking place throughout the world

**Fault Analysis in Cryptography** Marc Joye, Michael Tunstall, 2012-06-21 In the 1970s researchers noticed that radioactive particles produced by elements naturally present in packaging material could cause bits to flip in sensitive areas of electronic chips Research into the effect of cosmic rays on semiconductors an area of particular interest in the aerospace industry led to methods of hardening electronic devices designed for harsh environments Ultimately various mechanisms for fault creation and propagation were discovered and in particular it was noted that many cryptographic algorithms succumb to so called fault attacks Preventing fault attacks without sacrificing performance is nontrivial and this is the subject of this book Part I deals with side channel analysis and its relevance to fault attacks The chapters in Part II cover fault analysis in secret key cryptography with chapters on block ciphers fault analysis of DES and AES countermeasures for symmetric key ciphers and countermeasures against attacks on AES Part III deals with fault analysis in public key cryptography with chapters dedicated to classical RSA and RSA CRT implementations elliptic curve cryptosystems and countermeasures using fault detection devices resilient to fault injection attacks lattice based fault attacks on signatures and fault attacks on pairing based cryptography Part IV examines fault attacks on stream ciphers and how faults interact with countermeasures used to prevent power analysis attacks Finally Part V contains chapters that explain how fault attacks are implemented with chapters on fault injection technologies for microprocessors and fault injection and key retrieval experiments on a widely used evaluation board This is the first book on this topic and will be of interest to researchers and practitioners engaged with cryptographic engineering

*Emerging Technology and Architecture for Big-data Analytics* Anupam Chattopadhyay, Chip Hong Chang, Hao Yu, 2017-04-19 This book describes the current state of the art in big data analytics from a technology and hardware architecture perspective The presentation is designed to be accessible to a broad audience with general knowledge of hardware design and some interest in big data analytics Coverage includes emerging technology and devices for data analytics circuit design for data analytics and architecture and algorithms to support data analytics Readers will benefit from the realistic context used by the authors which demonstrates what works what doesn't work and what are the fundamental problems solutions upcoming challenges and opportunities Provides a single source reference to hardware architectures for big data analytics Covers various levels of big data analytics hardware design abstraction and flow from device to circuits and systems Demonstrates how non volatile memory NVM based hardware platforms can be a viable solution to existing challenges in hardware architecture for big data analytics

*An Ultra-Fast All-Optical Error Detection and Correction Circuit Based on Spectral Logic*, 2000 All optical logic circuits based on the polarization properties of non degenerate four

wave mixing are proposed It is shown that higher level Boolean operations involving several bits can be implemented without resorting to the standard 2 input gates As a simple illustration of the idea a circuit performing error correction on a 3 1 Hamming Code is demonstrated Error free performance Bit Error Rate of

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