



MICHEL MARDIGUIAN

# ELECTROSTATIC DISCHARGE

Understand, Simulate, and Fix  
ESD Problems

THIRD EDITION

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# Electrostatic Discharge Understand Simulate And Fix Esd Problems

**John R. Barnes**



## **Electrostatic Discharge Understand Simulate And Fix ESD Problems:**

Electro Static Discharge Michel Mardiguian, 2011-09-20 A thorough and concise treatment of ESD Recognizing its methodic step by step attack of the electrostatic discharge ESD problem the initial release of this book was quoted by specialists as the most thorough and concise treatment of the broad ESD continuum that is available Now in its Third Edition this book delivers the same trusted coverage of the topic while also incorporating recent technological advances that have taken place in the engineering community The book begins with the basics of ESD for humans and objects and goes on to cover Effects of ESD coupled to electronics Principal ESD specifications ESD diagnostics and testing Design for ESD immunity To help with troubleshooting many ESD case histories are given along with their successful fixes Electrostatic Discharge is essential reading for all designers who want to avoid component failures no trouble found incidents and random errors

**Electrostatic Discharge** Michel Mardiguian, 1985-01-01 *Testing for EMC Compliance* Mark I.

Montrose, Edward M. Nakauchi, 2004-07-26 The Keep It Simple KISS philosophy is the primary focus of this book It is written in very simple language with minimal math as a compilation of helpful EMI troubleshooting hints Its light hearted tone is at odds with the extreme seriousness of most engineering reference works that become boring after a few pages This text tells engineers what to do and how to do it Only a basic knowledge of math electronics and a basic understanding of EMI EMC are necessary to understand the concepts and circuits described Once EMC troubleshooting is demystified readers learn there are quick and simple techniques to solve complicated problems a key aspect of this book Simple and inexpensive methods to resolve EMI issues are discussed to help generate unique ideas and methods for developing additional diagnostic tools and measurement procedures An appendix on how to build probes is included It can be a fun activity even humorous at times with bizarre techniques i e the sticky finger probe

ESD Steven H. Voldman, 2015-01-05 A comprehensive and in depth review of analog circuit layout schematic architecture device power network and ESD design This book will provide a balanced overview of analog circuit design layout analog circuit schematic development architecture of chips and ESD design It will start at an introductory level and will bring the reader right up to the state of the art Two critical design aspects for analog and power integrated circuits are combined The first design aspect covers analog circuit design techniques to achieve the desired circuit performance The second and main aspect presents the additional challenges associated with the design of adequate and effective ESD protection elements and schemes A comprehensive list of practical application examples is used to demonstrate the successful combination of both techniques and any potential design trade offs Chapter One looks at analog design discipline including layout and analog matching and analog layout design practices Chapter Two discusses analog design with circuits examining single transistor amplifiers multi transistor amplifiers active loads and more The third chapter covers analog design layout also MOSFET layout before Chapters Four and Five discuss analog design synthesis The next chapters introduce the reader to analog digital mixed signal design synthesis analog signal pin ESD networks and

analog ESD power clamps Chapter Nine the last chapter covers ESD design in analog applications Clearly describes analog design fundamentals circuit fundamentals as well as outlining the various ESD implications Covers a large breadth of subjects and technologies such as CMOS LDMOS BCD SOI and thick body SOI Establishes an ESD analog design discipline that distinguishes itself from the alternative ESD digital design focus Focuses on circuit and circuit design applications Assessible with the artwork and tutorial style of the ESD book series PowerPoint slides are available for university faculty members Even in the world of digital circuits analog and power circuits are two very important but under addressed topics especially from the ESD aspect Dr Voldman s new book will serve as an essential and practical guide to the greater IC community With high practical and academic values this book is a bible for professionals graduate students device and circuit designers for investigating the physics of ESD and for product designs and testing *Handbook of Electromagnetic Materials* P. S. Neelakanta,1995-06-27 This Handbook explains basic concepts underlying electromagnetic properties of materials addresses ways of deploying them in modern applications and supplies pertinent data compiled for the first time in a single volume Examples including tables charts and graphs are furnished from a practical applications view point of electromagnetic materials in various fields These applications have grown enormously in recent years pertinent to electromagnetic shields radar absorbing materials bioelectromagnetic phantoms smart materials electromagnetically active surfaces exotic magnets application specific electrodes and ferrites etc *The ESD Handbook* Steven H. Voldman,2021-04-12 A practical and comprehensive reference that explores Electrostatic Discharge ESD in semiconductor components and electronic systems The ESD Handbook offers a comprehensive reference that explores topics relevant to ESD design in semiconductor components and explores ESD in various systems Electrostatic discharge is a common problem in the semiconductor environment and this reference fills a gap in the literature by discussing ESD protection Written by a noted expert on the topic the text offers a topic by topic reference that includes illustrative figures discussions and drawings The handbook covers a wide range of topics including ESD in manufacturing garments wrist straps and shoes ESD Testing ESD device physics ESD semiconductor process effects ESD failure mechanisms ESD circuits in different technologies CMOS Bipolar etc ESD circuit types Pin Power Pin to Pin etc and much more In addition the text includes a glossary index tables illustrations and a variety of case studies Contains a well organized reference that provides a quick review on a range of ESD topics Fills the gap in the current literature by providing information from purely scientific and physical aspects to practical applications Offers information in clear and accessible terms Written by the accomplished author of the popular ESD book series Written for technicians operators engineers circuit designers and failure analysis engineers The ESD Handbook contains an accessible reference to ESD design and ESD systems **Robust Electronic Design Reference Book: no special title** John R. Barnes,2004 If you design electronics for a living you need Robust Electronic Design Reference Book Written by a working engineer who has put over 115 electronic products into production at Sycor IBM and Lexmark Robust

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**ESD Basics** Steven H. Voldman, 2012-08-22 Electrostatic discharge ESD continues to impact semiconductor manufacturing semiconductor components and systems as technologies scale from micro to nano electronics This book introduces the fundamentals of ESD electrical overstress EOS electromagnetic interference EMI electromagnetic compatibility EMC and latchup as well as provides a coherent overview of the semiconductor manufacturing environment and the final system assembly It provides an illuminating look into the integration of ESD protection networks followed by examples in specific technologies circuits and chips The text is unique in covering semiconductor chip manufacturing issues ESD semiconductor chip design and system problems confronted today as well as the future of ESD phenomena and nano technology Look inside for extensive coverage on The fundamentals of electrostatics triboelectric charging and how they relate to present day manufacturing environments of micro electronics to nano technology Semiconductor manufacturing handling and auditing processing to avoid ESD failures ESD EOS EMI EMC and latchup semiconductor component and system level testing to demonstrate product resilience from human body model HBM transmission line pulse TLP charged device model CDM human metal model HMM cable discharge events CDE to system level IEC 61000 4 2 tests ESD on chip design and process manufacturing practices and solutions to improve ESD semiconductor chip solutions also practical off chip ESD protection and system level solutions to provide more robust systems System level concerns in servers laptops disk drives cell phones digital cameras hand held devices automobiles and space applications Examples of ESD design for state of the art technologies including CMOS BiCMOS SOI bipolar technology high voltage CMOS HVCMOS RF CMOS smart power magnetic recording technology micro machines MEMs to nano structures ESD Basics From Semiconductor Manufacturing to Product Use complements the author s series of books on ESD protection For those new to the field it is an essential reference and a useful insight into the issues that confront modern technology as we enter the Nano electronic Era

**System Level ESD Co-Design** Charvaka Duvvury, Harald Gossner, 2017-05-05 An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000 4 2 is paramount for any system design This pioneering book presents the collective knowledge of system designers and system testing experts and state of the art techniques for achieving efficient system level ESD protection with minimum impact on the system performance All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used The principal focus of System Level ESD Co Design is defining and establishing the importance of co design efforts from both IC supplier and system builder perspectives ESD designers often face challenges in meeting customers system level ESD requirements and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system

performance With contributions from Robert Ashton Jeffrey Dunnihoo Micheal Hopkins Pratik Maheshwari David Pomerence Wolfgang Reinprecht and Matti Usumaki readers benefit from hands on experience and in depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD robust systems that include mobile and automotive applications The first dedicated resource to system level ESD co design this is an essential reference for industry ESD designers system builders IC suppliers and customers and also Original Equipment Manufacturers OEMs

**Key features** Clarifies the concept of system level ESD protection Introduces a co design approach for ESD robust systems Details soft and hard ESD fail mechanisms Detailed protection strategies for both mobile and automotive applications Explains simulation tools and methodology for system level ESD co design and overviews available test methods and standards Highlights economic benefits of system ESD co design

**Electronic Enclosures, Housings and Packages** Frank Suli, 2018-11-01 Electronic Enclosures Housings and Packages considers the problem of heat management for electronics from an encasement perspective It addresses enclosures and their applications for industrial electronics as well as LED lighting solutions for stationary and mobile markets The book introduces fundamental concepts and defines dimensions of success in electrical enclosures Other chapters discuss environmental considerations shielding standardization materials selection thermal management product design principles manufacturing techniques and sustainability Final chapters focus on business fundamentals by outlining successful technical propositions and potential future directions

**Electrical Overstress (EOS)** Steven H. Voldman, 2013-08-27 Electrical Overstress EOS continues to impact semiconductor manufacturing semiconductor components and systems as technologies scale from micro to nano electronics This book teaches the fundamentals of electrical overstress and how to minimize and mitigate EOS failures The text provides a clear picture of EOS phenomena EOS origins EOS sources EOS physics EOS failure mechanisms and EOS on chip and system design It provides an illuminating insight into the sources of EOS in manufacturing integration of on chip and system level EOS protection networks followed by examples in specific technologies circuits and chips The book is unique in covering the EOS manufacturing issues from on chip design and electronic design automation to factory level EOS program management in today's modern world Look inside for extensive coverage on Fundamentals of electrical overstress from EOS physics EOS time scales safe operating area SOA to physical models for EOS phenomena EOS sources in today's semiconductor manufacturing environment and EOS program management handling and EOS auditing processing to avoid EOS failures EOS failures in both semiconductor devices circuits and system Discussion of how to distinguish between EOS events and electrostatic discharge ESD events e.g. such as human body model HBM charged device model CDM cable discharge events CDM charged board events CBE to system level IEC 61000 4 2 test events EOS protection on chip design practices and how they differ from ESD protection networks and solutions Discussion of EOS system level concerns in printed circuit boards PCB and manufacturing equipment Examples of EOS issues in state of the art digital analog and power

technologies including CMOS LDMOS and BCD EOS design rule checking DRC LVS and ERC electronic design automation EDA and how it is distinct from ESD EDA systems EOS testing and qualification techniques and Practical off chip ESD protection and system level solutions to provide more robust systems Electrical Overstress EOS Devices Circuits and Systems is a continuation of the author's series of books on ESD protection It is an essential reference and a useful insight into the issues that confront modern technology as we enter the nano electronic era

**Electrostatic Discharge in Electronics**  
William D. Greason, 1992      *Handbook of Electromagnetic Compatibility* Reinaldo Perez, 2013-10-22 This book gives readers a concise understanding of the fundamentals of EMC from basic mathematical and physical concepts through present computer age methods used in analysis design and tests With contributions from leading experts in their fields the text provides a comprehensive overview Fortified with information on how to solve potential electromagnetic interference EMI problems that may arise in electronic design practitioners will be better able to grasp the latest techniques trends and applications of this increasingly important engineering discipline Handbook of Electromagnetic Compatibility contains extensive treatment of EMC applications to radio and wireless communications fiber optics communications and plasma effects Coverage of EMC related issues includes lightning electromagnetic pulse biological effects and electrostatic discharge Practical examples are used to illustrate the material and all information is presented in an accessible and organized format The text is intended primarily for those practicing engineers who need a good foundation in EMC but it will also interest faculty and students since a good portion of the material covered can find use in the classroom or as a springboard for further research The chapters are written by experts in the field Details the fundamental principles then moves to more advanced topics Covers computational electromagnetics applied to EMC problems Presents an extensive treatment of EMC applications to Radio and wireless communications Fiber optic communications Plasma effects Wired circuits Microchips Includes practical examples Fiber optic Communications Plasma effects Wired circuits Microchips Includes practical examples

**Electrical Overstress/Electrostatic Discharge Symposium Proceedings**, 2000  
*Nature-Inspired Metaheuristic Algorithms for Engineering Optimization Applications* Serdar Carbas, Abdurrahim Toktas, Deniz Ustun, 2021-03-31 This book engages in an ongoing topic such as the implementation of nature inspired metaheuristic algorithms with a main concentration on optimization problems in different fields of engineering optimization applications The chapters of the book provide concise overviews of various nature inspired metaheuristic algorithms defining their profits in obtaining the optimal solutions of tiresome engineering design problems that cannot be efficiently resolved via conventional mathematical based techniques Thus the chapters report on advanced studies on the applications of not only the traditional but also the contemporary certain nature inspired metaheuristic algorithms to specific engineering optimization problems with single and multi objectives Harmony search artificial bee colony teaching learning based optimization electrostatic discharge grasshopper backtracking search and interactive search are just some of the methods

exhibited and consulted step by step in application contexts The book is a perfect guide for graduate students researchers academicians and professionals willing to use metaheuristic algorithms in engineering optimization applications

Electrostatic Discharge Protection for Electronics Neil Sclater,1990 Neil Sclater offers practical advice on selecting test and control equipment handling and storing semiconductors and other components building static free workstations creating a protective environment for electronics and much more A complete listing of manufacturers and suppliers is included Prior to founding his own electronics marketing research firm Neil Sclater worked as an engineer for several leading corporations and as editor for Electronic Design and Product Engineering He is the author of TPR s Gallium Arsenide IC Technology No 3089 **European Journal of Mechanical Engineering** ,1991 *Symposium Record* ,1992 **EMC Encyclopedia** ,1998 Units terms definitions formulas math models tutorials case histories problem solutions regulations standards test and measurement acronyms products services government agencies organizational bodies and almanac with applications to telecommunications and wireless computers medical electronics consumer electronics industrial process control military electronics electric power vehicles and buildings *Robust Electronic Design Reference Book* John R. Barnes,2004-03-31 If you design electronics for a living you need Robust Electronic Design Reference Book Written by a working engineer who has put over 115 electronic products into production at Sycor IBM and Lexmark Robust Electronic Design Reference covers all the various aspects of designing and developing electronic devices and systems that Work Are safe and reliable Can be manufactured tested repaired and serviced May be sold and used worldwide Can be adapted or enhanced to meet new and changing requirements Robust Electronic Design Reference Book is an electronics designer s reference library condensed into two volumes It guides you through the entire process of Gathering user requirements Developing the design specification Partitioning the design into electronics software and other technologies Designing circuits for signal integrity EMC EMI and ESD Choosing components and materials Reviewing the design Designing printed circuit boards backplanes and cables Bringing up prototypes Testing characterizing and refining your design Getting approvals Putting your product into production or your equipment into service Includes over 600 illustrations nearly 200 tables and an extensive Glossary and Index

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