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Index to IEEE Publications Institute of Electrical and Electronics Engineers, 1990 Issues for 1973 cover the entire IEEE technical literature *Proceedings in Print*, 1991 **Electrical & Electronics Abstracts**, 1997 Handbook of Energy-Aware and Green Computing - Two Volume Set Ishfaq Ahmad, Sanjay Ranka, 2016-02-03 Implementing energy efficient CPUs and peripherals as well as reducing resource consumption have become emerging trends in computing As computers increase in speed and power their energy issues become more and more prevalent The need to develop and promote environmentally friendly computer technologies and systems has also come to the forefront **Wireless Sensor Networks** Shafiullah Khan, Al-Sakib Khan Pathan, Nabil Ali Alrajeh, 2016-04-21 Wireless sensor networks WSNs utilize fast cheap and effective applications to imitate the human intelligence capability of sensing on a wider distributed scale But acquiring data from the deployment area of a WSN is not always easy and multiple issues arise including the limited resources of sensor devices run with one time batteries Additi *Advances in Hardware Design and Verification* Hon Li, David Probst, 2016-01-09 CHARM 97 is the ninth in a series of working conferences devoted to the development and use of formal techniques in digital hardware design and verification This series is held in collaboration with IFIP WG 10.5 Previous meetings were held in Europe every other year **Readings in Hardware/Software Co-Design** Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2001-06-19 Embedded system designers are constantly looking for new tools and techniques to help satisfy the exploding demand for consumer information appliances and specialized industrial products One critical barrier to the timely release of embedded system products is integrating the design of the hardware and software systems Hardware software co design is a set of methodologies and techniques specifically created to support the concurrent design of both systems effectively reducing multiple iterations and major redesigns In addition to its critical role in the development of embedded systems many experts believe that co design will be a key design methodology for Systems on a Chip **Readings in Hardware Software Co Design** presents the papers that have shaped the hardware software co design field since its inception in the early 90s Field experts Giovanni De Micheli Rolf Ernst and Wayne Wolf introduce sections of the book and provide context for the paper that follow This collection provides professionals researchers and graduate students with a single reference source for this critical aspect of computing design Over 50 peer reviewed papers written from leading researchers and designers in the field Selected edited and introduced by three of the fields most eminent researchers and educators Accompanied by an annually updated companion Web site with links and references to recently published papers providing a forum for the editors to comment on how recent work continues or breaks with previous work in the field **Antenna Optimization and Design Based on Binary Coding** Junping Geng, Ronghong Jin, 2022-01-11 This book investigates in detail the antenna optimization method with binary coding and their applications to antenna design It introduces the binary coding principle and optimization method the method of binary coding corresponding to geometry structure In further the

designs by binary coding optimization method of following items are introduced including multi frequency antenna based on binary coding low profile RFID tag antenna on metal wideband directional antenna with low profile mmWave antenna and UWB antenna Additionally improved hexagon unit to antenna optimization by binary coding method is given and a new method of antenna design based on optimization of linear motion trajectory is presented in the end This book proposes an automatic optimization method of meshed antenna based on binary coding reduce the artificial a priori influence and find the best antenna The book is intended for undergraduate and graduate students who are interested in antenna technology researchers investigating high performance antenna and antenna design engineers working on new antenna and the applications

Fault Injection Techniques and Tools for Embedded Systems Reliability Evaluation Alfredo Benso, Paolo Prinetto, 2005-12-15 Fault Injection Techniques and Tools for Embedded Systems Reliability Evaluation intends to be a comprehensive guide to Fault Injection techniques used to evaluate the dependability of a digital system The description and the critical analysis of different Fault Injection techniques and tools will be authored by key scientists in the field of system dependability and fault tolerance

Advances in Petri Nets 1991 Helmut Plünnecke, Wolfgang Reisig, 1991 The main idea behind the series of volumes Advances in Petri Nets is to present to the general computer science community recent results which are the most representative and significant for the development of the area The papers for the volumes are drawn mainly from the annual International Conferences on Applications and Theory of Petri Nets Selected papers from the latest conference are independently refereed and revised and extended as necessary Some further papers submitted directly to the editor are included Advances in Petri Nets 1991 covers the 11th International Conference on Applications and Theory of Petri Nets held in Paris France in June 1991 The volume contains the Bibliography of Petri Nets 1990 prepared by H Plünnecke and W Reisig with over 4000 entries PUBLISHER S WEBSITE

IBM Journal of Research and Development, 1990 Passive Microwave Research Ann M. Brennan, 1992 *Systematic Methodology for Real-Time Cost-Effective Mapping of Dynamic Concurrent Task-Based Systems on Heterogeneous Platforms* Zhe Ma, Pol Marchal, Daniele Paolo Scarpazza, Peng Yang, Chun Wong, José Ignacio Gómez, Stefaan Himpe, Chantal Ykman-Couvreur, Francky Catthoor, 2007-08-26

A genuinely useful text that gives an overview of the state of the art in system level design trade off explorations for concurrent tasks running on embedded heterogeneous multiple processors The targeted application domain covers complex embedded real time multi media and communication applications This material is mainly based on research at IMEC and its international university network partners in this area over the last decade In all the material those in the digital signal processing industry will find here is bang up to date

Sensors Fault Diagnosis Trends and Applications Piotr Witczak, 2021-09-01 Fault diagnosis has always been a concern for industry In general diagnosis in complex systems requires the acquisition of information from sensors and the processing and extracting of required features for the classification or identification of faults Therefore fault diagnosis of sensors is clearly important as faulty information from a sensor may lead

to misleading conclusions about the whole system As engineering systems grow in size and complexity it becomes more and more important to diagnose faulty behavior before it can lead to total failure In the light of above issues this book is dedicated to trends and applications in modern sensor fault diagnosis

Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

Electronic Design Automation for IC System Design, Verification, and Testing Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

Advances in Computers ,2017-08-23 Advances in Computers Volume 107 the latest volume in a series published since 1960 presents detailed coverage of innovations in computer hardware software theory design and applications Chapters in this updated volume include Advances in Model based Analysis and Testing Advances in Software Quality Assurance Advances in Handling Uncertainty in Testing Advances in Testing of Communicating Systems and Advances in Formal Verification and Cyber physical Systems This book provides contributors with a medium in which they can explore their subjects in greater depth and breadth than journal articles usually allow Provides in depth surveys and tutorials on new computer technology Presents well known authors and researchers in the field Includes extensive bibliographies with most chapters Volumes are devoted to single themes or subfields of computer science Frontiers of Quality Electronic Design (QED) Ali Iranmanesh,2023-01-11 Quality Electronic Design QED s landscape spans a vast region where territories of many participating disciplines and technologies overlap This book explores the latest trends in several key topics related to quality electronic design with emphasis on Hardware Security Cybersecurity Machine Learning and application of Artificial Intelligence AI The book includes topics in nonvolatile memories NVM Internet of Things IoT FPGA and Neural Networks

Biomedical Aspects of Manual Wheelchair Propulsion L. H. V. van der Woude, Maria T. E. Hopman, Cathelijne H. Kemenade, 1999 Mobility is fundamental to health social integration and individual well being of the human being Henceforth mobility must be viewed as being essential to the outcome of the rehabilitation process of wheelchair dependent persons and to the successful re integration into society and to a productive and active life Many lower limb disabled subjects depend upon a wheelchair for their mobility Estimated numbers for the Netherlands Europe and USA are respectively 80 000 2 5 million and 1 25 million wheelchair dependent individuals Groups large enough to allow a special research focus and conference activity Both the quality of the wheelchair the individual work capacity the functionality of the wheelchair user combination and the effectiveness of the rehabilitation programme do indeed determine the freedom of mobility Their optimization is highly dependent upon a continuous and high quality research effort in combination with regular discussion and dissemination with practitioners The book intends to give a state of the art view on the current fundamental clinical and applied research findings and their consequences upon wheelchair propulsion arm work wheelchair training and possible consequences of a wheelchair confined life style Also its implications for rehabilitation as well as alternative modes of ambulation and activity in the wheelchair confined population such as functional electrical stimulation and its possible future developments are dealt with Formal Techniques for Safety-Critical Systems Cyrille Artho, Peter Csaba

Ölveczky, 2016-02-02 This book constitutes the refereed proceedings of the 4th International Workshop on Formal Techniques for Safety Critical Systems FTSCS 2015 held in Paris France in November 2015 The 15 revised full papers presented together with one invited talk and two tool papers were carefully reviewed and selected from 41 submissions The papers are organized in topical sections on timed systems railway systems fault tolerance automotive systems software and

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