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devices Early in the 1960s an old research technique named chemical vapour phase deposition CVD which has several unique advantages developed into the most widely used technique for thin film preparation in electronics technology In the last 25 years tremendous advances have been made in the science and technology of thin films prepared by means of CVD This book presents in a single volume an up to date overview of the important field of CVD processes which has never been completely reviewed previously Contents Part I 1 Evolution of CVD Films Introductory remarks Short history of CVD thin films II Fundamentals 2 Techniques of Preparing Thin Films Electrolytic deposition techniques Vacuum deposition techniques Plasma deposition techniques Liquid phase deposition techniques Solid phase deposition techniques Chemical vapour conversion of substrate Chemical vapour deposition Comparison between CVD and other thin film deposition techniques 3 Chemical Processes Used in CVD Introduction Description of chemical reactions used in CVD 4 Thermodynamics of CVD Feasibility of a CVD process Techniques for equilibrium calculations in CVD systems Examples of thermodynamic studies of CVD systems 5 Kinetics of CVD Steps and control type of a CVD heterogeneous reaction Influence of experimental parameters on thin film deposition rate Continuous measurement of the deposition rate Experimental methods for studying CVD kinetics Role of homogeneous reactions in CVD Mechanism of CVD processes Kinetics and mechanism of dopant incorporation Transport phenomena in CVD Status of kinetic and mechanism investigations in CVD systems 6 Measurement of Thin Film Thickness Mechanical methods Mechanical optical methods Optical methods Electrical methods Miscellaneous methods 7 Nucleation and Growth of CVD Films Stages in the nucleation and growth mechanism Regimes of nucleation and growth Nucleation theory Dependence of nucleation on deposition parameters Heterogeneous nucleation and CVD film structural forms Homogeneous nucleation Experimental techniques Experimental results of CVD film nucleation 8 Thin Film Structure Techniques for studying thin film structure Structural defects in CVD thin films 9 Analysis of CVD Films Analysis techniques of thin film bulk Analysis techniques of thin film surfaces Film composition measurement Depth concentration profiling 10 Properties of CVD Films Mechanical properties Thermal properties Optical properties Photoelectric properties Electrical properties Magnetic properties Chemical properties Part III 11 Equipment and Substrates Equipment for CVD Safety in CVD Substrates 12 Preparation and Properties of Semiconducting Thin Films Homoepitaxial semiconducting films Heteroepitaxial semiconducting films 13 Preparation and Properties of Amorphous Insulating Thin Films Oxides Nitrides and Oxynitrides Polymeric thin films 14 Preparation and Properties of Conductive Thin Films Metals and metal alloys Resistor materials Transparent conducting films Miscellaneous materials 15 Preparation and Properties of Superconducting and Magnetic Thin Films Superconducting materials Magnetic materials 16 Uses of CVD Thin Films Applications in electronics and microelectronics Applications in the field of microwaves and optoelectronics Miscellaneous applications Artificial heterostructures Quantum wells superlattices monolayers two dimensional electron gases Part V 17 Present and Future Importance of CVD Films Gallium Arsenide Institute of Physics and the Physical Society, 1969 GaAs

Microelectronics Norman G. Einspruch, William R. Wisseman, 2014-12-01 VLSI Electronics Microstructure Science Volume 11 GaAs Microelectronics presents the important aspects of GaAs Gallium Arsenide IC technology development ranging from materials preparation and IC fabrication to wafer evaluation and chip packaging The volume is comprised of eleven chapters Chapter 1 traces the historical development of GaAs technology for high speed and high frequency applications This chapter summarizes the important properties of GaAs that serve to make this material and its related compounds technologically important Chapter 2 covers GaAs substrate growth ion implantation and annealing and materials characterization technologies that are essential for IC development Chapters 3 6 describe the various IC technologies that are currently under development These include microwave and digital MESFET ICs the most mature technologies and bipolar and field effect heterostructure transistor ICs The high speed capability of GaAs ICs introduces new problems on wafer testing and packaging These topics are discussed in Chapters 7 and 8 Applications for GaAs ICs are covered in Chapters 9 and 10 The first of these chapters is concerned with high speed computer applications the second addresses military applications. The book concludes with a chapter on radiation effects in GaAs ICs Scientists engineers researchers device designers and systems architects will find the book useful Selected Works of Professor Herbert Kroemer C. K. Maiti, 2008 Information technology has changed our society radically Just as the integrated circuits have been the prime mover for electronics high speed transistors and semiconductor lasers based on heterostructures are now playing the same role in modern telecommunications Professor Kroemer's conceptual work on heterostructures began in the early 1950s as he was looking for a way to improve transistor speed and performance In the 1960s he applied the same principles to the development of lasers and light emitting diodes showing that they could achieve continuous operation at room temperature OCo something thought impossible at that time His deep fundamental scientific work has had a profound effect on technology and society transforming and improving our lives This reprint collection brings together Professor Kroemer's most important papers presenting a comprehensive perspective of the field It covers topics ranging from substrate materials electronic properties process technology and devices to circuits and applications This reprint collection will help the reader identify the key stages in the development of heterostructure devices and lasers from early research through to its integration in current manufacturing Devoted to R D engineers and scientists who are actively involved in extending the nano and microelectronics roadmap mainly via heterostructure engineering this volume may also serve as a reference for postgraduate and research students **Growth of Crystals** E. Givargizov, 2012-12-06 The present volume continues the tradition of the preceding volumes covering a wide range of crystal growth problems and treating aspects of critical importance for crystallization Changes in this field of knowledge have however changed the criteria for selection of papers for inclusion in this series The increasing role of crystals in science and technology is even more apparent today The study and utilization of these highly perfect objects of nature considerably facilitates progress in the physics and chemistry of

solids quantum electronics optics microelectron ics and other sciences The demand for crystals and crystal devices has grown steadily and has led to the emergence and rapid growth of the single crystal industry we can safely saythat the state of the art in this industry is indicative of the overall scientific and technolo cal potential of a country At the same time the introduction of crystallization techniques into other industries is gaining ever increasing importance To illustrate this last state ment we can mention the fabrication of textured structural materials and direct methods of metal reduction in ores by using chemical vapor transport techniques Crystallization tech ll niques progress both in width and in depth traditional methods are modernized and novel techniques appear some of them at the junction of the already existing technologies for example flux growth of crystals growth from vapor with participation of the liquid phase etc **GaAs Devices and Circuits** Michael S. Shur, 2013-11-21 GaAs devices and integrated circuits have emerged as leading contenders for ultra high speed applications This book is intended to be a reference for a rapidly growing GaAs community of researchers and graduate students It was written over several years and parts of it were used for courses on GaAs devices and integrated circuits and on heterojunction GaAs devices developed and taught at the University of Minnesota Many people helped me in writing this book I would like to express my deep gratitude to Professor Lester Eastman of Cornell University whose ideas and thoughts inspired me and helped to determine the direction of my research work for many years I also benefited from numerous discussions with his students and associates and from the very atmosphere of the pursuit of excellence which exists in his group I would like to thank my former and present co workers and colleagues Drs Levinstein and Gelmont of the AF Ioffe Institute of Physics and Technology Professor Melvin Shaw of Wayne State University Dr Kastalsky of Bell Communi cations Professor Gary Robinson of Colorado State University Professor Tony Valois and Dr Tim Drummond of Sandia Labs for their contributions to our joint research and for valuable discussions My special thanks to Professor Morko for his help his ideas and the example set by his pioneering work Since 1978 I have been working with engineers from Honeywell Inc Drs

Metal-Semiconductor Schottky Barrier Junctions and Their Applications B.L. Sharma, 2013-11-11 The present day semiconductor technology would be inconceivable without extensive use of Schottky barrier junctions In spite of an excellent book by Professor E H Rhoderick 1978 dealing with the basic principles of metal semiconductor contacts and a few recent review articles the need for a monograph on Metal Semiconductor Schottky Barrier Junctions and Their Applications has long been felt by students researchers and technologists It was in this context that the idea of publishing such a monograph by Mr Ellis H Rosenberg Senior Editor Plenum Publishing Corporation was considered very timely Due to the numerous and varied applications of Schottky barrier junctions the task of bringing it out however looked difficult in the beginning After discussions at various levels it was deemed appropriate to include only those typical applications which were extremely rich in R D and still posed many challenges so that it could be brought out in the stipulated time frame Keeping in view the larger interest it was also considered necessary to have the different topics of Schottky barrier junctions written by experts

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