Fault-Tolerance and Reliability Techniques for High-Density Random-Access Memories

KANAD CHAKRABORTY | PINAKI MAZUMDER

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Douglas Brooks

Fault Tolerance And Reliability Techniques For High Density Random Access Memories:
    Fault Tolerance And Reliability Techniques For High Density Random Access Memories Kanad Chakraborty,Pinaki Mazumder,2002   **Fault-tolerance and Reliability Techniques for High-density Random-access Memories** Kanad Chakraborty,Pinaki Mazumder,2002 This book deals with primarily with reliable and faul tolerant circuit design and evaluation techniques for RAMS It examines both the manufacturing faul tolerance e g self repair at the time of manufacturing and online and field related fault tolerance e g error correction It talks a lot about important techniques and requirements and explains what needs to be done and why for each of the techniques   **Signal Integrity Issues and Printed Circuit Board Design** Douglas Brooks,2003 Complicated concepts explained succinctly and in laymen s terms to both experienced and novice PCB designers Numerous examples allow reader to visualize how high end software simulators see various types of SI problems and then their solutions Author is a frequent and recognized seminar leader in the industry
    High-speed Signal Propagation Howard W. Johnson,Martin Graham,2003 This advanced level reference presents a complete and unified theory of signal propagation for all metallic media from cables to pcb traces to chips It includes numerous examples pictures tables and wide ranging discussion of the high speed properties of transmission lines   Signal Integrity Eric Bogatin,2004 This thorough review of the fundamental principles associated with signal integrity provides engineering principles behind signal integrity effects and applies this understanding to solving problems   *Essential Electronic Design Automation (EDA)* Mark Birnbaum,2004 Describes the engineering needs addressed by the individual EDA tools and covers EDA from both the provider and user viewpoints Learn the importance of marketing and business trends in the EDA industry The EDA consortium is made up of major corporations including SUN HP and Intel   *Nanoelectronics and Photonics* Anatoli Korkin,Federico Rosei,2008-09-23 Nanoelectronics and Photonics From Atoms to Materials Devices and Architectures provides a description of the core elements and challenges of advanced and future information technology Tutorial chapters from leaders in the field cover fundamental topics ranging from materials to devices to system architecture By linking the materials physics and chemistry at the atomic scale with device and circuit design and performance requirements the book presents a coherent picture of theoretical and experimental research efforts and technology development in this highly interdisciplinary area Short visionary articles by Nicolaas Bloembergen Nobel Laureate in Physics 1981 Konstantin Likharev distinguished professor at Stony Brook University and Stanley Williams senior fellow and director of the Quantum Science Research group at Hewlett Packard offer unique perspectives and insights Nanoelectronics and Photonics is essential reading for researchers and graduate students in materials science device physics and electrical and computer engineering Key Features Provides an authoritative overview of the current status and future trends of nanoelectronics and photonics Presents broad ranging tutorials on both theoretical and experimental aspects of key topics in nanotechnology Written by recognized international experts in each area Addresses the needs of both graduate students and

nanotechnology gurus   Printed Circuit Board Designer's Reference Christopher T. Robertson,2004 PCB design instruction and reference manual all in one book with in depth explanation of the processes and tools used in modern PCB design Standards formulas definitions and procedures plus software to tie it all together   Microelectronic Applications of Chemical Mechanical Planarization Yuzhuo Li,2008 An authoritative systematic and comprehensive description of current CMP technology Chemical Mechanical Planarization CMP provides the greatest degree of planarization of any known technique The current standard for integrated circuit IC planarization CMP is playing an increasingly important role in other related applications such as microelectromechanical systems MEMS and computer hard drive manufacturing This reference focuses on the chemical aspects of the technology and includes contributions from the foremost experts on specific applications After a detailed overview of the fundamentals and basic science of CMP Microelectronic Applications of Chemical Mechanical Planarization Provides in depth coverage of a wide range of state of the art technologies and applications Presents information on new designs capabilities and emerging technologies including topics like CMP with nanomaterials and 3D chips Discusses different types of CMP tools pads for IC CMP modeling and the applicability of tribometrology to various aspects of CMP Covers nanotopography CMP performance and defect profiles CMP waste treatment and the chemistry and colloidal properties of the slurries used in CMP Provides a perspective on the opportunities and challenges of the next fifteen years Complete with case studies this is a valuable hands on resource for professionals including process engineers equipment engineers formulation chemists IC manufacturers and others With systematic organization and questions at the end of each chapter to facilitate learning it is an ideal introduction to CMP and an excellent text for students in advanced graduate courses that cover CMP or related semiconductor manufacturing processes   **From ASICs to SOCs** Farzad Nekoogar,Faranak Nekoogar,2003 From ASICs to SOCs A Practical Approach by Farzad Nekoogar and Faranak Nekoogar covers the techniques principles and everyday realities of designing ASICs and SOCs Material includes current issues in the field front end and back end designs integration of IPs on SOC designs and low power design techniques and methodologies Appropriate for practicing chip designers as well as graduate students in electrical engineering   **Records of the 1993 IEEE International Workshop on Memory Testing, August 9-10, 1993, San Jose, California** Rochit Rajsuman,1993 From the August 1993 workshop in San Jose California 26 papers report the latest findings on testing computer memory The sections include test pattern generation algorithms fault models testing for process defects and yield improvement and radiation issues and space applications No subject i   **Design for Electrical and Computer Engineers** Ralph Ford,Chris Coulston,2008 This book is written for students and teachers engaged in electrical and computer engineering ECE design projects primarily in the senior year It guides students and faculty through the steps necessary for the successful execution of design projects The objective of the text is to provide a treatment of the design process in ECE with a sound academic basis that is integrated with practical application It has a strong guiding vision that a

solid understanding of the Design Process Design Tools and the right mix of Professional Skills are critical for project and career success This text is unique in providing a comprehensive design treatment for ECE   **Sci-tech News** ,2003

   Proceedings of the ... ACM Great Lakes Symposium on VLSI. ,2007   **Proceedings** ,2004   *Emerging Nanotechnologies* Mohammad Tehranipoor,2007-12-08 Emerging Nanotechnologies Test Defect Tolerance and Reliability covers various technologies that have been developing over the last decades such as chemically assembled electronic nanotechnology Quantum dot Cellular Automata QCA and nanowires and carbon nanotubes Each of these technologies offers various advantages and disadvantages Some suffer from high power some work in very low temperatures and some others need indeterministic bottom up assembly These emerging technologies are not considered as a direct replacement for CMOS technology and may require a completely new architecture to achieve their functionality Emerging Nanotechnologies Test Defect Tolerance and Reliability brings all of these issues together in one place for readers and researchers who are interested in this rapidly changing field   *Zuverlässigkeit mechatronischer Systeme* Bernd Bertsche,Peter Göhner,Uwe Jensen,Wolfgang Schinköthe,Hans-Joachim Wunderlich,2009-02-17 Dieses Buch thematisiert die Zuverl ssigkeitsbewertung mechatronischer Systeme speziell in fr hen Entwicklungsphasen Herausforderungen hierbei sind vor allem die ganzheitliche Betrachtung ber die Dom nen Mechanik Elektronik und Software sowie unsichere bzw unvollst ndige Daten Neben der dom nen bergreifenden Betrachtungsweise werden zudem Themenaspekte in den einzelnen Dom nen vertieft die zur Zuverl ssigkeitsbewertung in fr hen Entwicklungsphasen dienen   Journal of Nanoscience and Nanotechnology ,2007

   Preprints of the Annual Automotive Technology Development Contractors' Coordination Meeting ,1996

   IBM Journal of Research and Development ,1984

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