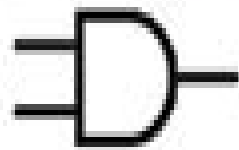
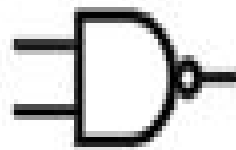


Basic Digital Logic Gates

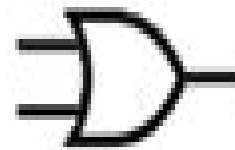
| INPUT | | OUTPUT |
|-------|---|--------|
| A | B | |
| 0 | 0 | 0 |
| 1 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 1 | 1 |



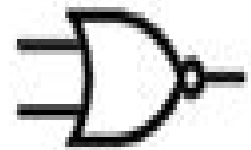
AND



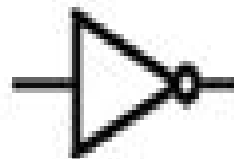
NAND



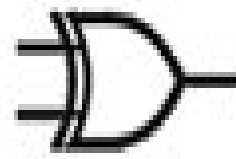
OR



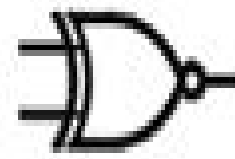
NOR



NOT



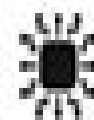
XOR



XNOR



| | |
|---------|----------------|
| A AND B | $A \cdot B$ |
| A OR B | $A + B$ |
| NOT A | \overline{A} |
| A XOR B | $A \oplus B$ |



Electronic Logic Circuits

J. R. Gibson



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