

Electronic Packaging and Interconnection Handbook

McGraw-Hill **Handbook**

ELECTRONIC PACKAGING AND INTERCONNECTION HANDBOOK

FOURTH EDITION

- Micro packaging
- Communications packaging
- Chip scale, flip-chip, and direct chip attachment

CHARLES A. HARPER

Electronic Packaging And Interconnection Handbook

Karl J. Puttlitz, Paul A. Totta



Electronic Packaging And Interconnection Handbook:

Electronic Packaging and Interconnection Handbook Charles A. Harper, 1991 Electronic Packaging and Interconnection Handbook 4/E Charles A. Harper, 2005 Whether you're designing an electronic system from scratch or engineering the project from someone else's design the Handbook gives you the tools you need to get the job done faster cheaper and more reliably than ever We guarantee it From development and design to manufacturing and testing the Handbook has you covered It's the one resource to turn to first Why not put it to the test and see for yourself *Area Array Interconnection Handbook* Karl J. Puttlitz, Paul Totta, 2001 This handbook provides a comprehensive treatment of area array interconnections for both chips and microelectronic packages in terms of optimizing densification functionality and reliability It provides comparisons with alternative and competing technologies clearly defining cost versus benefit tradeoffs and strategies Process details are defined in the order of their typical manufacturing sequence indicating tooling requirements and potential yield detractors In addition the handbook has individual chapters devoted to supporting disciplines that play a key role in satisfying the requirements of microelectronic package applications efficient thermal dissipation techniques metallurgical and mechanical characteristics of interconnections and electrical design strategies Area array technology at both die and chip carrier levels offers the best opportunity of satisfying the demanding performance requirements that users at all levels of the product spectrum have come to expect Electronic Packaging John H. Lau, John Prince, C. P. Wong, Wataru Nakayama, 1998 Here is the ultimate electronic packaging resource in which luminaries from the four intertwined disciplines of packaging present a one stop guide to the state of the art An absolute necessity for anyone working in the field this how to reference covers all the newest technologies including BGA Flip Chip and CSP Microelectronics Packaging Handbook Rao Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 1997-01-31 This thoroughly revised and updated three volume set continues to be the standard reference in the field providing the latest in microelectronics design methods modeling tools simulation techniques and manufacturing procedures Unlike reference books that focus only on a few aspects of microelectronics packaging these outstanding volumes discuss state of the art packages that meet the power cooling protection and interconnection requirements of increasingly dense and fast microcircuitry Providing an excellent balance of theory and practical applications this dynamic compilation features step by step examples and vital technical data simplifying each phase of package design and production In addition the volumes contain over 2000 references 900 figures and 250 tables Part I Technology Drivers covers the driving force of microelectronics packaging electrical thermal and reliability It introduces the technology developer to aspects of manufacturing that must be considered during product development Part II Semiconductor Packaging discusses the interconnection of the IC chip to the first level of packaging and all first level packages Electrical test sealing and encapsulation technologies are also covered in detail Part III Subsystem Packaging explores board level packaging as well as connectors cables and optical packaging **Electronic Packaging**

Materials and Their Properties Michael Pecht, Rakish Agarwal, F. Patrick McCluskey, Terrance J. Dishongh, Sirus Javadpour, Rahul Mahajan, 2017-12-19 Packaging materials strongly affect the effectiveness of an electronic packaging system regarding reliability design and cost In electronic systems packaging materials may serve as electrical conductors or insulators create structure and form provide thermal paths and protect the circuits from environmental factors such as moisture contamination hostile chemicals and radiation Electronic Packaging Materials and Their Properties examines the array of packaging architecture outlining the classification of materials and their use for various tasks requiring performance over time Applications discussed include interconnections printed circuit boards substrates encapsulants dielectrics die attach materials electrical contacts thermal materials solders Electronic Packaging Materials and Their Properties also reviews key electrical thermal thermomechanical mechanical chemical and miscellaneous properties as well as their significance in electronic packaging **Integrated Circuit, Hybrid, and Multichip Module Package Design Guidelines** Michael G. Pecht, 1994-03-31 Circuit designers packaging engineers printed board fabricators and procurement personnel will find this book s microelectronic package design for reliability guidelines and approaches essential for achieving their life cycle cost effectiveness and on time delivery goals Its uniquely organized time phased approach to design development qualification manufacture and in service management shows you step by step how to Define realistic system requirements in terms of mission profile operating life performance expectations size weight and cost Define the system usage environment so that all operating shipping and storage conditions including electrical thermal radiation and mechanical loads are assessed using realistic data Identify potential failure modes sites mechanisms and architecture stress interactions PLUS appropriate measures you can take to reduce eliminate or accommodate expected failures Characterize materials and processes by the key controllable factors such as types and levels of defects variations in material properties and dimensions and the manufacturing and assembly processes involved Use experiment step stress and accelerated methods to ensure optimum design before production begins Detailed design guidelines for substrate wire and wire tape automated and flip chip bonding element attachment and case lead lead and lid seals incorporating dimensional and geometric configurations of package elements manufacturing and assembly conditions materials selection and loading conditions round out this guide s comprehensive coverage Detailed guidelines for substrate wire and wire tape automated and flip chip bonding element attachment and case lead lead and lid seals incorporating dimensional and geometric configurations of package elements manufacturing and assembly conditions materials selection and loading conditions round out this guide s comprehensive coverage **Multichip Module Technologies and Alternatives: The Basics** Daryl Ann Doane, Paul Franzon, 2013-11-27 Far from being the passive containers for semiconductor devices of the past the packages in today s high performance computers pose numerous challenges in interconnecting powering cooling and protecting devices While semiconductor circuit performance measured in picoseconds continues to improve computer performance is expected to be in nanoseconds

for the rest of this century a factor of 1000 difference between on chip and off chip performance which is attributable to losses associated with the package. Thus the package which interconnects all the chips to form a particular function such as a central processor is likely to set the limits on how far computers can evolve. Multichip packaging which can relax these limits and also improve the reliability and cost at the systems level is expected to be the basis of all advanced computers in the future. In addition, since this technology allows chips to be spaced more closely in less space and with less weight, it has the added advantage of being useful in portable consumer electronics as well as in medical, aerospace, automotive, and telecommunications products. The multichip technologies with which these applications can be addressed are many. They range from ceramics to polymer metal thin films to printed wiring boards for interconnections, flip chip, TAB or wire bond for chip to substrate connections, and air or water cooling for the removal of heat.

Materials for Electronic Packaging

Deborah D.L. Chung, 1995-03-31. Although materials play a critical role in electronic packaging, the vast majority of attention has been given to the systems aspect. *Materials for Electronic Packaging* targets materials engineers and scientists by focusing on the materials perspective. The last few decades have seen tremendous progress in semiconductor technology, creating a need for effective electronic packaging. *Materials for Electronic Packaging* examines the interconnections, encapsulations, substrates, heat sinks, and other components involved in the packaging of integrated circuit chips. These packaging schemes are crucial to the overall reliability and performance of electronic systems. Consists of 16 self-contained chapters contributed by a variety of active researchers from industrial, academic, and governmental sectors. Addresses the need of materials scientists, engineers, electrical engineers, mechanical engineers, physicists, and chemists to acquire a thorough knowledge of materials science. Explains how the materials for electronic packaging determine the overall effectiveness of electronic systems.

Area Array Interconnection Handbook Karl J. Puttlitz, Paul A. Totta, 2012-12-06

Microelectronic packaging has been recognized as an important enabler for the solid state revolution in electronics which we have witnessed in the last third of the twentieth century. Packaging has provided the necessary external wiring and interconnection capability for transistors and integrated circuits while they have gone through their own spectacular revolution from discrete device to gigascale integration. At IBM we are proud to have created the initial simple concept of flip chip with solder bump connections at a time when a better way was needed to boost the reliability and improve the manufacturability of semiconductors. The basic design which was chosen for SLT (Solid Logic Technology) in the 1960s was easily extended to integrated circuits in the 70s and VLSI in the 80s and 90s. Three I/O bumps have grown to 3000 with even more anticipated for the future. The package families have evolved from thick film SLT to thin film metallized ceramic to co-fired multi-layer ceramic. A later family of ceramics with matching expansivity to silicon and copper internal wiring was developed as a predecessor of the chip interconnection revolution in copper multilevel submicron wiring. Powerful server packages have been developed in which the combined chip and package copper wiring exceeds a kilometer. All of this was

achieved with the constant objective of minimizing circuit delays through short efficient interconnects **Chip On Board**

John H. Lau, 1994-06-30 This book is a one stop guide to the state of the art of COB technology For professionals active in COB and MCM research and development those who wish to master COB and MCM problem solving methods and those who must choose a cost effective design and high yield manufacturing process for their interconnect systems here is a timely summary of progress in all aspects of this fascinating field It meets the reference needs of design material process equipment manufacturing quality reliability packaging and system engineers and technical managers working in electronic packaging and interconnection *Microelectronics Packaging Handbook* R.R. Tummala, Eugene J. Rymaszewski, Alan G.

Klopfenstein, 2012-12-06 Electronics has become the largest industry surpassing agriculture auto and heavy metal industries It has become the industry of choice for a country to prosper already having given rise to the phenomenal prosperity of Japan Korea Singapore Hong Kong and Ireland among others At the current growth rate total worldwide semiconductor sales will reach 300B by the year 2000 The key electronic technologies responsible for the growth of the industry include semiconductors the packaging of semiconductors for systems use in auto telecom computer consumer aerospace and medical industries displays magnetic and optical storage as well as software and system technologies There has been a paradigm shift however in these technologies from mainframe and supercomputer applications at any cost to consumer applications at approximately one tenth the cost and size Personal computers are a good example going from 500MIP when products were first introduced in 1981 to a projected 11MIP within 10 years Thin light portable user friendly and very low cost are therefore the attributes of tomorrow's computing and communications systems Electronic packaging is defined as interconnection powering cooling and protecting semiconductor chips for reliable systems It is a key enabling technology achieving the requirements for reducing the size and cost at the system and product level **Practical Guide to the Packaging of**

Electronics, Second Edition Ali Jamnia, 2008-11-20 As the demand for packaging more electronic capabilities into smaller packages rises product developers must be more cognizant of how the system configuration will impact its performance Practical Guide to the Packaging of Electronics Second Edition Thermal and Mechanical Design and Analysis provides a basic understanding of the issues that concern the field of electronics packaging First published in 2003 this book has been extensively updated includes more detail where needed and provides additional segments for clarification This volume supplies a solid foundation for heat transfer vibration and life expectancy calculations Topics discussed include various modes of heat removal such as conduction radiation and convection the impact of thermal stresses vibration and the resultant stresses shock management mechanical electrical and chemically induced reliability and more Unlike many other available works it neither assumes the reader's familiarity with the subject nor is it so basic that the reader may lose interest Dr Ali Jamnia has published a large number of engineering papers and presentations and is the holder of a number of patents and patent applications He has been involved in the issues of electronics packaging since the early 90s and since 1995 has

worked toward the development of innovative electronics systems to aid individuals with physical or cognitive disabilities By consulting this manual engineers program managers and quality assurance managers involved in electronic systems gain a fundamental grasp of the issues involved in electronics packaging learn how to define guidelines for a system s design develop the ability to identify reliability issues and concerns and are able to conduct more complete analyses for the final design

Microelectronic Packaging M. Datta,Tetsuya Osaka,J. Walter Schultze,2004-12-20 Microelectronic Packaging analyzes the massive impact of electrochemical technologies on various levels of microelectronic packaging Traditionally interconnections within a chip were considered outside the realm of packaging technologies but this book emphasizes the importance of chip wiring as a key aspect of microelectronic packaging and focuses on electrochemical processing as an enabler of advanced chip metallization Divided into five parts the book begins by outlining the basics of electrochemical processing defining the microelectronic packaging hierarchy and emphasizing the impact of electrochemical technology on packaging The second part discusses chip metallization topics including the development of robust barrier layers and alternative metallization materials Part III explores key aspects of chip package interconnect technologies followed by Part IV s analysis of packages boards and connectors which covers materials development technology trends in ceramic packages and multi chip modules and electroplated contact materials Illustrating the importance of processing tools in enabling technology development the book concludes with chapters on chemical mechanical planarization electroplating and wet etching cleaning tools Experts from industry universities and national laboratories submitted reviews on each of these subjects capturing the technological advances made in each area A detailed examination of how packaging responds to the challenges of Moore s law this book serves as a timely and valuable reference for microelectronic packaging and processing professionals and other industrial technologists

An Introduction to Microelectromechanical Systems Engineering Nadim Maluf,Kirt Williams,2004 Bringing you up to date with the latest developments in MEMS technology this major revision of the best selling An Introduction to Microelectromechanical Systems Engineering offers you a current understanding of this cutting edge technology You gain practical knowledge of MEMS materials design and manufacturing and learn how it is being applied in industrial optical medical and electronic markets The second edition features brand new sections on RF MEMS photo MEMS micromachining on materials other than silicon reliability analysis plus an expanded reference list With an emphasis on commercialized products this unique resource helps you determine whether your application can benefit from a MEMS solution understand how other applications and companies have benefited from MEMS and select and define a manufacturable MEMS process for your application You discover how to use MEMS technology to enable new functionality improve performance and reduce size and cost The book teaches you the capabilities and limitations of MEMS devices and processes and helps you communicate the relative merits of MEMS to your company s management From critical discussions on design operation and process fabrication of devices and systems to a thorough explanation of

MEMS packaging this easy to understand book clearly explains the basics of MEMS engineering making it an invaluable reference for your work in the field

Wireless Medical Systems and Algorithms Pietro Salvo, Miguel Hernandez-Silveira, 2017-11-22 Wireless Medical Systems and Algorithms Design and Applications provides a state of the art overview of the key steps in the development of wireless medical systems from biochips to brain computer interfaces and beyond The book also examines some of the most advanced algorithms and data processing in the field Addressing the latest challenges and solutions related to the medical needs electronic design advanced materials chemistry wireless body sensor networks and technologies suitable for wireless medical devices the text Investigates the technological and manufacturing issues associated with the development of wireless medical devices Introduces the techniques and strategies that can optimize the performances of algorithms for medical applications and provide robust results in terms of data reliability Includes a variety of practical examples and case studies relevant to engineers medical doctors chemists and biologists Wireless Medical Systems and Algorithms Design and Applications not only highlights new technologies for the continuous surveillance of patient health conditions but also shows how disciplines such as chemistry biology engineering and medicine are merging to produce a new class of smart devices capable of managing and monitoring a wide range of cognitive and physical disabilities

Advances in Imaging and Electron Physics, 2012-11-01 Advances in Imaging and Electron Physics merges two long running serials Advances in Electronics and Electron Physics and Advances in Optical and Electron Microscopy This series features extended articles on the physics of electron devices especially semiconductor devices particle optics at high and low energies microlithography image science and digital image processing electromagnetic wave propagation electron microscopy and the computing methods used in all these domains Contributions from leading authorities Informs and updates on all the latest developments in the field

Encapsulation Technologies for Electronic Applications Haleh Ardebili, Michael G. Pecht, 2009-07-22 Electronics are used in a wide range of applications including computing communication biomedical automotive military and aerospace They must operate in varying temperature and humidity environments including indoor controlled conditions and outdoor climate changes Moisture ionic contamination heat radiation and mechanical stresses are all highly detrimental to electronic devices and can lead to device failures Therefore it is essential that the electronic devices be packaged for protection from their intended environments as well as to provide handling assembly electrical and thermal considerations Currently more than 99% of microelectronic devices are plastic encapsulated Improvements in encapsulant materials and cost incentives have stretched the application boundaries for plastic electronic packages Many electronic applications that traditionally used hermetic packages such as military are now using commercial off the shelf COTS plastic packages Plastic encapsulation has the advantages of low cost smaller form factors and improved manufacturability With recent trends in environmental awareness new environmentally friendly or green encapsulant materials i e without brominated additives have emerged Plastic packages are also being considered for

use in extreme high and low temperature electronics 3 D packaging and wafer level packaging WLP require unique encapsulation techniques Encapsulant materials are also being developed for micro electro mechanical systems MEMS bio MEMS bio electronics and organic light emitting diodes O LEDs This book offers a comprehensive discussion of encapsulants in electronic applications The main emphasis is on the encapsulation of microelectronic devices however the encapsulation of connectors and transformers is also addressed This book discusses 2 D and 3 D packaging and encapsulation encapsulation materials including environmentally friendly green encapsulants and the properties and characterization of encapsulants Furthermore this book provides an extensive discussion on defects and failures related to encapsulation how to analyze such defects and failures and how to apply quality assurance and qualification process for encapsulated packages This book also provides information on the trends and challenges of encapsulation and microelectronic packages including application of nanotechnology Guidance on the selection and use of encapsulants in the electronics industry with a particular focus on microelectronics Coverage of environmentally friendly green encapsulants Practical coverage of faults and defects how to analyze them and how to avoid them Using the Engineering Literature Bonnie A. Osif,2006-08-23 The field of engineering is becoming increasingly interdisciplinary and there is an ever growing need for engineers to investigate engineering and scientific resources outside their own area of expertise However studies have shown that quality information finding skills often tend to be lacking in the engineering profession Using the Engineerin **Robust Electronic Design Reference Book: no special title** John R. Barnes,2004 If you design electronics for a living you need Robust Electronic Design Reference Book Written by a working engineer who has put over 115 electronic products into production at Sycor IBM and Lexmark Robust Electronic Design Reference covers all the various aspects of designing and developing electronic devices and systems that Work Are safe and reliable Can be manufactured tested repaired and serviced May be sold and used worldwide Can be adapted or enhanced to meet new and changing requirements

Unveiling the Magic of Words: A Report on "**Electronic Packaging And Interconnection Handbook**"

In a global defined by information and interconnectivity, the enchanting power of words has acquired unparalleled significance. Their capability to kindle emotions, provoke contemplation, and ignite transformative change is really awe-inspiring. Enter the realm of "**Electronic Packaging And Interconnection Handbook**," a mesmerizing literary masterpiece penned by way of a distinguished author, guiding readers on a profound journey to unravel the secrets and potential hidden within every word. In this critique, we shall delve in to the book is central themes, examine its distinctive writing style, and assess its profound effect on the souls of its readers.

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