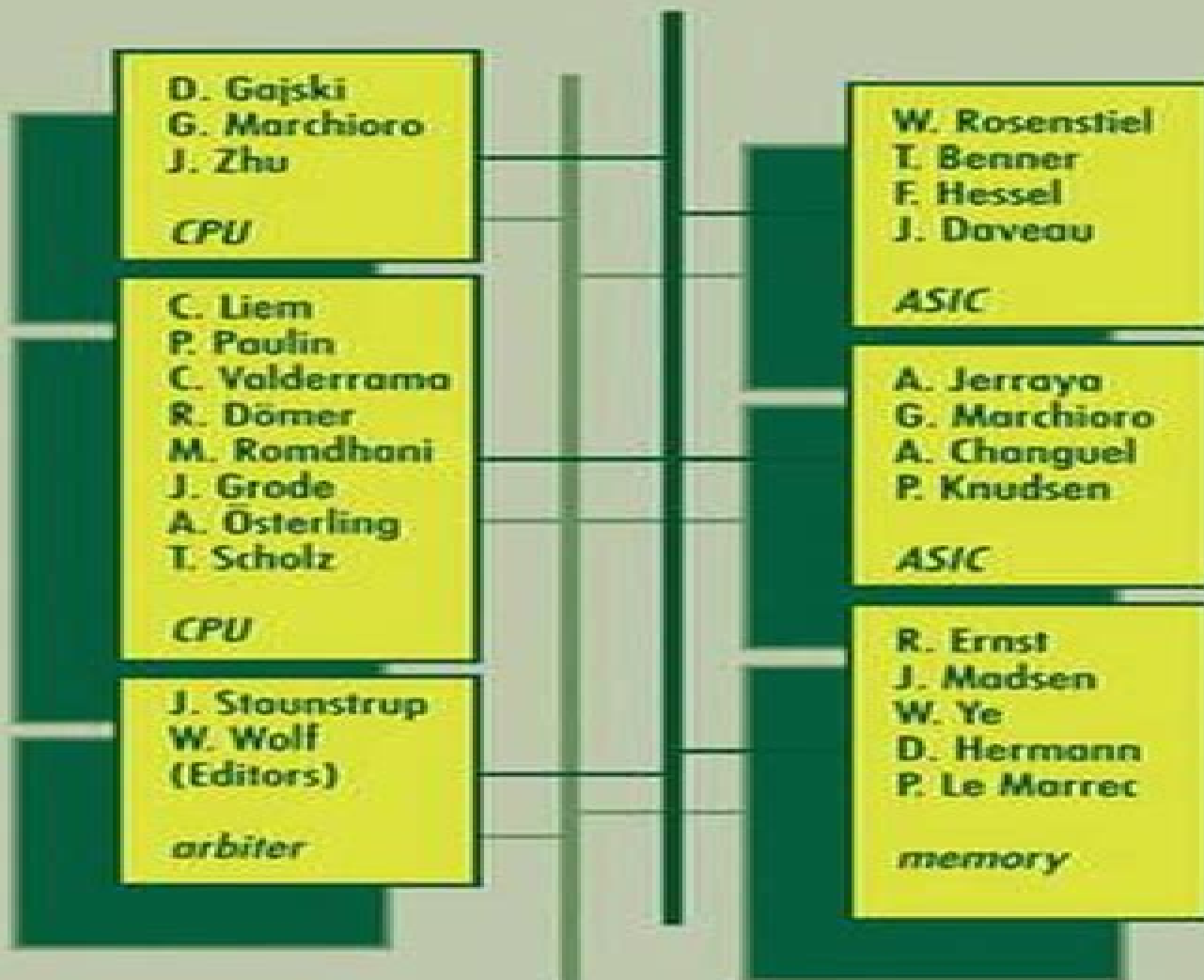


# Hardware/Software Co-Design

## Principles and Practice



# Hardware Software Co Design Principles And Practice

**Ian Pickup**



## **Hardware Software Co Design Principles And Practice:**

*Hardware/Software Co-Design* Jørgen Staunstrup, Wayne Wolf, 2013-04-17 Introduction to Hardware Software Co Design presents a number of issues of fundamental importance for the design of integrated hardware software products such as embedded communication and multimedia systems This book is a comprehensive introduction to the fundamentals of hardware software co design Co design is still a new field but one which has substantially matured over the past few years This book written by leading international experts covers all the major topics including fundamental issues in co design hardware software co synthesis algorithms prototyping and emulation target architectures compiler techniques specification and verification system level specification Special chapters describe in detail several leading edge co design systems including Cosyma LYCOS and Cosmos Introduction to Hardware Software Co Design contains sufficient material for use by teachers and students in an advanced course of hardware software co design It also contains extensive explanation of the fundamental concepts of the subject and the necessary background to bring practitioners up to date on this increasingly important topic

**Hardware/Software Co-Design: Principles And Practice** Staunstrup, 2007-05-01 **Readings in Hardware/Software Co-Design** Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

*System-Level Synthesis* Ahmed Amine Jerraya, Jean Mermet, 2012-12-06 System Level Synthesis deals with the concurrent design of electronic applications including both hardware and software The issue has become the bottleneck in the design of electronic systems including both hardware and software in several major industrial fields including telecommunications automotive and aerospace engineering The major difficulty with the subject is that it demands contributions from several research fields including system specification system architecture hardware design and software design Most existing books cover well only a few aspects of system level synthesis The present volume presents a comprehensive discussion of all the aspects of system level synthesis Each topic is covered by a contribution written by an international authority on the subject

**Distributed and Parallel Embedded Systems** Franz J. Rammig, 2013-03-09 Embedded systems are becoming one of the major driving forces in computer science Furthermore it is the impact of embedded information technology that dictates the pace in most engineering domains Nearly all technical products above a certain level of complexity are not only controlled but increasingly even dominated by their embedded computer systems Traditionally such embedded control systems have been implemented in a monolithic centralized way Recently distributed solutions are gaining increasing importance In this approach the control task is carried out by a number of controllers distributed over the entire system and connected by some interconnect network like fieldbuses Such a distributed embedded system may consist of a few controllers up to several hundred as in today's top range automobiles Distribution and parallelism in embedded systems design increase the engineering challenges and require new development methods and tools This book is the result of the

International Workshop on Distributed and Parallel Embedded Systems DIPES 98 organized by the International Federation for Information Processing IFIP Working Groups 10.3 Concurrent Systems and 10.5 Design and Engineering of Electronic Systems. The workshop took place in October 1998 in Schloss Eringerfeld near Paderborn, Germany, and the resulting book reflects the most recent points of view of experts from Brazil, Finland, France, Germany, Italy, Portugal, and the USA. The book is organized in six chapters: Formalisms for Embedded System Design, IP-based system design and various approaches to multi-language formalisms, Synthesis from Synchronous/Asynchronous Specification, Synthesis techniques based on Message Sequence Charts, MSC, StateCharts, and Predicate Transition Nets, Partitioning and Load Balancing, Application in simulation models and target systems, Verification and Validation, Formal techniques for precise verification and more pragmatic approaches to validation, Design Environments for distributed embedded systems and their impact on the industrial state of the art, Object Oriented Approaches, Impact of OO techniques on distributed embedded systems. LIST: This volume will be essential reading for computer science researchers and application developers.

**Design Principles for Embedded Systems** K.S. Murti, 2021-09-20. The book is designed to serve as a textbook for courses offered to graduate and undergraduate students enrolled in electronics and electrical engineering and computer science. This book attempts to bridge the gap between electronics and computer science students, providing complementary knowledge that is essential for designing an embedded system. The book covers key concepts tailored for embedded system design in one place. The topics covered in this book are: models and architectures, Executable Specific Languages, SystemC, Unified Modeling Language, real-time systems, real-time operating systems, networked embedded systems, Embedded Processor architectures and platforms that are secured and energy efficient. A major segment of embedded systems needs hard real-time requirements. This textbook includes real-time concepts including algorithms and real-time operating system standards like POSIX threads. Embedded systems are mostly distributed and networked for deterministic responses. The book covers how to design networked embedded systems with appropriate protocols for real-time requirements. Each chapter contains 2-3 solved case studies and 10 real-world problems as exercises to provide detailed coverage and essential pedagogical tools that make this an ideal textbook for students enrolled in electrical and electronics engineering and computer science programs.

**Hardware/Software Co-Design** Giovanni DeMicheli, M.G. Sami, 2013-11-11. Concurrent design or co-design of hardware and software is extremely important for meeting design goals such as high performance, which are the key to commercial competitiveness. Hardware/Software Co-Design covers many aspects of the subject, including methods and examples for designing: 1) general-purpose and embedded computing systems based on instruction set processors; 2) telecommunication systems using general-purpose digital signal processors as well as application-specific instruction set processors; 3) embedded control systems and applications to automotive electronics. The book also surveys the areas of emulation and prototyping systems with field-programmable gate array technologies, hardware/software synthesis and verification, and industrial design.

trends Most contributions emphasize the design methodology the requirements and state of the art of computer aided co design tools together with current design examples

**Power Estimation and Optimization Methodologies for VLIW-based Embedded Systems** Vittorio Zaccaria,M.G. Sami,Donatella Sciuto,Cristina Silvano,2007-05-08 LOW power design is playing an important role in today ultra large scale integration ULSI design particularly as we continue to double the number of transistors on a die every two years and increase the frequency of operation at fairly the same rate Certainly an important aspect of low power faces with mobile communications and it has a huge impact on our lives as we are at the start line of the proliferation of mobile PDA s Personal Digital Assistants Wireless LAN and portable multi media computing All of these devices are shaping the way we will be interacting with our family peers and workplace thus requiring also a new and innovative low power design paradigm Furthermore low power design techniques are becoming paramount in high performance desktop base station and server applications such as high speed microprocessors where excess in power dissipation can lead to a number of cooling reliability and signal integrity concerns severely burdening the total industrialcost Hence low power design can be easily anticipated to further come into prominence as we move to next generation System on Chip and Network on Chip designs This book is entirely devoted to disseminate the results of a long term research program between Politecnico di Milano Italy and STMic electronics in the field of architectural exploration and optimization techniques to designing low power embedded systems

Embedded Systems Design with 8051 Microcontrollers Zdravko Karakehayov,2018-10-08 A presentation of developments in microcontroller technology providing lucid instructions on its many and varied applications It focuses on the popular eight bit microcontroller the 8051 and the 83C552 The text outlines a systematic methodology for small scale control dominated embedded systems and is accompanied by a disk of all the example problems included in the book

*Architecture and Design of Distributed Embedded Systems* Bernd Kleinjohann,2013-04-18 Due to the decreasing production costs of IT systems applications that had to be realised as expensive PCBs formerly can now be realised as a system on chip Furthermore low cost broadband communication media for wide area communication as well as for the realisation of local distributed systems are available Typically the market requires IT systems that realise a set of specific features for the end user in a given environment so called embedded systems Some examples for such embedded systems are control systems in cars airplanes houses or plants information and communication devices like digital TV mobile phones or autonomous systems like service or edutainment robots For the design of embedded systems the designer has to tackle three major aspects The application itself including the man machine interface The target architecture of the system including all functional and non functional constraints and the design methodology including modelling specification synthesis test and validation The last two points are a major focus of this book This book documents the high quality approaches and results that were presented at the International Workshop on Distributed and Parallel Embedded Systems DIPES 2000 which was sponsored by the International Federation for

Information Processing IFIP and organised by IFIP working groups WG10.3, WG10.4 and WG10.5. The workshop took place on October 18-19, 2000 in Schlo Eringerfeld near Paderborn, Germany. Architecture and Design of Distributed Embedded Systems is organised similar to the workshop. Chapters 1 and 4, Methodology I and II deal with different modelling and specification paradigms and the corresponding design methodologies. Generic system architectures for different classes of embedded systems are presented in Chapter 2. In Chapter 3, several design environments for the support of specific design methodologies are presented. Problems concerning test and validation are discussed in Chapter 5. The last two chapters include distribution and communication aspects. Chapter 6 and synthesis techniques for embedded systems. Chapter 7. This book is essential reading for computer science researchers and application developers.

**SPECC: Specification Language and Methodology** Daniel D. Gajski, Jianwen Zhu, Rainer Dömer, Andreas Gerstlauer, Shuqing Zhao, 2012-12-06. For the near future, the recent predictions and roadmaps of silicon semiconductor technology all agree that the number of transistors on a chip will keep growing exponentially according to Moore's Law, pushing technology towards the system on a chip (SOC) era. However, we are increasingly experiencing a productivity gap where the chip complexity that can be handled by current design teams falls short of the possibilities offered by technological advances. Together with growing time to market pressures, this drives the need for innovative measures to increase design productivity by orders of magnitude. It is commonly agreed that the solutions for achieving such a leap in design productivity lie in a shift of the focus of the design process to higher levels of abstraction on the one hand and in the massive reuse of pre-designed complex system components (intellectual property IP) on the other hand. In order to be successful, both concepts eventually require the adoption of new languages and methodologies for system design, backed up by the availability of a corresponding set of system level design automation tools. This book presents the SpecC system level design language (SLDL) and the corresponding SpecC design methodology. The SpecC language is intended for specification and design of SOC or embedded systems including software and hardware, whether using fixed platforms integrating systems from different IPs or synthesizing the system blocks from programming or hardware description languages. SpecC Specification Language and Methodology describes the SpecC methodology that leads designers from an executable specification to an RTL implementation through a well-defined sequence of steps. Each model is described and guidelines are given for generating these models from executable specifications. Finally, the SpecC methodology is demonstrated on an industrial size example. The design community is now entering the system level of abstraction era, and SpecC is the enabling element to achieve a paradigm shift in design culture needed for system product design and manufacturing. SpecC Specification Language and Methodology will be of interest to researchers, designers, and managers dealing with system level design, design flows, and methodologies, as well as students learning system specification, modeling, and design.

**Integrated System-Level Modeling of Network-on-Chip enabled Multi-Processor Platforms** Tim Kogel, Rainer Leupers, Heinrich Meyr, 2006-08-25. We are presently observing a paradigm change in designing complex

SoC as it occurs roughly every twelve years due to the exponentially increasing number of transistors on a chip. This design discontinuity as all previous ones is characterized by a move to a higher level of abstraction. This is required to cope with the rapidly increasing design costs. While the present paradigm change shares the move to a higher level of abstraction with all previous ones, there exists also a key difference. For the first time, shrinking geometries do not lead to a corresponding increase of performance. In a recent talk, Lisa Su of IBM pointed out that in 65nm technology only about 25% of performance increase can be attributed to scaling geometries while the lion share is due to innovative processor architecture [1]. We believe that this fact will revolutionize the entire semiconductor industry. What is the reason for the end of the traditional view of Moore's law? It is instructive to look at the major drivers of the semiconductor industry: wireless communications and multimedia. Both areas are characterized by a rapidly increasing demand of computational power in order to process the sophisticated algorithms necessary to optimally utilize the precious resource bandwidth. The computational power cannot be provided by traditional processor architectures and shared bus type of interconnects. The simple reason for this fact is energy efficiency: there exist orders of magnitude between the energy efficiency of an algorithm implemented as a fixed functionality computational element and of a software implementation on a processor.

*Networks on Chip* Axel Jantsch, Hannu Tenhunen, 2007-05-08. As the number of processor cores and IP blocks integrated on a single chip is steadily growing, a systematic approach to design the communication infrastructure becomes necessary. Different variants of packet switched on-chip networks have been proposed by several groups during the past two years. This book summarizes the state of the art of these efforts and discusses the major issues from the physical integration to architecture to operating systems and application interfaces. It also provides a guideline and vision about the direction this field is moving to. Moreover, the book outlines the consequences of adopting design platforms based on packet switched network. The consequences may in fact be far reaching because many of the topics of distributed systems, distributed real time systems, fault tolerant systems, parallel computer architecture, parallel programming, as well as traditional system on chip issues will appear relevant but within the constraints of a single chip VLSI implementation.

**Electronic Engineering and Computing Technology** Len Gelman, 2010-04-21. *Electronic Engineering and Computing Technology* contains sixty-one revised and extended research articles written by prominent researchers participating in the conference. Topics covered include Control Engineering, Network Management, Wireless Networks, Biotechnology, Signal Processing, Computational Intelligence, Computational Statistics, Internet Computing, High Performance Computing, and industrial applications. *Electronic Engineering and Computing Technology* will offer the state of the art of tremendous advances in electronic engineering and computing technology and also serve as an excellent reference work for researchers and graduate students working with on electronic engineering and computing technology.

**Encyclopedia of Computer Science and Technology** Allen Kent, James G.

Williams,2000-04-28 Combining Artificial Neural Networks to Symbolic and Algebraic computation      **Microelectronics Education** B. Courtois,N. Guillemot,G. Kamarinos,G. Stéhelin,2013-03-09 This is the third edition of the European Workshop on Microelectronics Education EWME A steady state regime has now been reached An international community of university teachers is constituted they exchange their experience and their pedagogical tools They discuss the best ways to transfer the rapidly changing techniques to their students and to introduce them to the new physical and mathematical concepts and models for the innovative techniques devices circuits and design methods The number of abstracts submitted to EWME 2000 about one hundred enabled the scientific committee to proceed to a clear selection EWME is a European meeting Indeed authors from 20 different European countries contribute to this volume Nevertheless the participation of authors from Brazil Canada China New Zealand and USA shows that the workshop gradually attains an international dimension th The 20 century can be characterized as the century of electron The electron as an elementary particle was discovered by J J Thomson in 1897 and was rapidly used to transfer energy and information Thanks to electron universe and micro cosmos could be explored Electron became the omnipotent and omnipresent almost immaterial angel of our W orld This was made possible thanks to electronics and for the last 30 years to microelectronics Microelectronics not only modified and even radically transformed the industrial and the every day landscapes but it also led to the so called information revolution with which begins the 21 st century      *Advanced Intelligent Computing Theories and Applications* De-Shuang Huang,Laurent Heutte,Marco Loog,2007-07-31 This volume in conjunction with the two volumes CICS 0002 and LNCS 4681 constitutes the refereed proceedings of the Third International Conference on Intelligent Computing held in Qingdao China in August 2007 The 139 full papers published here were carefully reviewed and selected from among 2 875 submissions These papers offer important findings and insights into the field of intelligent computing      **Intelligent Systems Technologies and Applications** Stefano Berretti,Sabu M. Thampi,Praveen Ranjan Srivastava,2015-08-28 This book contains a selection of refereed and revised papers of Intelligent Techniques and Applications track and the Special Track on Intelligent Image Processing and Artificial Vision track originally presented at the International Symposium on Intelligent Systems Technologies and Applications ISTA August 10 13 2015 Kochi India      **High-Performance Embedded Computing** Marilyn Wolf,2014-03-17 High Performance Embedded Computing Second Edition combines leading edge research with practical guidance in a variety of embedded computing topics including real time systems computer architecture and low power design Author Marilyn Wolf presents a comprehensive survey of the state of the art and guides you to achieve high levels of performance from the embedded systems that bring these technologies together The book covers CPU design operating systems multiprocessor programs and architectures and much more Embedded computing is a key component of cyber physical systems which combine physical devices with computational resources for control and communication This revised edition adds new content and examples of cyber physical systems throughout the book including design methodologies



scheduling and wide area CPS to illustrate the possibilities of these new systems Revised and updated with coverage of recently developed consumer electronics architectures and models of computing Includes new VLIW processors such as the TI Da Vinci and CPU simulation Learn model based verification and middleware for embedded systems Supplemental material includes lecture slides labs and additional resources      *Embedded Systems* Santanu Chattopadhyay,

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