Lecture Notes in Computer Science

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M. Leeser   G. Brown   (Eds.)

Hardware Specification, Verification and Synthesis: Mathematical Aspects

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Proceedings

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Hardware Specification Verification And Synthesis Mathematical Aspects

A. F. Schwarz

Hardware Specification Verification And Synthesis Mathematical Aspects:

    Hardware Specification, Verification and Synthesis Miriam Leeser,Geoffrey Brown,2014-01-15 *Workshop on Hardware Specification, Verification and Synthesis* Workshop on Hardware Specification, Verification and Synthesis (1989, Ithaca, NY),Mathematical Sciences Institute (Ithaca, NY),1989 Hardware Specification, Verification and Synthesis Miriam Leeser,Geoffrey Brown,1990 Correct Hardware Design and Verification Methods George J. Milne,Laurence Pierre,1993-05-12 These proceedings contain the papers presented at the Advanced Research Working Conference on Correct Hardware Design Methodologies held in Arles France in May 1993 and organized by the ESPRIT Working Group 6018 CHARME 2and the Universit de Provence Marseille in cooperation with IFIP Working Group 10 2 Formal verification is emerging as a plausible alternative to exhaustive simulation for establishing correct digital hardware designs The validation of functional and timing behavior is a major bottleneck in current VLSI design systems slowing the arrival of products in the marketplace with its associated increase in cost From being a predominantly academic area of study until a few years ago formal design and verification techniques are now beginning to migrate into industrial use As we are now witnessing an increase in activity in this area in both academia and industry the aim of this working conference was to bring together researchers and users from both communities Correct Hardware Design and Verification Methods Laurence Pierre,Thomas Kropf,2003-07-31 CHARME 99 is the tenth in a series of working conferences devoted to the dev opment and use of leading edge formal techniques and tools for the design and veri cation of hardware and systems Previous conferences have been held in Darmstadt 1984 Edinburgh 1985 Grenoble 1986 Glasgow 1988 Leuven 1989 Torino 1991 Arles 1993 Frankfurt 1995 and Montreal 1997 This workshop and conference series has been organized in cooperation with IFIP WG 10 5 It is now the biannual counterpart of FMCAD which takes place every even numbered year in the USA The 1999 event took place in Bad Her nalb a resort village located in the Black Forest close to the city of Karlsruhe The validation of functional and timing behavior is a major bottleneck in current VLSI design systems A predominantly academic area of study until a few years ago formal design and veri cation techniques are now migrating into industrial use The aim of CHARME 99 is to bring together researchers and users from academia and industry working in this active area of research Two invited talks illustrate major current trends the presentation by G erard Berry Ecole des Mines de Paris Sophia Antipolis France is concerned with the use of synchronous languages in circuit design and the talk given by Peter Jansen BMW Munich Germany demonstrates an application of formal methods in an industrial environment The program also includes 20 regular presentations and 12 short presentations poster exhibitions that have been selected from the 48 submitted papers **Hardware Specification, Verification, and Synthesis** ,1990 *Prospects for Hardware Foundations* Bernhard Möller,John V. Tucker,2003-07-31 Preface VI I X Table of Contents B M ller and J V Tucker Eds Prospects for Hardware Foundations LNCS 1546 pp 1 26 1998 Springer Verlag Berlin Heidelberg 1998 2 The NADA Group Introduction NADA and

NIL 3 4 The NADA Group Introduction NADA and NIL 5 6 The NADA Group Introduction NADA and NIL 7 8 The NADA Group Introduction NADA and NIL 9 10 The NADA Group Introduction NADA and NIL 11 12 The NADA Group Introduction NADA and NIL 13 14 The NADA Group Introduction NADA and NIL 15 16 The NADA Group Introduction NADA and NIL 17 18 The NADA Group Introduction NADA and NIL 19 20 The NADA Group Introduction NADA and NIL 21 22 The NADA Group Introduction NADA and NIL 23 24 The NADA Group Introduction NADA and NIL 25 26 The NADA Group Streams Stream Transformers and Domain Representations B M ller and J V Tucker Eds Prospects for Hardware Foundations LNCS 1546 pp 27 68 1998 Springer Verlag Berlin Heidelberg 1998 28 J Blanck V Stoltenberg Hansen and J V Tucker Streams Stream Transformers and Domain Representations 29 30 J Blanck V Stoltenberg Hansen and J V Tucker Streams Stream Transformers and Domain Representations 31 32 J Blanck V Stoltenberg Hansen and J V Tucker Streams Stream Transformers and Domain Representations 33 34 J Blanck V Stoltenberg Hansen and J V Tucker Streams Stream Transformers and Domain Representations 35 36 J Blanck V Stoltenberg Hansen and J V Tucker Streams Stream Transformers and Domain Representations 37   *Correct Hardware Design and Verification Methods* Dominique Borrione,2005-09-19 This book constitutes the refereed proceedings of the 13th IFIP WG 10 5 Advanced Research Working Conference on Correct Hardware Design and Verification Methods CHARME 2005 held in Saarbr cken Germany in October 2005 The 21 revised full papers and 18 short papers presented together with 2 invited talks and one tutorial were carefully reviewed and selected from 79 submissions The papers are organized in topical sections on functional approaches to design description game solving approaches abstraction algorithms and techniques for speeding DD based verification real time and LTL model checking evaluation of SAT based tools model reduction and verification of memory hierarchy mechanisms

   Computer Hardware Description Languages and their Applications D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification   Certified Programs and Proofs Jean-Pierre Jouannaud,Zhong Shao,2011-11-18 This book constitutes the referred proceedings of the First International Conference on Certified Programs and Proofs CPP 2011 held in Kenting Taiwan in December 2011 The 24 revised regular papers presented together with 4

invited talks were carefully reviewed and selected from 49 submissions They are organized in topical sections on logic and types certificates formalization proof assistants teaching programming languages hardware certification miscellaneous and proof perls   Computer Hardware Description Languages and their Applications D. Borrione,R. Waxman,2014-06-28 The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test The strong relationship between the topics of CHDL 91 and the work around the use and re standardization of the VHDL language is also explored The quality of this proceedings and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled   Theoretical Aspects of Computer Software Takayasu Itō,Albert R. Meyer,1991-08-28 TACS 91 is the first International Conference on Theoretical Aspects of Computer Science held at Tohoku University Japan in September 1991 This volume contains 37 papers and an abstract for the talks presented at the conference TACS 91 focused on theoretical foundations of programming and theoretical aspects of the design analysis and implementation of programming languages and systems The following range of topics is covered logic proof specification and semantics of programs and languages theories and models of concurrent parallel and distributed computation constructive logic category theory and type theory in computer science theory based systems for specifying synthesizing transforming testing and verifying software   Handbook of VLSI Chip Design and Expert Systems A. F. Schwarz,2014-05-10 Handbook of VLSI Chip Design and Expert Systems provides information pertinent to the fundamental aspects of expert systems which provides a knowledge based approach to problem solving This book discusses the use of expert systems in every possible subtask of VLSI chip design as well as in the interrelations between the subtasks Organized into nine chapters this book begins with an overview of design automation which can be identified as Computer Aided Design of Circuits and Systems CADCAS This text then presents the progress in artificial intelligence with emphasis on expert systems Other chapters consider the impact of design automation which exploits the basic capabilities of computers to perform complex calculations and to handle huge amounts of data with a high speed and accuracy This book discusses as well the characterization of microprocessors The final chapter deals with interactive I O devices This book is a valuable resource for system design experts circuit analysts and designers logic designers device engineers technologists and application specific designers   **Programming Language Implementation and Logic Programming** Pierre Deransart,Jan Maluszynski,1990-08-08 This volume consists of the papers accepted for presentation at the second international workshop on Programming Language Implementation and Logic Programming PLILP 90 held in Link ping Sweden August 20 22 1990 The aim of the workshop was to identify concepts and techniques used both in implementation of programming languages regardless of the underlying programming paradigm and in logic programming The intention was to bring together researchers working in these fields The volume includes 26 selected papers falling into two categories Papers in the first category present certain ideas from the point of view of a

particular class of programming languages or even a particular language The ideas presented seem to be applicable in other classes of languages Papers in the second category directly address the problem of integration of various programming paradigms The proceedings of the predecessor workshop PLILP 88 held in Orl ans France May 16 18 1988 are available as Lecture Notes in Computer Science Vol 348 **Efficient Query Processing in Geographic Information Systems** Beng Chin Ooi,1990-11-28 Very Good No Highlights or Markup all pages are intact A Collection of Test Problems for Constrained Global Optimization Algorithms Christodoulos A. Floudas,Panos M. Pardalos,1990-09-15 Significant research activity has occurred in the area of global optimization in recent years Many new theoretical algorithmic and computational contributions have resulted Despite the major importance of test problems for researchers there has been a lack of representative nonconvex test problems for constrained global optimization algorithms This book is motivated by the scarcity of global optimization test problems and represents the first systematic collection of test problems for evaluating and testing constrained global optimization algorithms This collection includes problems arising in a variety of engineering applications and test problems from published computational reports *Semantics of Systems of Concurrent Processes* Irene Guessarian,Laboratoire informatique théorique et programmation (France),1990-11-28 This volume contains the proceedings of the 1990 Spring School of Theoretical Computer Science devoted to the semantics of concurrency The papers are of two kinds surveys and tutorials introducing the subject to novices and students and giving updates of the state of the art research papers presenting recent achievements in the semantics of concurrency The contributions explicate the connections similarities and differences between various approaches to the semantics of concurrency such as pomsets and metric semantics event structures synchronization trees fixpoints and languages traces CCS and Petri nets and categorical models They also cover and compare the various notions of observation and bisimulation equivalences logics for concurrency and applications to dis tributed systems CONPAR 90 - VAPP IV Helmar Burkhart,1990-08-30 Parallel architectures are no longer pure research vehicles as they were some years ago There are now many commercial systems competing for market segments in scientific computing The 1990s are likely to become the decade of parallel processing CONPAR 90 VAPP IV is the joint successor meeting of two highly successful international conference series in the field of vector and parallel processing This volume contains the 79 papers presented at the conference The various topics of the papers include hardware software and application issues Some of the session titles best reflect the contents new models of computation logic programming large grain data flow interconnection networks communication issues reconfigurable and scalable systems novel architectures and languages high performance systems and accelerators performance prediction analysis measurement performance monitoring and debugging compile time analysis and restructurers load balancing process partitioning and concurrency control visualization and runtime analysis parallel linear algebra architectures for image processing efficient use of vector computers transputer tools and applications array processors algorithmic studies for

hypercube type systems systolic arrays and algorithms The volume gives a comprehensive view of the state of the art in a field of current interest   Attribute Grammars and Their Applications Pierre Deransart,Martin Jourdan,1990-09-07 Proceedings   A Systematic Catalogue of Reusable Abstract Data Types Jürgen Uhl,Hans A. Schmid,1990-10-24 This book presents a comprehensive catalogue of elementary data types like sets maps orders trees and lists written in Ada Such data types are often used in systems programming The major focus is on a uniform syntactic and semantic interface for all data types many implementation variants per data type all ac cessible through a single interface a hierarchical system of the data types as a basis for data type selection and implementation Meeting these goals is the main achievement of the book The combination of efficient applicability and ease of learning and maintenance is achieved by the carefully elaborated interfaces of the catalogue s data types These interfaces combine abstraction which is necessary for easy learning and for leaving implementation freedom and functional completeness which is an essential prerequisite for high performance in different application contexts The selection of the right data type implementation for a given context is supported by the data type hierarchy which imposes different abstraction levels and an orthogonal scheme of implementation variants which can be freely combined Together with the uniformity of interfaces the hierarchical composition of the catalogue leads to a small code base from which different implementation variants are generated using a macro processor

Whispering the Strategies of Language: An Psychological Quest through **Hardware Specification Verification And Synthesis Mathematical Aspects**

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