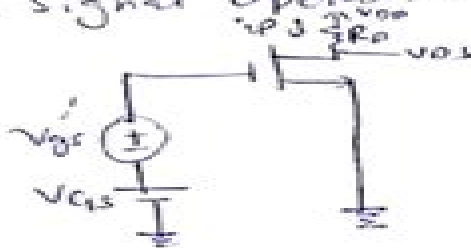


Lecture 7

Small signal Operation and Models.



① DC Bias Point

DC bias current find by letting $v_{gs} = 0$

$$I_{D2} = \frac{1}{2} k_n \frac{W}{L} (V_{GS} - V_T)^2$$

$$I_{D2} = \frac{1}{2} k_n V_{GS}^2$$

$$V_{DS} = V_{DD} - I_D R_D$$

For saturation $V_{DS} > V_{GS} - V_T$

since v_{gs} is superimposed on V_{GS}

$$V_{GS} \gg V_T$$

①
sat
 $V_{GS} < V_T$
 $V_{GS} > V_{GS}$
cutoff
 $V_{GS} < V_T$
linear
 $V_{GS} > V_T$
open switch
closed switch
 $R = 0$
 $V_{GS} = 0$
 $V = 0$

② Signal current in DC bias point

consider input signal v_{gs} applied.

$$V_{GS} = V_{GS} + v_{gs}$$

$$I_D = \frac{1}{2} k_n (V_{GS} + v_{gs} - V_T)^2$$

$$= \frac{1}{2} k_n (V_{GS} - V_T)^2 + k_n (V_{GS} - V_T) v_{gs} + \frac{1}{2} k_n v_{gs}^2$$

dc bias
current
 I_D

current component
 v_{gs} input

current component
 v_{gs} input → undesirable

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C Cleary



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