

Proceedings


Euromicro Symposium on Digital Systems Design

Architectures, Methods and Tools

DSD 2001

Euromicro Symposium On Digital System Design Proceedings

**Bhattacharyya, Siddhartha,De,
Sourav,Pan, Indrajit,Dutta,
Paramartha**



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2018 21st Euromicro Conference on Digital System Design (DSD). Martin Novotný, Nikos Konofaos, Amund Skavhaug, 2018 **VLSI 2010 Annual Symposium** Nikolaos Voros, Amar Mukherjee, Nicolas Sklavos, Konstantinos Masselos, Michael Huebner, 2011-09-08 VLSI 2010 Annual Symposium will present extended versions of the best papers presented in ISVLSI 2010 conference The areas covered by the papers will include among others Emerging Trends in VLSI Nanoelectronics Molecular Biological and Quantum Computing MEMS VLSI Circuits and Systems Field programmable and Reconfigurable Systems System Level Design System on a Chip Design Application Specific Low Power VLSI System Design System Issues in Complexity Low Power Heat Dissipation Power Awareness in VLSI Design Test and Verification Mixed Signal Design and Analysis Electrical Packaging Co Design Physical Design Intellectual property creating and sharing

Multi-Core Embedded Systems Georgios Kornaros, 2018-10-08 Details a real world product that applies a cutting edge multi core architecture Increasingly demanding modern applications such as those used in telecommunications networking and real time processing of audio video and multimedia streams require multiple processors to achieve computational performance at the rate of a few giga operations per second This necessity for speed and manageable power consumption makes it likely that the next generation of embedded processing systems will include hundreds of cores while being increasingly programmable blending processors and configurable hardware in a power efficient manner Multi Core Embedded Systems presents a variety of perspectives that elucidate the technical challenges associated with such increased integration of homogeneous processors and heterogeneous multiple cores It offers an analysis that industry engineers and professionals will need to understand the physical details of both software and hardware in embedded architectures as well as their limitations and potential for future growth Discusses the available programming models spread across different abstraction levels The book begins with an overview of the evolution of multiprocessor architectures for embedded applications and discusses techniques for autonomous power management of system level parameters It addresses the use of existing open source and free tools originating from several application domains such as traffic modeling graph theory parallel computing and network simulation In addition the authors cover other important topics associated with multi core embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications

Digital Arithmetic Milos D. Ercegovac, Tomás Lang, 2004 The authoritative reference on the theory and design practice of computer arithmetic *Design and Test Technology for Dependable Systems-on-Chip* Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC **Intelligent Multidimensional Data Clustering and Analysis** Bhattacharyya, Siddhartha, De, Sourav, Pan, Indrajit, Dutta, Paramartha, 2016-11-29 Data mining analysis techniques have

undergone significant developments in recent years This has led to improved uses throughout numerous functions and applications Intelligent Multidimensional Data Clustering and Analysis is an authoritative reference source for the latest scholarly research on the advantages and challenges presented by the use of cluster analysis techniques Highlighting theoretical foundations computing paradigms and real world applications this book is ideally designed for researchers practitioners upper level students and professionals interested in the latest developments in cluster analysis for large data sets

Ultra Low-Power Electronics and Design E. Macii, 2007-05-08 Power consumption is a key limitation in many high speed and high data rate electronic systems today ranging from mobile telecom to portable and desktop computing systems especially when moving to nanometer technologies Ultra Low Power Electronics and Design offers to the reader the unique opportunity of accessing in an easy and integrated fashion a mix of tutorial material and advanced research results contributed by leading scientists from academia and industry covering the most hot and up to date issues in the field of the design of ultra low power devices systems and applications

Advances and Innovations in Systems, Computing Sciences and Software Engineering Khaled Elleithy, 2007-08-28 Advances and Innovations in Systems Computing Sciences and Software Engineering includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Computing Sciences Software Engineering and Systems Advances and Innovations in Systems Computing Sciences and Software Engineering includes selected papers from the conference proceedings of the International Conference on Systems Computing Sciences and Software Engineering SCSS 2006 which was part of the International Joint Conferences on Computer Information and Systems Sciences and Engineering CISSE 2006 All aspects of the conference were managed on line not only the reviewing submissions and registration processes but also the actual conference Conference participants authors presenters and attendees only needed an internet connection and sound available on their computers in order to be able to contribute and participate in this international ground breaking conference The on line structure of this high quality event allowed academic professionals and industry participants to contribute work and attend world class technical presentations based on rigorously refereed submissions live without the need for investing significant travel funds or time out of the office Suffice to say that CISSE received submissions from more than 70 countries for whose researchers this opportunity presented a much more affordable dynamic and well planned event to attend and submit their work to versus a classic on the ground conference The CISSE conference audio room provided superb audio even over low speed internet connections the ability to display PowerPoint presentations and cross platform compatibility the conferencing software runs on Windows Mac and any other operating system that supports Java In addition the conferencing system allowed for an unlimited number of participants which in turn granted CISSE the opportunity to allow all participants to attend all presentations as opposed to limiting the number of available seats for each session

Finite State Machine Logic Synthesis for Complex Programmable Logic Devices Robert Czerwinski, Dariusz

Kania,2013-01-12 This book is a monograph devoted to logic synthesis and optimization for CPLDs CPLDs macrocell can also be interpreted as programmable AND fixed OR structure well known as PAL based structure The question is what should be done when the number of implicants representing function exceeds the number of product terms available in a logic block The answer is in the book Logic synthesis and optimization methods dedicated for PAL based structures are proposed The methods strive to find the optimum fit for the combinational logic and finite state machines to the structure of the logic device and aim at area and speed optimization The theoretical background and complete strategies are richly illustrated with examples and figures

On-Chip Communication Architectures Sudeep Pasricha,Nikil Dutt,2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architectures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures Comprehensive survey of all research on communication architectures covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years

Parallel and Distributed Processing Techniques Hamid R. Arabnia,Masami Takata,Leonidas Deligiannidis,Pablo Rivas,Masahito Ohue,Nobuaki Yasuo,2025-03-25 This book constitutes the proceedings of the 30th International Conference on Parallel and Distributed Processing Techniques PDPTA 2024 held as part of the 2024 World Congress in Computer Science Computer Engineering and Applied Computing in Las Vegas USA during July 22 to July 25 2024 The 24 papers included in this book were carefully reviewed and selected from 143 submissions They have been organized in topical sections as follows Parallel and distributed processing techniques and applications and HPC and Workshop on Mathematical Modeling and Problem Solving

Zhang Functions and Various Models Yunong Zhang,Dongsheng Guo,2015-05-29 This book focuses on solving different types of time varying problems It presents various Zhang dynamics ZD models by defining various Zhang functions ZFs in real and complex domains It then provides theoretical analyses of such ZD models and illustrates their results It also uses simulations to substantiate their efficacy and show the

feasibility of the presented ZD approach i.e. different ZFs leading to different ZD models which is further applied to the repetitive motion planning RMP of redundant robots showing its application potential

Advances in Intelligent Systems Research and Innovation Vassil Sgurev, Vladimir Jotsov, Janusz Kacprzyk, 2021-11-03 This book represents the experience of successful researchers from four continents on a broad range of intelligent systems and it hints how to avoid anticipated conflicts and problems during multidisciplinary innovative research from Industry 4.0 and/or Internet of Things through modern machine learning and software agent applications to open data science big data advance analytics visual analytics text mining web mining knowledge discovery deep data mining issues The considered intelligent part is essential in most smart control systems cyber security bioinformatics virtual reality robotics mathematical modelling projects and its significance rapidly increases in other technologies Theoretical foundations of fuzzy sets mathematical and non classical logic also are rapidly developing

Residue Number Systems P.V. Ananda Mohan, 2016-10-14 This new and expanded monograph improves upon Mohan's earlier book Residue Number Systems Springer 2002 with a state of the art treatment of the subject Replete with detailed illustrations and helpful examples this book covers a host of cutting edge topics such as the core function the quotient function new Chinese Remainder theorems and large integer operations It also features many significant applications to practical communication systems and cryptography such as FIR filters and elliptic curve cryptography Starting with a comprehensive introduction to the basics and leading up to current research trends that are not yet widely distributed in other publications this book will be of interest to both researchers and students alike

Graphs in VLSI Rassul Bairamkulov, Eby G. Friedman, 2022-11-28 Networks are pervasive Very large scale integrated VLSI systems are no different consisting of dozens of interconnected subsystems hundreds of modules and many billions of transistors and wires Graph theory is crucial for managing and analyzing these systems In this book VLSI system design is discussed from the perspective of graph theory Starting from theoretical foundations the authors uncover the link connecting pure mathematics with practical product development This book not only provides a review of established graph theoretic practices but also discusses the latest advancements in graph theory driving modern VLSI technologies covering a wide range of design issues such as synchronization power network models and analysis and interconnect routing and synthesis Provides a practical introduction to graph theory in the context of VLSI systems engineering Reviews comprehensively graph theoretic methods and algorithms commonly used during VLSI product development process Includes a review of novel graph theoretic methods and algorithms for VLSI system design

Integrated Model of Distributed Systems Wiktor B. Daszczuk, 2019-03-16 In modern distributed systems such as the Internet of Things or cloud computing verifying their correctness is an essential aspect This requires modeling approaches that reflect the natural characteristics of such systems the locality of their components autonomy of their decisions and their asynchronous communication However most of the available verifiers are unrealistic because one or more of these features are not reflected Accordingly in this book we present

an original formalism the Integrated Distributed Systems Model IMDS which defines a system as two sets states and messages and a relation of the actions between these sets The server view and the traveling agent s view of the system provide communication duality while general temporal formulas for the IMDS allow automatic verification The features that the model checks include partial deadlock and partial termination communication deadlock and resource deadlock Automatic verification can support the rapid development of distributed systems Further on the basis of the IMDS the Dedan tool for automatic verification of distributed systems has been developed

Languages for System Specification Christoph Grimm, 2007-05-08 Contributions on UML address the application of UML in the specification of embedded HW SW systems C Based System Design embraces the modeling of operating systems modeling with different models of computation generation of test patterns and experiences from case studies with SystemC Analog and Mixed Signal Systems covers rules for solving general modeling problems in VHDL AMS modeling of multi nature systems synthesis and modeling of Mixed Signal Systems with SystemC Languages for formal methods are addressed by contributions on formal specification and refinement of hybrid embedded and real time stems Together with articles on new languages such as SystemVerilog and Software Engineering in Automotive Systems the contributions selected for this book embrace all aspects of languages and models for specification design modeling and verification of systems Therefore the book gives an excellent overview of the actual state of the art and the latest research results

Parallel and Distributed Computing Alberto Ros, 2010-01-01 The 14 chapters presented in this book cover a wide variety of representative works ranging from hardware design to application development Particularly the topics that are addressed are programmable and reconfigurable devices and systems dependability of GPUs General Purpose Units network topologies cache coherence protocols resource allocation scheduling algorithms peertopeer networks largescale network simulation and parallel routines and algorithms In this way the articles included in this book constitute an excellent reference for engineers and researchers who have particular interests in each of these topics in parallel and distributed computing

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