

ERROR-CONTROL CODING FOR COMPUTER SYSTEMS



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T. R. N. RAO
E. FUJIMURA

Error Control Coding For Computer Systems

P. PAL CHAUDHURI



Error Control Coding For Computer Systems:

Error-Control Coding for Computer Systems Prentice Hall PTR,1991-01-01 Error-control Coding for Computer Systems Thammavarapu R. N. Rao,Eiji Fujiwara,1989 **Essentials of Error-Control Coding Techniques** Hideki Imai,2014-06-28 Essentials of Error Control Coding Techniques presents error control coding techniques with an emphasis on the most recent applications It is written for engineers who use or build error control coding equipment Many examples of practical applications are provided enabling the reader to obtain valuable expertise for the development of a wide range of error control coding systems Necessary background knowledge of coding theory the theory of error correcting codes is also included so that the reader is able to assimilate the concepts and the techniques The book is divided into two parts The first provides the reader with the fundamental knowledge of the coding theory that is necessary to understand the material in the latter part Topics covered include the principles of error detection and correction block codes and convolutional codes The second part is devoted to the practical applications of error control coding in various fields It explains how to design cost effective error control coding systems Many examples of actual error control coding systems are described and evaluated This book is particularly suited for the engineer striving to master the practical applications of error control coding It is also suitable for use as a graduate text for an advanced course in coding theory *Error-Control Coding for Data Networks* Irving S. Reed,Xuemin Chen,2012-12-06 The purpose of Error Control Coding for Data Networks is to provide an accessible and comprehensive overview of the fundamental techniques and practical applications of the error control coding needed by students and engineers An additional purpose of the book is to acquaint the reader with the analytical techniques used to design an error control coding system for many new applications in data networks Error control coding is a field in which elegant theory was motivated by practical problems so that it often leads to important useful advances Claude Shannon in 1948 proved the existence of error control codes that under suitable conditions and at rates less than channel capacity would transmit error free information for all practical applications The first practical binary codes were introduced by Richard Hamming and Marcel Golay from which the drama and excitement have infused researchers and engineers in digital communication and error control coding for more than fifty years Nowadays error control codes are being used in almost all modem digital electronic systems and data networks Not only is coding equipment being implemented to increase the energy and bandwidth efficiency of communication systems but coding also provides innovative solutions to many related data networking problems On-Line Testing for VLSI Michael Nicolaidis,Yervant Zorian,Dhiraj Pradhan,2013-03-09 Test functions fault detection diagnosis error correction repair etc that are applied concurrently while the system continues its intended function are defined as on line testing In its expanded scope on line testing includes the design of concurrent error checking subsystems that can be themselves self checking fail safe systems that continue to function correctly even after an error occurs reliability monitoring and self test and fault tolerant designs On Line Testing for VLSI contains a selected set of

articles that discuss many of the modern aspects of on line testing as faced today The contributions are largely derived from recent IEEE International On Line Testing Workshops Guest editors Michael Nicolaidis Yervant Zorian and Dhiraj Pradhan organized the articles into six chapters In the first chapter the editors introduce a large number of approaches with an expanded bibliography in which some references date back to the sixties On Line Testing for VLSI is an edited volume of original research comprising invited contributions by leading researchers

Coding Approaches to Fault Tolerance in Combinational and Dynamic Systems Christoforos N. Hadjicostis, 2012-12-06 Coding Approaches to Fault Tolerance in Combinational and Dynamic Systems describes coding approaches for designing fault tolerant systems i e systems that exhibit structured redundancy that enables them to distinguish between correct and incorrect results or between valid and invalid states Since redundancy is expensive and counter intuitive to the traditional notion of system design the book focuses on resource efficient methodologies that avoid excessive use of redundancy by exploiting the algorithmic dynamic structure of a particular combinational or dynamic system The first part of Coding Approaches to Fault Tolerance in Combinational and Dynamic Systems focuses on fault tolerant combinational systems providing a review of von Neumann s classical work on Probabilistic Logics including some more recent work on noisy gates and describing the use of arithmetic coding and algorithm based fault tolerant schemes in algebraic settings The second part of the book focuses on fault tolerance in dynamic systems Coding Approaches to Fault Tolerance in Combinational and Dynamic Systems also discusses how in a dynamic system setting one can relax the traditional assumption that the error correcting mechanism is fault free by using distributed error correcting mechanisms The final chapter presents a methodology for fault diagnosis in discrete event systems that are described by Petri net models coding techniques are used to quickly detect and identify failures From the Foreword Hadjicostis has significantly expanded the setting to processes occurring in more general algebraic and dynamic systems The book responds to the growing need to handle faults in complex digital chips and complex networked systems and to consider the effects of faults at the design stage rather than afterwards George Verghese Massachusetts Institute of Technology Coding Approaches to Fault Tolerance in Combinational and Dynamic Systems will be of interest to both researchers and practitioners in the area of fault tolerance systems design and control

Error Control Coding Shu Lin, Daniel J. Costello, 1983 Completely updated to cover latest developments this text provides a bridge between introductory courses in digital communications and more advanced courses in information technology It presents state of the art control techniques

Reliability of Computer Systems and Networks Martin L. Shooman, 2003-03-25 With computers becoming embedded as controllers in everything from network servers to the routing of subway schedules to NASA missions there is a critical need to ensure that systems continue to function even when a component fails In this book bestselling author Martin Shooman draws on his expertise in reliability engineering and software engineering to provide a complete and authoritative look at fault tolerant computing He clearly explains all fundamentals including how to use redundant elements in system

design to ensure the reliability of computer systems and networks Market Systems and Networking Engineers Computer Programmers IT Professionals *Dependable Computing - EDCC-1* Klaus Ehtle,Dieter Hammer,David Powell,1994-09-21 This book presents the proceedings of the First European Dependable Computing Conference EDCC 1 held in Berlin Germany in October 1994 EDCC is the merger of two former European events on dependable computing The volume comprises 34 refereed full papers selected from 106 submissions The contributions address all current aspects of dependable computing and reflect the state of the art in dependable systems research and advanced applications among the topics covered are hardware and software reliability safety critical and secure systems fault tolerance and detection verification and validation formal methods hardware and software testing and parallel and distributed systems *Embedded Computer Systems: Architectures, Modeling, and Simulation* Mladen Berekovic,Nikitas Dimopoulos,Stephan Wong,2008-07-07 This book constitutes the refereed proceedings of the 8th International Workshop on Systems Architectures Modeling and Simulation SAMOS 2008 held in Samos Greece in July 2008 The 24 revised full papers presented together with a contemplative keynote and additional papers of two special workshop sessions were carefully reviewed and selected from 62 submissions The papers are organized in topical sections on architecture new frontiers SoC application specific contributions system level design for heterogeneous systems programming multicores sensors and sensor networks and systems modeling and design **COMPUTER ORGANIZATION AND DESIGN** P. PAL CHAUDHURI,2008-04-15 The merging of computer and communication technologies with consumer electronics has opened up new vistas for a wide variety of designs of computing systems for diverse application areas This revised and updated third edition on Computer Organization and Design strives to make the students keep pace with the changes both in technology and pedagogy in the fast growing discipline of computer science and engineering The basic principles of how the intended behaviour of complex functions can be realized with the interconnected network of digital blocks are explained in an easy to understand style WHAT IS NEW TO THIS EDITION Includes a new chapter on Computer Networking Internet and Wireless Networks Introduces topics such as wireless input output devices RAID technology built around disk arrays USB SCSI etc Key Features Provides a large number of design problems and their solutions in each chapter Presents state of the art memory technology which includes EEPROM and Flash Memory apart from Main Storage Cache Virtual Memory Associative Memory Magnetic Bubble and Charged Couple Device Shows how the basic data types and data structures are supported in hardware Besides students practising engineers should find reading this design oriented text both useful and rewarding *Soft Computing Applications* Valentina Emilia Balas,Lakhmi C. Jain,Branko Kovačević,2015-11-02 These volumes constitute the Proceedings of the 6th International Workshop on Soft Computing Applications or SOFA 2014 held on 24 26 July 2014 in Timisoara Romania This edition was organized by the University of Belgrade Serbia in conjunction with Romanian Society of Control Engineering and Technical Informatics SRAIT Arad Section The General Association of Engineers in Romania Arad Section Institute of Computer

Science Iasi Branch of the Romanian Academy and IEEE Romanian Section The Soft Computing concept was introduced by Lotfi Zadeh in 1991 and serves to highlight the emergence of computing methodologies in which the accent is on exploiting the tolerance for imprecision and uncertainty to achieve tractability robustness and low solution cost Soft computing facilitates the use of fuzzy logic neurocomputing evolutionary computing and probabilistic computing in combination leading to the concept of hybrid intelligent systems The combination of such intelligent systems tools and a large number of applications introduce a need for a synergy of scientific and technological disciplines in order to show the great potential of Soft Computing in all domains The conference papers included in these proceedings published post conference were grouped into the following area of research Image Text and Signal Processing li Intelligent Transportation Modeling and Applications Biomedical Applications Neural Network and Applications Knowledge Based Technologies for Web Applications Cloud Computing Security Algorithms and Computer Networks Knowledge Based Technologies Soft Computing Techniques for Time Series Analysis Soft Computing and Fuzzy Logic in Biometrics Fuzzy Applications Theory and Fuzzy Control Bussiness Process Management Methods and Applications in Electrical Engineering The volumes provide useful information to professors researchers and graduated students in area of soft computing techniques and applications as they report new research work on challenging issues **Handbook of Performability Engineering** Krishna B. Misra,2008-08-24

Dependability and cost effectiveness are primarily seen as instruments for conducting international trade in the free market environment These factors cannot be considered in isolation of each other This handbook considers all aspects of performability engineering The book provides a holistic view of the entire life cycle of activities of the product along with the associated cost of environmental preservation at each stage while maximizing the performance **Digital Signal**

Processing for Multimedia Systems Keshab K. Parhi,Takao Nishitami,1999-03-11 Addresses a wide selection of multimedia applications programmable and custom architectures for the implementations of multimedia systems and arithmetic architectures and design methodologies The book covers recent applications of digital signal processing algorithms in multimedia presents high speed and low priority binary and finite field arithmetic architectures details VHDL based implementation approaches and more Microprocessor-Based Parallel Architecture for Reliable Digital Signal Processing Systems Alan D. George,2018-01-18 This book presents a distributed multiprocessor architecture that is faster more versatile and more reliable than traditional single processor architectures It also describes a simulation technique that provides a highly accurate means for building a prototype system in software The system prototype is studied and analyzed using such DSP applications as digital filtering and fast Fourier transforms The code is included as well which allows others to build software prototypes for their own research systems The design presented in Microprocessor Based Parallel Architecture for Reliable Digital Signal Processing Systems introduces the concept of a dual mode architecture that allows users a dynamic choice between either a conventional or fault tolerant system as application requirements dictate This

volume is a must have for all professionals in digital signal processing parallel and distributed computer architecture and fault tolerant computing Journal of New Generation Computer Systems ,1990 *Nonvolatile Memory Technologies with Emphasis on Flash* Joe Brewer,Manzur Gill,2011-09-23 Presented here is an all inclusive treatment of Flash technology including Flash memory chips Flash embedded in logic binary cell Flash and multilevel cell Flash The book begins with a tutorial of elementary concepts to orient readers who are less familiar with the subject Next it covers all aspects and variations of Flash technology at a mature engineering level basic device structures principles of operation related process technologies circuit design overall design tradeoffs device testing reliability and applications **Theory and Applications of Models of Computation** Manindra Agrawal,Barry S. Cooper,Angsheng Li,2012-05-04 This book constitutes the refereed proceedings of the 9th International Conference on Theory and Applications of Models of Computation TAMC 2012 held in Beijing China in May 2012 The conference was combined with the Turing Lectures 2012 dedicated to celebrating Alan Turing s unique impact on mathematics computing computer science informatics morphogenesis philosophy and the wider scientific world Eight Turing Lectures were given at the TAMC 2012 The 40 revised full papers presented together with invited talks were carefully reviewed and selected from 86 submissions The papers address 4 special sessions at TAMC 2012 which were algorithms and information in networks complexity and cryptography models of computing and networking programming and verification **Fault-Tolerant Computing Systems** Mario Dal Cin,Wolfgang Hohl,2012-12-06 5th International GI ITG GMA Conference N rnberg September 25 27 1991 Proceedings **Comprehensive Dictionary of Electrical Engineering** Phillip A. Laplante,1999-01-01 Complete coverage of all fields of electrical engineering The book provides workable definitions for practicing engineers while serving as a reference and research tool for students and offering practical information for scientists and engineers in other disciplines Areas examined include applied electrical microwave control power and digital systems engineering plus device electronics

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