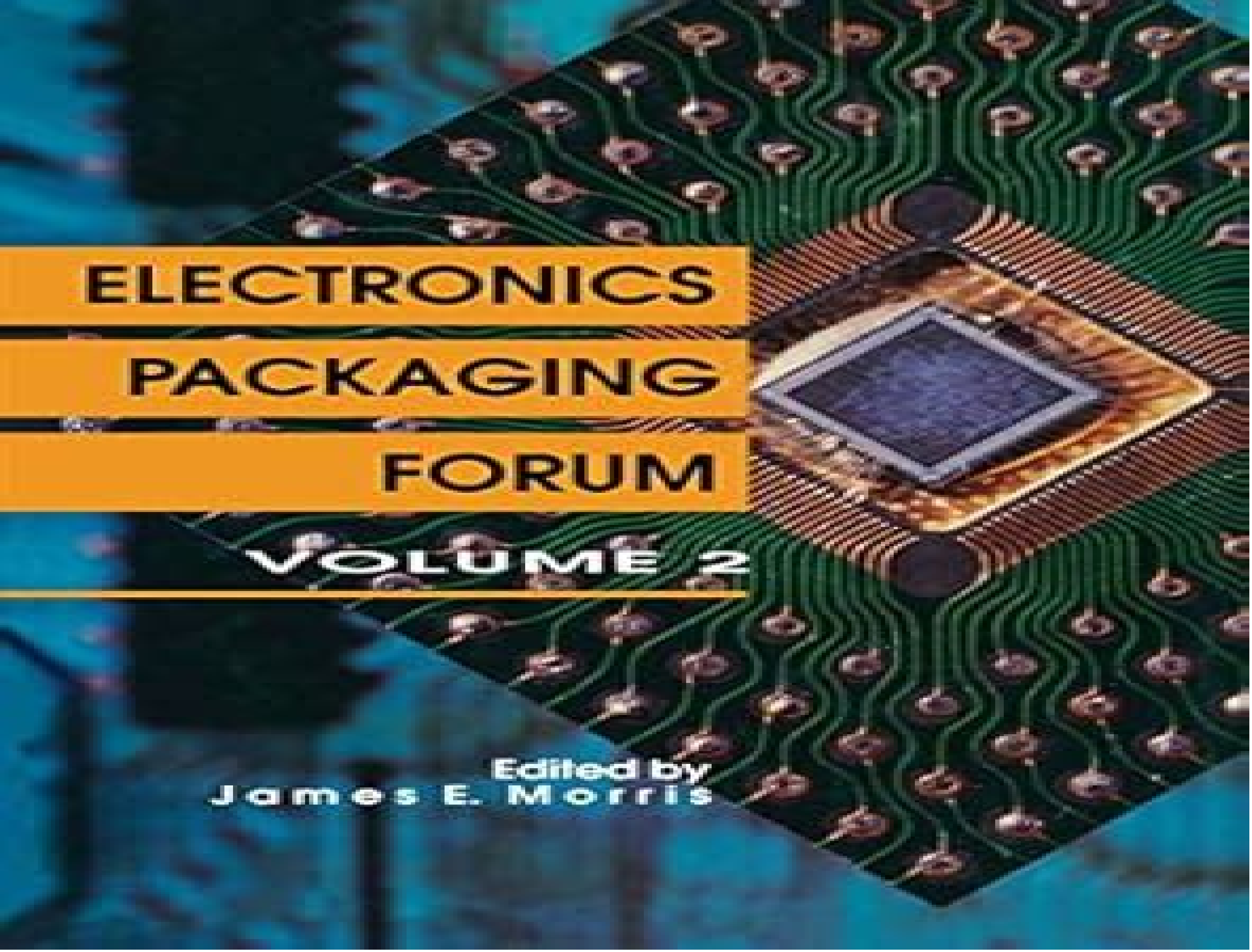

ELECTRONICS PACKAGING FORUM

VOLUME 2

Edited by
James E. Morris

Electronics Packaging Forum Volume 2

John H. Lau

Electronics Packaging Forum Volume 2:

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Morris,2012-12-06 Each May the Continuing Education Division of the T J Watson School of Engineering Applied Science and Technology at the State University of New York at Binghamton sponsors an Annual Symposium in Electronics Packaging in cooperation with local professional societies IEEE ASME SME IEPS and UnlPEG the University Industry Partnership for Economic Growth Each volume of this Electronics Packaging Forum series is based on the the preceding Symposium with Volume Two based on the 1990 presentations The Preface to Volume One included a brief definition of the broad scope of the electronics packaging field with some comments on why it has recently assumed such a more prominent priority for research and development Those remarks will not be repeated here at this point it is assumed that the reader is a professional in the packaging field or possibly a student of one of the many academic disciplines which contribute to it It is worthwhile repeating the series objectives however so the reader will be clear as to what might be expected by way of content and level of each chapter **Electronics Packaging Forum** James E Morris,1991-01-17 **Electronics Packaging Forum** James E. Morris,1991-01-17 Each May the Continuing Education Division of the T J Watson School of Engineering Applied Science and Technology at the State University of New York at Binghamton sponsors an Annual Symposium in Electronics Packaging in cooperation with local professional societies IEEE ASME SME IEPS and UnlPEG the University Industry Partnership for Economic Growth Each volume of this Electronics Packaging Forum series is based on the the preceding Symposium with Volume Two based on the 1990 presentations The Preface to Volume One included a brief definition of the broad scope of the electronics packaging field with some comments on why it has recently assumed such a more prominent priority for research and development Those remarks will not be repeated here at this point it is assumed that the reader is a professional in the packaging field or possibly a student of one of the many academic disciplines which contribute to it It is worthwhile repeating the series objectives however so the reader will be clear as to what might be expected by way of content and level of each chapter **42nd Porcelain Enamel Institute Technical Forum** William J. Smothers,2009-09-28 This volume is part of the Ceramic Engineering and Science Proceeding CESP series This series contains a collection of papers dealing with issues in both traditional ceramics i e glass whitewares refractories and porcelain enamel and advanced ceramics Topics covered in the area of advanced ceramic include bioceramics nanomaterials composites solid oxide fuel cells mechanical properties and structural design advanced ceramic coatings ceramic armor porous ceramics and more *Chip On Board* John H. Lau,1994-06-30 This book is a one stop guide to the state of the art of COB technology For professionals active in COB and MCM research and development those who wish to master COB and MCM problem solving methods and those who must choose a cost effective design and high yield manufacturing process for their interconnect systems here is a timely summary of progress in al aspects of this fascinating field It meets the reference needs of design material process equipment manufacturing quality reliability packaging and system engineers and technical managers working in electronic packaging and interconnection Multichip Module Technologies and Alternatives: The Basics Daryl Ann Doane,Paul Franzon,1992-10-31 Far from being the passive containers for semiconductor devices of the past the packages in today s high performance computers pose numerous challenges in interconnecting powering cooling and protecting devices While semiconductor circuit performance measured in picoseconds continues to improve computer performance is expected to be in nanoseconds for the rest of this century a factor of 1000 difference between on chip and off chip performance which is attributable to losses associated with the package Thus the package which interconnects all the chips to form a particular function such as a central processor is likely to set the limits on how far computers can evolve Multichip packaging which can relax these limits and also improve the reliability and cost at the systems level is expected to be the basis of all advanced computers in the future In addition since this technology allows chips to be spaced more closely in less space and with less weight it has the added advantage of being useful in portable consumer electronics as well as in medical aerospace automotive and telecommunications products The multichip technologies with which these applications can be addressed are many They range from ceramics to polymer metal thin films to printed wiring boards for interconnections flip chip TAB or wire bond for chip to substrate connections and air or water cooling for the removal of heat **Electronic Packaging and Production** ,1996 Journal of Electronic Packaging ,1993 Materials for Electronic Packaging Deborah D.L. Chung,1995-03-31 Although materials play a critical role in electronic packaging the vast majority of attention has been given to the systems aspect Materials for Electronic Packaging targets materials engineers and scientists by focusing on the materials perspective The last few decades have seen tremendous progress in semiconductor technology creating a need for effective electronic packaging Materials for Electronic Packaging examines the interconnections encapsulations substrates heat sinks and other components involved in the packaging of integrated circuit chips These packaging schemes are crucial to the overall reliability and performance of electronic systems Consists of 16 self contained chapters contributed by a variety of active researchers from industrial academic and governmental sectors Addresses the need of materials scientists engineers electrical engineers mechanical engineers physicists and chemists to acquire a thorough knowledge of materials science Explains how the materials for electronic packaging determine the overall effectiveness of electronic systems *Microengineering Technology for Space Systems* Henry Helvajian,1997 A follow on to Micro and Nanotechnology for Space Systems this second monograph in the series uses the more universal term microengineering to define the discipline and processes that lead to the development of an integrated and intelligent microinstrument Microengineering Technology for Space Systems addresses specific issues concerning areas for ASIM application in current space systems operation in the space environment ultra high density packaging and nonsilicon materials processing tools and the feasibility of the nanosatellite concept **Small Business Bibliography** ,1958 *Silicon Carbide, Volume 2* Peter Friedrichs,Tsunenobu Kimoto,Lothar Ley,Gerhard Pensl,2011-04-08

Silicon Carbide this easy to manufacture compound of silicon and carbon is said to be THE emerging material for applications in electronics High thermal conductivity high electric field breakdown strength and high maximum current density make it most promising for high powered semiconductor devices Apart from applications in power electronics sensors and NEMS SiC has recently gained new interest as a substrate material for the manufacture of controlled graphene SiC and graphene research is oriented towards end markets and has high impact on areas of rapidly growing interest like electric vehicles This volume is devoted to high power devices products and their challenges in industrial application Readers will benefit from reports on development and reliability aspects of Schottky barrier diodes advantages of SiC power MOSFETs or SiC sensors The authors discuss MEMS and NEMS as SiC based electronics for automotive industry as well as SiC based circuit elements for high temperature applications and the application of transistors in PV inverters The list of contributors reads like a Who s Who of the SiC community strongly benefiting from collaborations between research institutions and enterprises active in SiC crystal growth and device development Among the former are CREE Inc and Fraunhofer ISE while the industry is represented by Toshiba Nissan Infineon NASA Naval Research Lab and Rensselaer Polytechnic Institute to name but a few **Low Cost Flip Chip Technologies** John H. Lau,2000 Of the Standard NuBGA Packages Thinner Substrate and Nonuniform Heat Spreader NuBGA Thermal Performance of the New NuBGA Package Temperature Distribution Thermal Resistance Cooling Power Wind Tunnel Experimental Analysis Solder Joint Reliability of the New NuBGA Package Electrical Performance of the New NuBGA Package Capacitance Inductance Summary of the New NuBGA Package Solder Bumped Flip Chip in PBGA Packages Intel s OLGA Package Technology OLGA Package Design OLGA Wafer Bumping OLGA Substrate Technology OLGA Package Assembly OLGA Package Reliability Mitsubishi s FC BGA Package Wafer Bumping Mitsubishi s SBU Substrate PC BGA Assembly Process Thermal Management Electrical Performance Qualification Tests and Results IBM s FC PBGA Package CFD Analysis for Thermal Boundary Conditions Nonlinear Finite Element Stress Analysis Simulation Results Solder Joint Thermal Fatigue Life Prediction Motorola s FC PBGA Packages Thermal Management of FC PBGA Assemblies with E3 Bumps Solder Joint Reliability of FC PBGA Assemblies with C4 Bumps Failure Analysis of Flip Chip on Low Cost Substrates Failure Analysis of FCOB with Imperfect Underfills Test Chip Test Board Flip Chip Assembly Preconditions Reflows and Qualification Tests Failure Modes and Discussions Die Cracking Interfacial Shear Strength Interfacial Shear Strength Between Solder Mask and Underfill **Electronics Manufacturing** John H. Lau,C. P. Wong,Ning-Cheng Lee,Ricky S. W. Lee,2002-09-13 ELECTRONICS MANUFACTUIRNG WITH LEAD FREE HALOGEN FREE AND CONDUCTIVE ADHESTIVE MATERIALS This comprehensive guide provides cutting edge information on lead free halogen free and conductive adhesive technologies and their application to low cost high density reliable and green products Essential for electronics manufacturing and packaging professionals who wish to master lead free halogen free and conductive adhesive problem solving methods and those demanding cost effective designs and high yield environmental benign manufacturing processes this valuable reference covers all aspects of this fast growing field Written for design materials process equipment manufacturing reliability component packaging and system engineers and technical and marketing managers in electronics and photonics packaging and interconnection this book teaches a practical understanding of the cost design materials process equipment manufacturing and reliability issues of lead free halogen free and conductive adhesive technologies Among the topics explored Chip wafer level interconnects with lead free solder bumps Lead free solder wafer bumping with micro ball mounting and paste printing methods Lead free solder joint reliability of WLCSPs on organic and ceramic substrates Chip wafer level interconnects with solderless bumps such as Ni Au Au and Cu Cu wires Au wires Au studs and Cu studs Design materials process and reliability of WLCSPs with solderless interconnects on PCB substrate Halogen free molding compounds for PQFP PBGA and MAP PBGA packages Environmentally benign die attach films for PQFP and PBGA packages and lead free die attach bonding techniques for IC packaging Environmental issues for conventional PCBs and substrates Some environmentally conscious flame retardants for PCBs and organic substrates Emerging technologies for fabricating environmental friendly PCBs such as design for environment green PCB manufacturing and environmental safety Lead free soldering activities such as legislation consortia programs and regional preferences on lead free solder alternatives Criteria development approaches and varieties of alloys and properties of lead free solders Physical mechanical chemical electrical and soldering properties of lead free solders Manufacturing process and performance of lead free surface finishes for both PCB and component applications Implementation and execution challenges of lead free soldering especially for the reflow and wave soldering process Fundamental understanding of electrically conductive adhesive ECA technology Effects of lubricant removal and cure shrinkage on ECAs Mechanisms underlying the contact resistance shifts of ECAs Effects of electrolytes and moisture absorption on contact resistance shifts of ECAs Stabilization of contact resistance of ECAs using various additives Electronic Packaging Interconnect Technology Kazuhiro Nogita,Mohd Arif Anuar Mohd Salleh,Mohd Mustafa Al Bakri Abdullah,Liyana Jamaludin,Muhammad Faheem Mohd Tahir,2018-04-13 Electronic Packaging Interconnect Technology Symposium EPITS 2017 Selected peer reviewed papers from the Electronic Packaging Interconnect Technology Symposium EPITS 2017 November 1 2 2017 Fukuoka Japan **Flip Chip Technologies** John H. Lau,1996 A guide to flip chip technologies for professionals in flip chip and MCM research and development and for engineers and technical managers choosing design and manufacturing processes for electronic packaging and interconnect systems Discusses economic design material quality and reliability issues of flip chip technologies and details aspects of classical solder bumped flip chip interconnect technologies the next generations of flip chip technologies and known good die testing for multiple module applications Annotation copyright by Book News Inc Portland OR **Through-Silicon Vias for 3D Integration** John Lau,2012-09-20 A comprehensive guide to TSV and other enabling technologies for 3D integration Written by an expert with more than 30 years of experience in the electronics industry Through Silicon Vias for 3D Integration provides cutting edge information on TSV wafer thinning thin wafer handling microbumping and assembly and thermal management technologies Applications to highperformance high density low power consumption wide bandwidth and small form factor electronic products are discussed This book offers a timely summary of progress in all aspects of this fascinating field for professionals active in 3D integration research and development those who wish to master 3D integration problem solving methods and anyone in need of a low power wide bandwidth design and high yield manufacturing process for interconnect systems Coverage includes Nanotechnology and 3D integration for the semiconductor industry TSV etching dielectric barrier and seed layer deposition Cu plating CMP and Cu revealing TSVs mechanical thermal and electrical behaviors Thin wafer strength measurement Wafer thinning and thin wafer handling Microbumping assembly and reliability Microbump electromigration Transient liquid phase bonding C2C C2W and W2W 2 5D IC integration with interposers 3D IC integration with interposers Thermal management of 3D IC integration 3D IC packaging Ball Grid Array Technology John H. Lau,1995 A summary of progress in ball grid array BGA packaging technology for professionals in BGA research and development and for manufacturers researching BGA for their interconnect systems Discusses economic design material process and quality issues and describes techniques for processing substrates routing PCB assembling CBGA PBGA and TBGA packages and inspection of BGA PCB assemblies Includes treatment of BGA industry infrastructure and an electronic packaging glossary Contains bandw photos and diagrams Annotation copyright by Book News Inc Portland OR **Electronic Packaging and Interconnection Handbook** Charles A. Harper,1997 Charles A Harper s 2nd edition on designing and manufacturing all the major types of electronic systems is now double the size of the 1st edition It draws upon the expertise of a dozen experts to make sense of this highly interdisciplinary field International Scientific Siberian Transport Forum TransSiberia - 2021 Aleksey Manakov,Arkadii Edigarian,2022-03-17 This book presents innovations in the field of high speed rail technology hyperloop transportation technologies and Maglev system information and communication technology ICT for intelligent transportation systems ITS multimodal transportation sustainable freight transportation and others The papers presented in the book are proceedings of the annual scientific forum TransSiberia which is the foremost Russian transport event that focuses on innovations in rail transport The book also presents research in the field of railway engineering health monitoring inspection NDT E and signal processing Developments in the field of decarbonization of railway transport and new types of fuel as an alternative to electrification are proposed The issues of sustainable operation and maintenance of railway systems and sustainable freight transportation such as digitalization and AI technologies for sustainable asset management operation and maintenance of railway systems have received a lot of research attention The book serves as a medium for railroad academia and industry to exchange new ideas and share the latest achievements as well as to continue supporting the productivity of the transport industry in a sustainable manner

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