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# Electronic Packaging Materials Science VII

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# **Electronic Packaging Materials Science Vii**

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Manufacturing Challenges in Electronic Packaging Y.C. Lee, W.T. Chen, 2012-12-06 About five to six years ago the words packaging and manufacturing started to be used together to emphasize that we have to make not only a few but thousands or even millions of packages which meet functional requirements The aim of this book is to provide the much needed reviews and in depth discussions on the advanced topics surrounding packaging and manufacturing The first chapter gives a comprehensive review of manufacturing challenges in electronic packaging based on trends predicted by different resources Almost all the functional specifications have already been met by technologies demonstrated in laboratories However it would take tremendous efforts to implement these technologies for mass production or flexible manufacturing The topics crucial to this implementation are discussed in the following chapters Chapter 2 Challenges in solder assembly technologies Chapter 3 Testing and characterization Chapter 4 Design for manufacture and assembly of electronic packages Chapter 5 Process modeling optimization and control in electronics manufacturing and Chapter 6 Integrated manufacturing system for printed circuit board assembly The electronics based products are very competitive and becoming more and more application specific Their packages should fulfill cost speed power weight size reliability and time to market requirements More importantly the packages should be manufacturable in mass or flexible production lines. These chapters are excellent references for professionals who need to meet the challenge through design and manufacturing improvements This book will also introduce students to the critical issues for competitive design and manufacturing in electronic packaging Environment Electronics Ahmed Sharif, 2019-03-19 Provides in depth knowledge on novel materials that make electronics work under high temperature and high pressure conditions This book reviews the state of the art in research and development of lead free interconnect materials for electronic packaging technology It identifies the technical barriers to the development and manufacture of high temperature interconnect materials to investigate into the complexities introduced by harsh conditions It teaches the techniques adopted and the possible alternatives of interconnect materials to cope with the impacts of extreme temperatures for implementing at industrial scale The book also examines the application of nanomaterials current trends within the topic area and the potential environmental impacts of material usage Written by world renowned experts from academia and industry Harsh Environment Electronics Interconnect Materials and Performance Assessment covers interconnect materials based on silver gold and zinc alloys as well as advanced approaches utilizing polymers and nanomaterials in the first section The second part is devoted to the performance assessment of the different interconnect materials and their respective environmental impact Takes a scientific approach to analyzing and addressing the issues related to interconnect materials involved in high temperature electronics Reviews all relevant materials used in interconnect technology as well as alternative approaches otherwise neglected in other literature Highlights emergent research and theoretical concepts in the implementation of different materials in soldering and die attach applications Covers wide bandgap semiconductor device technologies for high temperature and harsh environment

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